**Date:** 2/3/2017

**To:** CSS 422, Winter 2017

**From:** Team Dankware (Arsany Azmy, Connor McGwire, Spencer Milner)

**Subject:** Progress Report 02

# Work Completed: (\* = done)

|  |  |
| --- | --- |
| Data Register Direct | 000 (register number) |
| Address Register Direct | 001 (register number) |
| Address Register Indirect | 010 (register number) |
| Immediate Data | 111 100 |
| Address Register Indirect Post Increment | 011 (register number) |
| Address Register Indirect Pre Decrement | 100 (register number) |
| Absolute Long Address | 111 001 |
| Absolute Word Address | 111 000 |
| NOP \* | 4E71 |
| MOVE \* | 0001+<ea>,<ea>  0011+<ea>,<ea>  0010+<ea>,<ea> |
| MOVEA \* | 0011+<Dn>+001+<ea>  0010+<Dn>+001+<ea> |
| MOVEQ \* | 0111+<reg>+0+<data> |
| MOVEM \* | 01001+0+0010+<ea>  01001+0+0011+<ea>  01001+1+0010+<ea>  01001+1+0011+<ea> |
| ADD \* | 1101+<Dn>+<000/001/010>+<ea>  1101+<Dn>+<100/101/110>+<ea> |
| ADDI \* | 00000110+00+<ea>  00000110+01+<ea>  00000110+10+<ea> |
| ADDA \* | 1101+<An>+<011/111>+<ea> |
| ADDQ \* | 0101+Data(000-111)+0+00+<ea>  0101+Data(000-111)+0+01+<ea>  0101+Data(000-111)+0+10+<ea> |
| SUB \* | 1001+<Dn>+<000/001/010>+<ea>  1001+<Dn>+<100/101/110>+<ea> |
| MULS | 1100+<Dn>+111+<ea>  0100110000+<ea>  0+<Dl>+1+<0/1>+0000000+<Dh> |
| DIVU \* | 1000+<Dn>+011+<ea> |

|  |  |
| --- | --- |
| LEA | 0100+<An>+111+<ea> |
| CLR | 01000010+<00/01/10>+<ea> |
| AND | 1100+<Dn>+<OpMode>+<ea> |
| OR \* | 1000+<Dn>+<000/001/010>+<ea>  1000+<Dn>+<100/101/110>+<ea> |
| LSL | 1110001111+<ea> |
| LSR | 1110001011+<ea> |
| ASR | 1110000111+<ea> |
| ASL | 1110000011+<ea> |
| ROL | 1110011111+<ea> |
| ROR | 1110011011+<ea> |
| CMP | 1011+<Dn>+<OpMode>+<ea> |
| BCC | 0110+<con>+<8 bit Displacement/$00/$FF>  <16 bit displacement> if $00  <32 bit displacement> if $FF |
| JSR | 0100111010+<ea> |

|  |  |
| --- | --- |
| RTS | 0100111001110101 – 4E75 |

Cc chart:

|  |  |
| --- | --- |
| T | 0000 |
| F | 0001 |
| HI | 0010 |
| LS | 0011 |
| CC(HI) | 0100 |
| CS(LO) | 0101 |
| NE | 0110 |
| EQ | 0111 |
| VC | 1000 |
| VS | 1001 |
| PL | 1010 |
| MI | 1011 |
| GE | 1100 |
| LT | 1101 |
| GT | 1110 |
| LE | 1111 |

# Problems:

None

# Work Scheduled:

-Meet again this week

-Prototype systems