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Homework #1

Q1. Convert the hexadecimal number 973D0 to a number with base 15

 $\text{Hex} \rightarrow \text{Dec} \rightarrow \text{base-15}$

• $9 \times 16^4 + 7 \times 16^3 + 3 \times 16^2 + 14 \times 16^1 + 0 \times 16^0$ $\rightarrow (619488)_{10}$

$$15 \rightarrow 619472 \rightarrow 2$$

 $15 \rightarrow 41298 \rightarrow 3$
 $15 \rightarrow 2753 \rightarrow 8$
 $15 \rightarrow 183 \rightarrow 3$
 $15 \rightarrow 12 \rightarrow 12$
 $15 \rightarrow 0$

Ans. C3832 \rightarrow base 15

Q2. Floating point numbers.

I. Convert the following floating-point numbers to hexadecimal number in IEEE single-precision format. Please give the result as eight hexadecimal digits.

A.
$$-69/32 = -2.15625 \implies -10.00101$$

Step 1: Change to binary

Integer part: 10
 $2 \Rightarrow 2 \Rightarrow 0$
 $2 \Rightarrow 1 \Rightarrow 1$
 $2 \Rightarrow 0$

Fraction part: 00101
 $0.15625 \times 2 \Rightarrow 0$
 $0.3125 \times 2 \Rightarrow 0$
 $0.625 \times 2 \Rightarrow 1$

Step 2: Normalize -1.000101 x 2¹

Sign bit: 1

Exponent: 127 + 1 = 128 = 10000000 Mantissa: 010 1000 0000 0000 0000 0000

 $0.25 \times 2 \rightarrow 0$ $0.5 \times 2 \rightarrow 1$

Therefore, real number in floating point representation is:

1 100 0000 0 000 1010 0000 0000 0000 0000

Sign(1) Exponent(8) Mantissa (23)

1 100 0000 0 000 1010 0000 0000 0000 0000

The Hex representation is by grouping, binary digits into 4 bits

Ans. C00A0000

B. 13.625

- Change to binary

 $2 \rightarrow 0$

- For fraction
 - $0.625 \times 2 = 1$ $0.25 \times 2 = 0$ $0.5 \times 2 = 1$

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1101.101

1.101101 x 2^3 => \text{mantissa} => 101101

127+3=130 => binary representation => 10000010

2>130>0

2>65>1

2>32>0

2>16>0

2>8>0

2>4>0

2>2>0

2>1>1

2>0

Sign(1) Exponent(8)
```

 Sign(1)
 Exponent(8)
 Mantissa (23)

 0
 100 0001 0
 101 1010 0000 0000 0000 0000

floating point representation:

Ans. 0x 415A0000

II. Convert the following hexadecimal numbers in IEEE single-precision format to floating-point numbers:

A. 42E48000

- \circ Hex \rightarrow Binary \rightarrow change from hex to binary by directly representing numbers in their binary representation

Floating point representation

Sign	(1) Exponent (8)	Mantissa (23)
0	100 0010 1	110 0100 1000 0000 0000 0000

Ans. 114.25

B. C6F00040

- O Hex → Binary → change from hex to binary by directly representing numbers in their binary representation
 - **•** 1100 0110 1111 0000 0000 0000 0100 0000

Floating point representation

Sign	(1) Exponent(8)	Mantissa (23)
1	10001101	1110000 00000000 01000000

Exponent \rightarrow 10001101= Decimal representation = 141 – 127 = 14 \rightarrow 2¹⁴

Number after shifting 14 digits → 1. 1110000 0000000 01000000

Sign → negative (- ve)

Binary representation of the number → - 11110000000000.001000000

Decimal representation \rightarrow 1 x 2¹⁴+1 x 2¹³+1 x 2¹²+ 1 x 2¹¹ = 30720

Fraction representation $\rightarrow 1 \times 2^{-3} = 0.125$

Floating point representation: -30720.125

Q3. Error Finding

1) MOVE.B \$A000, A3

From manual MOVE.B is size limited to 8-bit

• The MOVE.B is size limited to 8-bit, which the hexadecimal value \$A000(16-bit) is more than 8-bit. Also, MOVEA or MOVE.L opcode should've been chosen for this because it involves moving or copying data to an address register. This flags out an error because the immediate data excides size limit.

2) ADD.B #\$1000, D2

From manual ADD.B is size limited to 8-bit

Syntax: ADD < ea > ,Dn ADD Dn, < ea >

• From manual the data size limit for ADD.B is an 8-bit, adding the hex value #\$1000 (13 bit) to data register D2 cause to flag an error because the size limit excides 8-bit, as it excides 8-bit size. ADD adds the source operand to the destination operand using binary addition and stores the result in the destination location, in this instruction destination is D2.

3) MOVEA.W \$1234, D0

From the manual the syntax should be MOVEA.W <ea>, An

• Clearly There is an error in the syntax for coping an hexadecimal address value from source to destination (\$1234 to D0). MOVEA moves the contents of the source to the destination address register. This is error and can be fixed by changing D0 to address mode (An).

4) ANDI.B #23, #\$100

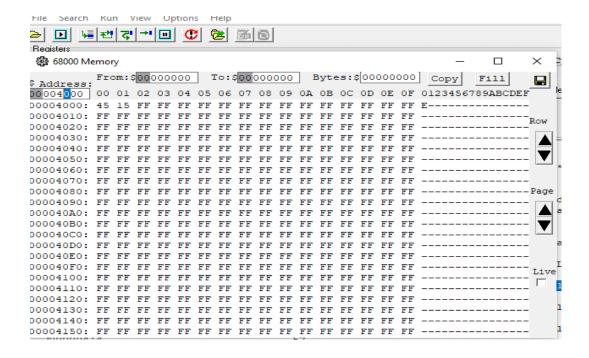
- From the easy68K manual ANDI # < data >, < ea >
- ANDI (AND immediate) → Adds a value to an effective addressing mode, instead in this operation the value #23 is adding to hexadecimal value #\$100. Therefore, this syntax is an error. To correct this error the #\$100 can be replace by effective addressing (Dn).

Q4. Create a source file and analyze the results.

• HW1Q4.X68 and HW1Q4.L68 files submitted

The word value of the data in memory location \$4000, when the program is just about to loop back to the place where "start" is labelled is \$45 15 in hexadecimal. I got this answer by doing a trace as it was executed. I use both "auto trace" and "step into" for tracing, and I analyze the memory value change at address \$4000 by looking into the memory view window.

Fig. 1 value saved at memory \$6000

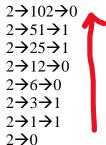


Q5. Two's complement

Assume that we are using a **16-bit system**. Represent a negative integer with two's complement format.

1. Convert the decimal numbers -102 and -87 into hexadecimal number.

Step 1: Convert to Bin.



102 \(\rightarrow 0000000001100110

Step 2: flip 0's to 1's and vice versa 11111111110011001

Step 3, add 1: 111111111110011001 + 1 = 111111111110011010

Step 4 convert to hex: **FF9A**

For -87

Step 1: change to bin → 0000000010101111

Step 2: flip numbers → 11111111110101000

Step 4: change to hex: **FFA9**

2. Add the two numbers from above

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FF9A + FFA9 = 1| FF43 N=1, Z=V=0, C=1

<u>Answer = FF43</u>

1111111110011010

111111111101000011
```

Ans: FF43

- The sign bit of the result is 1, which is the result is a negative number.
- Adding these two negative number doesn't result in a positive number, therefore no overflow.

3.

• HW1Q5.X68 and HW1Q5.L68 files submitted

Fig.2 the output result of two value (-102 and -87)

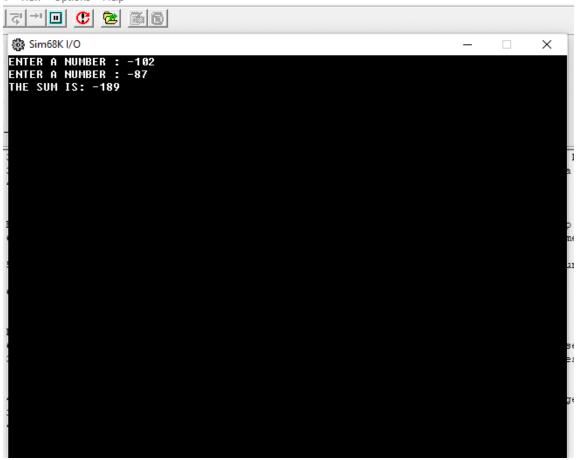


Fig 3. Value stored at memory address \$6000

