Regie Pineda Dr. Amed Awad CSS422 October 16th, 2019

Homework 1

Q1. Convert the hexadecimal number 973D4 to a number with base 15. (2 pts)

(Q1.)
$$H_{X}16 = 4$$
 $589,824 + 28,472 + 768 + 208 + 4$
 $D=13;13 \times 16 = 208$ $7 = 699,476$
 $3 \times 16^{2} = 168$
 $7 \times 16^{3} = 28,672$
 $9 \times 16^{4} = 589,824$
 $15 \times 69,476 = 41298 - 78 = 6$
 $15 \times 69,476 = 41298 - 78 = 3$
 $15 \times 79,476 = 183 - 78 = 8$
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Q2. Floating point numbers. (2 points)

2. 13.625

- Convert the following floating-point numbers to hexadecimal number in IEEE single-precision format. Please give the result as eight hexadecimal digits.
 -69/32 (-69 divide by 32)

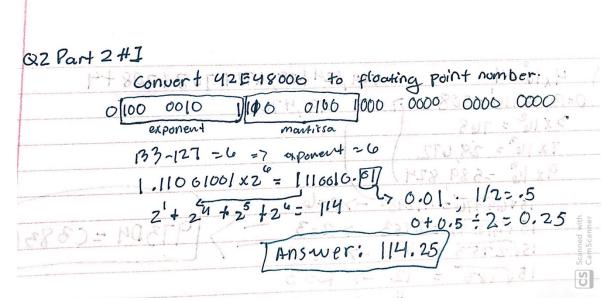
Convert 13.625; 13 = |10|0.625 x2 = 6.250 + 1 0.5 x2 = 0.0 + 1

0.25 x2 = 0.5 + 0 = 7 |10| \cdot |0| = 1.10|10| \times 2

Sign bit is 0, which is positive; exponent = 3, so 127 + 3 = 130, = 100000102

= 0 1000001610110100000 @000 00000

- Convert the following hexadecimal numbers in IEEE single-precision format to floating-point numbers:
 - 1. 42E48000



2. C6F00040

Q3. Error Finding (4 points)

Each of the following 68K assembly language instructions will cause an assembler error. Examine each instruction and explain why the assembler would flag it as an error.

1) MOVE.B \$A000, A3

O The assembly language instructions will cause an assembler error for two different reasons. First, the size of the operation is wrong. It's better to use MOVEA since that opcode limits the size to Word and Long Integer sizes. The minimum size for this intended operation should have been at least the size of a Word with it affected by sign extension. Also, MOVEA opcode should've been chosen for this because it involves moving or copying data to an address register. It should be noted that Easy68K allowed the use of a normal MOVE opcode.

2) ADD.B #\$1000, D2

O The assembly language instructions will cause an assembler error for two different reasons. First, the value that is being added is #\$1000 that is represented by 4 hexadecimal digits which is equivalent to 2 bytes. ADD.B will only account for a single byte, not the value of #\$1000. This is a size of a word so the operation should be ADDI.W or ADDI.L. Also, ADD opcode itself requires two effective addresses. Since the goal was to add an immediate value, the ADDI opcode should be used instead.

3) MOVEA.W \$1234, D0

 The assembly language instructions will cause an assembler error because it is copying data to a data register rather than an address register. The operation that would work is using MOVE.W

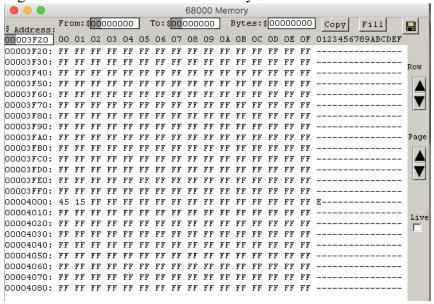
4) ANDI.B #23, #\$100

o The assembly language instructions will cause an assembler error because the destination should not be an immediate data. I say this because the destination operand in this operation is a value when proper usage requires it to be either data register, indirect address register, and memory address.

Q4. Create a source file and analyze the results. (4 points)

- o The .X68 and .L68 files are submitted separately.
- What is the **WORD VALUE** (not byte, or longword) of the data in memory location \$4000, when the program is just about to loop back to the place where "start" is labelled? **Please describe how you got the answer as well**. (For example, you can describe how you analyzed the code segments, or how you traced the code segments with debug tools)
 - The word value of the data in memory location \$4000, when the program is just about to loop back to the place where "start" is labelled is \$4515 in hexadecimal. I got this answer by doing a trace as it was executed. I was able to see the value in memory \$4000 by clicking on View then viewing the memory. Please see Figure 1 to confirm the word value at memory location \$4000.

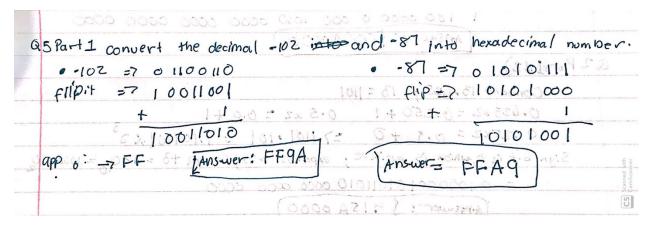
Figure 1: The value located in memory \$4000



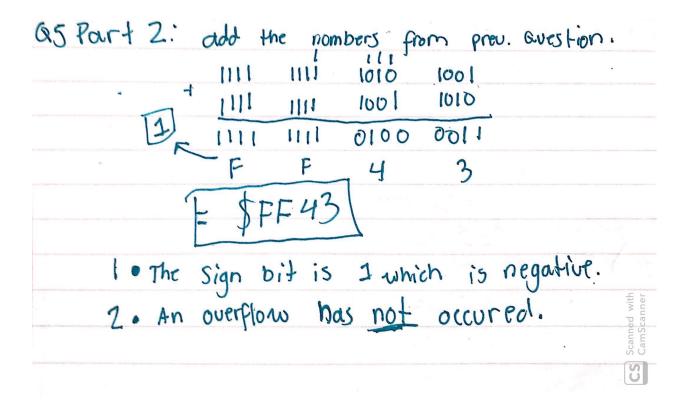
Q5. Two's complement (7 points)

Assume that we are using a **16-bit system**. Represent a negative integer with two's complement format.

1. (2 pts) Convert the decimal numbers -102 and -87 into hexadecimal number.



- 2. (1 pt) Add two numbers of the previous question as hexadecimal, and state
 - 1. whether the sign bit of the result is 1, and
 - 2. whether an overflow occurred.



- 3. (4 pts) Write a program in assembly language to add the two numbers (-102 and -87). Inputs should be in decimal format. Store the result as hexadecimal numbers at address \$6000. Print out the result in command output window in *decimal* format. (Hint: use the trap function task #3). If an error (overflow) happens, you should also print out the error (overflow) message as well.
 - o The .X68 and .L68 files are submitted separately.

Figure 2 shows the output result and Figure 3 shows the answer that is stored in memory system at address \$6000.

Figure 2: Output of the Program

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Sim68K I/O

Please enter a number:
-192

Please enter a second number:
-87

The sum of the two values is:
-189
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Figure 3: Answer located at memory address \$6000

-Re	gisters																							
DO	=00000003	D4=	00000000	A0= 0000000) A	4=[0	0000	000	00		T	S	INT		XNZ	VC.		Су	cle					
D1	=FFFFFF43	D5=	00000000	A1=0000108	9 A	.5=[c	0000	000	00	SR=	00	100	000	000	100	00					124			
n2	=0000FF9A	n6=	00000000	A2=0000000		6=[0000	200	00	115=	00:	e e o	200					Clea	ar Cyc	cles	1			
				1		1					_						_		_		1			
DЗ	00000000 D7=00000000 A3=00000000 A7=01000000																							
	Address	AddressCode From: \$00000000									68000 Memory To: \$00000000 Bytes: \$00													
				= \$ Address:	Fre	om: \$	000	000	000] '	Го::	00	000	000]]	3yte	25:	\$ 00	000	000	Copy Fill			
	00001010	4E4F		00005F30	00	01	02	03	04	05	06	07	08	09	OA	ОВ	0C	OD	ΟE	OF	0123456789ABCDE			
	00001012			00005F30:	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF				
	00001012 00001014	3401		00005F40:	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF				
	00001014	4250	0000106B	00005F50:	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF				
	00001014 0000101A	43F9 303C		00005F60:	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF								
	0000101K	4E4F	0000	00005F70:	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF				
	00001012	11.11		00005F80:	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF				
	00001020	3030	0004	00005F90:	FF	FF	FF	FF	FF	FF	FF	FF	FF			FF		FF	FF	FF				
	00001024	303C 4E4F		00005FA0:					FF	FF	FF	FF	FF					FF		FF				
	00001026			00005FB0:	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF				
	00001026	B242		00005FC0:					FF	FF	FF	FF	FF			FF	FF	FF		FF				
	00001028	691E		00005FD0:						FF	FF	FF	FF	FF	FF		FF	FF		FF				
	0000102A	D242		00005FE0:				FF			FF	FF	FF						FF					
	0000102C	691A		00005FF0:						FF		FF				FF		FF						
	0000102E			00006000:					FF			FF		FF							-C			
0	0000102E	31C1	6000	00006010:								FF						FF						
	00001032			00006020:								FF						FF						
0	00001032	43F9	00001089	00006030:						FF		FF						FF						
0	00001038	303C	000D	00006040:							FF	FF		FF						FF				
0	0000103C	4E4F		00006050:							FF	FF		FF		FF		FF		FF				
	0000103E			00006060:				77 77		77 77	4 T	FF FF	77	4 T		FF FF		7 T		44 44				
0	0000103E	103C	0003	00006070:				FF				11	77							11				
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