Brian Sheridan

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Education

Drexel University　　　　　　　　　　　　　　　　　　　　　　　　　**Philadelphia, PA**
BS/MS, GPA: 3.57　　　　　　　　　　　　　　　　　　　　　　　*September 2012 - June 2017*
Majoring in Computer Engineering; Minor in Computer Crime

Awards and Achievements

○ Eagle Scout, Boy Scouts of America, November 2011

○ AJ Drexel Scholarship, Drexel University, Fall 2012 - Present

○ IEEE-Eta Kappa Nu, Beta Alpha Chapter, November 2015

Computer Skills

Languages: C/C++, Python, CUDA, LaTeX, Matlab, VHDL, Android, LabVIEW, Bash, Make

Software: VIM, IAR, SVN, Git, GDB, Linux, Visual Studio, Vivado, ModelSim, PSpice, DOORS 8.3, Doxygen

Experience

Battelle Memorial Institute　　　　　　　　　　　　　　　　　　　　　　**Columbus, OH**
Embedded Software Developer　　　　　　　　　　　　　　　　　　*March 2016 - Present*
○ Developed embedded software for a neurostimulation medical device using Agile workflow
○ Implemented a hardware abstraction layer for peripherals such as SPI, DAC, DMA, UART and I2C
○ Utilized STM32F4 and PIC16 microcontrollers in the device
○ Wrote C code in compliance with MISRA C using PC-Lint
○ Worked closely with the hardware team to assist in design and testing of hardware

Drexel University　　　　　　　　　　　　　　　　　　　　　　　　　**Philadelphia, PA**
Help Desk Analyst　　　　　　　　　　　　　　　　　　　　　　*August 2015 - March 2016*
○ Provided help desk support for the Lebow College faculty and students

Woodward McCoach Inc.　　　　　　　　　　　　　　　　　　　　　**West Chester, PA**
Embedded Software Engineer　　　　　　　　　　　　　　　　*September 2014 - March 2015*
○ Developed embedded C driver code for high speed fiber optic switch and chassis managers
○ Performed software development in a Unix environment
○ Wrote test scripts in Python to test hardware functionality and regression testing
○ Created I2C bus and ELF file analysis scripts with Python and LaTeX
○ Performed quality assurance and testing on hardware in production

Allen-Sherman-Hoff　　　　　　　　　　　　　　　　　　　　　　　　**Exton, PA**
Electrical Designer　　　　　　　　　　　　　　　　　　　　*September 2013 - March 2014*
○ Created and revised AutoCAD drawings for ASH handling systems, including: Elementary Wiring Diagrams, Control Panel Arrangements, Input/Output Wiring Diagrams, Logic Diagrams, and Terminal Block Arrangements
○ Performed quality control testing for control panels and maintenance stations
○ Wrote Visual Basic Application and AutoLISP scripts to automate tedious tasks in AutoCAD

Innovative Solutions & Support　　　　　　　　　　　　　　　　　　　　**Eagle, PA**
Software Test Engineer　　　　　　　　　　　　　　　　　　*June 2013 - September 2013*
○ Tested embedded C software functionality through visual, manual and automated testing
○ Wrote and edited Excel based test scripts for use with their automated testing program
○ Maintained documentation and tracing in compliance with Federal Aviation Administration through Doors 8.3
○ Wrote and verified software bug reports with Seapine TestTrack