Brian Sheridan

14 Glen Loch Way - Malvern, PA - 19355

□ +1 (484) 614 0828 • □ bts37@drexel.edu • • • bsheridan94

Education

Drexel University BS/MS, GPA: 3.54

Philadelphia, PA

September 2012 - June 2017

Majoring in Computer Engineering

Awards and Achievements

o Eagle Scout, Boy Scouts of America, November 2011

o AJ Drexel Scholarship, Drexel University, Fall 2012 - Present

o IEEE-Eta Kappa Nu, Beta Alpha Chapter, November 2015

Computer Skills

Languages: C/C++, Python, Assembly, LATEX, Matlab, VHDL, Android, LabVIEW Software: VIM, IAR, SVN, Git, GDB, Eagle, Linux, AWS, Visual Studio, Vivado

Microcontroller Families: STM32F4, ESP32, Xilinx, Atmega, Raspberry Pi, Kinetis K60, PIC

Experience

Battelle Memorial Institute

Columbus, OH

Embedded Software Developer

March 2016 - September 2016

- o Developed embedded software for a neurostimulation medical device using Agile workflow
- o Implemented a hardware abstraction layer for peripherals such as SPI, DAC, DMA, UART and I2C
- o Utilized STM32F4 and PIC16 microcontrollers in the device
- o Wrote C code in compliance with MISRA C using PC-Lint
- o Worked closely with the hardware team to assist in design and testing of hardware

Drexel University Philadelphia, PA August 2015 - March 2016 Help Desk Analyst

o Provided help desk support for the Lebow College faculty and students

Woodward McCoach Inc. Embedded Software Engineer West Chester, PA

September 2014 - March 2015

- o Developed embedded C driver code for high speed fiber optic switch and chassis managers
- o Performed software development in a Unix environment
- o Wrote test scripts in Python to test hardware functionality and regression testing
- o Created I2C bus and ELF file analysis scripts with Python and LATEX
- o Performed quality assurance and testing on hardware in production

Allen-Sherman-Hoff Exton, PA

Electrical Designer

Software Test Engineer

September 2013 - March 2014

June 2013 - September 2013

- o Created and revised AutoCAD drawings for ASH handling systems, including: Elementary Wiring Diagrams, Control Panel Arrangements, Input/Output Wiring Diagrams, Logic Diagrams, and Terminal Block Arrangements
- o Performed quality control testing for control panels and maintenance stations
- o Wrote Visual Basic Application and AutoLISP scripts to automate tedious tasks in AutoCAD

Innovative Solutions & Support

Eagle, PA

- o Tested embedded C software functionality through visual, manual and automated testing
- o Wrote and edited Excel based test scripts for use with their automated testing program
- o Maintained documentation and tracing in compliance with Federal Aviation Administration through Doors 8.3
- o Wrote and verified software bug reports with Seapine TestTrack