

## **TO WHOMSOEVER IT MAY CONCERN**

This is to certify that Buddanti Pardiv Satya Kumar (21BEC7242), has successfully completed his VLSI Design Internship Program from 22/08/2023 to 30/10/2023

During his internship program with us, he worked on the below mentioned project AHB2APB bridge design using Verilog HDL.

We wish him all the best in his future endeavours.

For Maven Silicon Softech Pvt Ltd.



**Sweety Dharamdasani**  
Head – Learning & Development

Place: Bengaluru  
Date: 23/11/2023