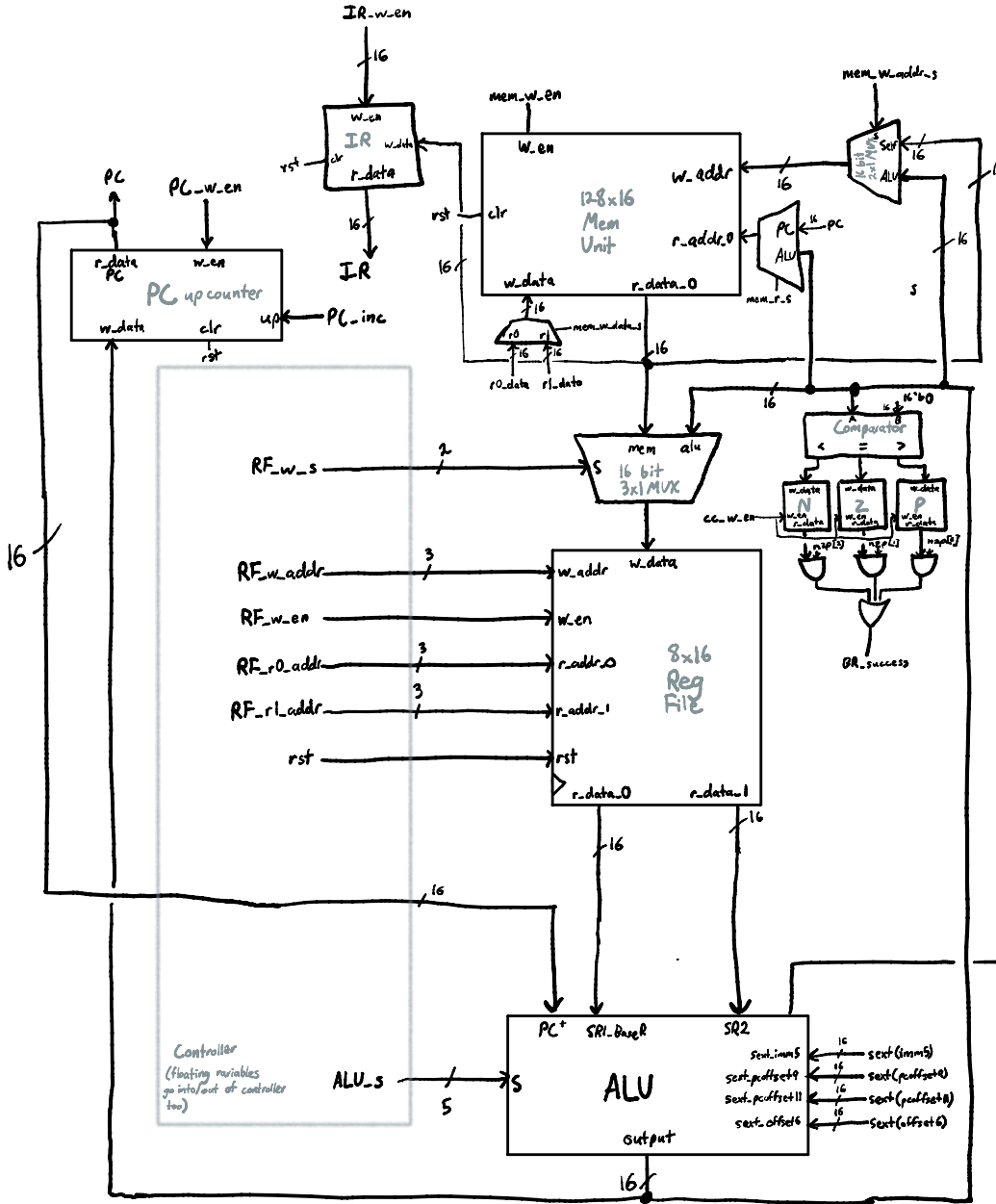


# PUnC Checkpoint 2

Monday, December 5, 2022 6:18 PM



s	ALU output
`add	SR1_BaseR + SR2
`addi	SR1_BaseR + sext_imm5
`and	SR1_BaseR & SR2
`andi	SR1_BaseR & sext_imm5
`br	if (br_success) pc <sup>+</sup> + sext_pcoffset9 else pc <sup>+</sup>
`jmp_ret	SR1_BaseR
`jsr	pc <sup>+</sup> + sext_pcoffset11
`jsrr	SR1_BaseR
`ld	pc <sup>+</sup> + sext_pcoffset9
`ldi1	pc <sup>+</sup> + sext_pcoffset9
`ldi2	SR1_BaseR //really mem[pc <sup>+</sup> +sext_pcoffset9]
`ldr	SR1_BaseR + sext_offset6
`lea	pc <sup>+</sup> + sext_pcoffset9
`not	NOT(SR1_BaseR)
`st	pc <sup>+</sup> + sext_pcoffset9
`sti1	pc <sup>+</sup> + sext_pcoffset9
`str	SR1_BaseR + sext_offset6