

PUNC Checkpoint 2

Monday, December 5, 2022 10:23 PM

	fetch	decode	ADD	ADDI	AND	ANDI	BR	JMP/RET	JSR	JSRR	LD	LDI(inner)	LDI(outer)	LDR	LEA	NOT	ST	STI	STR	HALT
IR_w_en	1																			
PC_inc		1																		
PC_w_en							1	1	1											
RF_w_en			1	1	1	1					1	1	1	1	1	1				
RF_w_data_s			`rf_w_s_alu	`rf_w_s_alu	`rf_w_s_alu	`rf_w_s_alu					`rf_w_s_mem	`rf_w_s_mem	`rf_w_s_mem	`rf_w_s_mem	`rf_w_s_alu	`rf_w_s_alu				
RF_w_addr			DR	DR	DR	DR					DR	DR	DR	DR	DR	DR				
RF_r0_addr			SR1	SR1	SR1	SR1		BaseR		BaseR			DR	BaseR		SR	SR	SR	BaseR	
RF_r1_addr			SR2		SR2														SR	
ALU_s			`add	`addi	`and	`andi	`br	`jmp_ret	`jsr	`jsrr	`ld	`ldi1	`ldi2	`ldr	`lea	`not	`st	`sti	`str	
cc_w_en			1		1						1		1	1	1	1				
mem_r_s	`mem_r_s_pc										`mem_r_s_alu	`mem_r_s_alu	`mem_r_s_alu	`mem_r_s_alu				`mem_r_s_alu		
mem_w_addr_s																	`mem_wa_s_alu	`mem_wa_s_self	`mem_wa_s_alu	
mem_w_data_s																	`mem_wd_s_r0	`mem_wd_s_r0	`mem_wd_s_r1	
mem_w_en																	1		1	