

BIYYAPU HARI KEERTHI

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CAREER OBJECTIVE

Electronics and Communication Engineering student with a strong foundation in programming and analytics, seeking to contribute to software development and risk management initiatives. Passionate about technology, collaboration, and continuous learning.

ACADEMIC QUALIFICATIONS

1. **SASTRA Deemed University (2022-2026)**
 - B.Tech. in Electronics and Communication Engineering
 - **Current CGPA: 9.75**
2. **Narayana Junior College (2020-2022)**
 - **12th standard - 97.7%**
3. **Ratnam High School (2014-2020)**
 - **10th standard - 99.33%**

SKILLS

- **Programming Languages:** Python, C++, C
- **SQL**
- **Frameworks and Libraries:** Django
- **HTML , CSS**
- **Machine Learning Techniques**
- **Verilog HDL**

DESIGN TOOLS

Model Sim, Quartus, Xilinx Vivado, Matlab, Power BI

AREAS OF INTERESTS

- Artificial intelligence and Machine learning
- Data Science and Data Visualization
- FPGA programming
- VLSI

CERTIFICATIONS

- **Digital Design prototyping using FPGA**
 - Board Used: ZED FPGA Board
 - Programming Language: Verilog
 - Software Used: Modelsim, Vivado
 - Implemented many operations on ZED board using Verilog and Vivado software
- VLSI for Beginners course organised by NIELIT Calicut

- Verilog Workshop conducted by ASCIIEE Organisation of SASTRA
 - Machine Learning
 - Deep Learning: Image Classification using CNN
 - MATLAB Onramp
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PROJECTS

- **Web Development: Amazon clone**
 - **Technologies Used:** HTML,CSS
 - **Car Price Prediction using Linear Regression**
 - **Technologies Used:** Python
 - Predicted price of the car with new features.
 - **Mini Radio Station using Adalm Pluto**
 - **Technologies Used:** Matlab
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INTERNSHIPS

- **Machine Failure Prediction using Random Forest**
 - Technologies Used: Python, Scikit-learn, Numpy, Pandas
 - Predicted potential machine failures by analyzing operational data using a Random Forest model.
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COURSES

- Electrical and Electronics: Digital Electronics, Digital Signal Processing
 - Current courses: System Verilog, Digital VLSI Design, Deep Learning
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ACHIEVEMENTS

- Secured 3rd price in Logic Gates Competition conducted by ASCIIEE Organisation of SASTRA Deemed University.
 - Currently doing minor specialisation in Artificial Intelligence and Machine Learning course.
 - Scholarship from Blue Star company
 - (National Scholarship Portal) NSP scholarship
 - Temenos Scholarship
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LANGUAGES KNOWN : English, Telugu