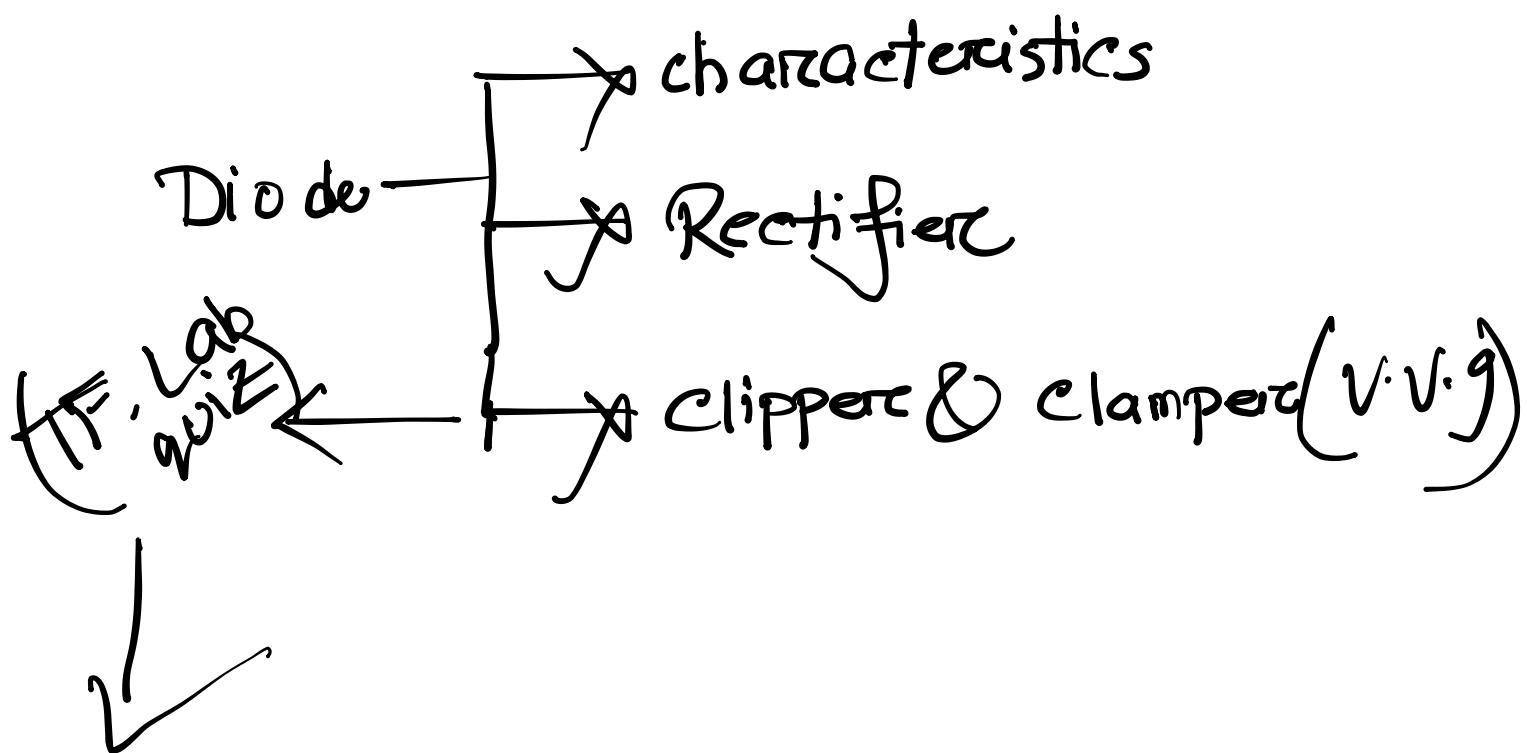


## Experiments:-

① Diode  $\rightarrow$  3

② BJT

③ OP AMP



## Variations

$\downarrow$   $\rightarrow$  Design problems

(circuit  $\rightarrow$  output)

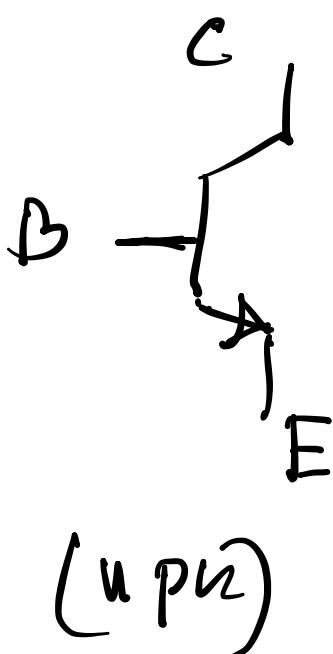
(output  $\rightarrow$  circuit)

(important)  
(graphs)

BJT

characteristics

l. fix one terminal &  
I-V for other 2)

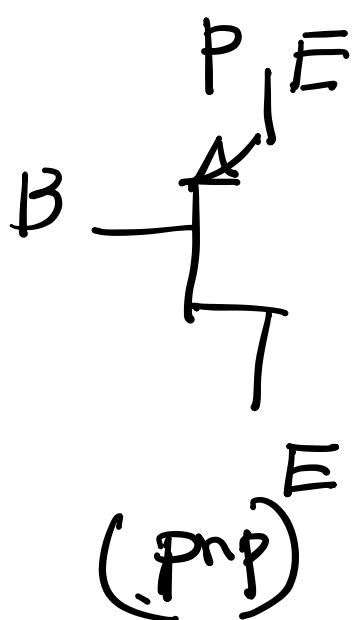
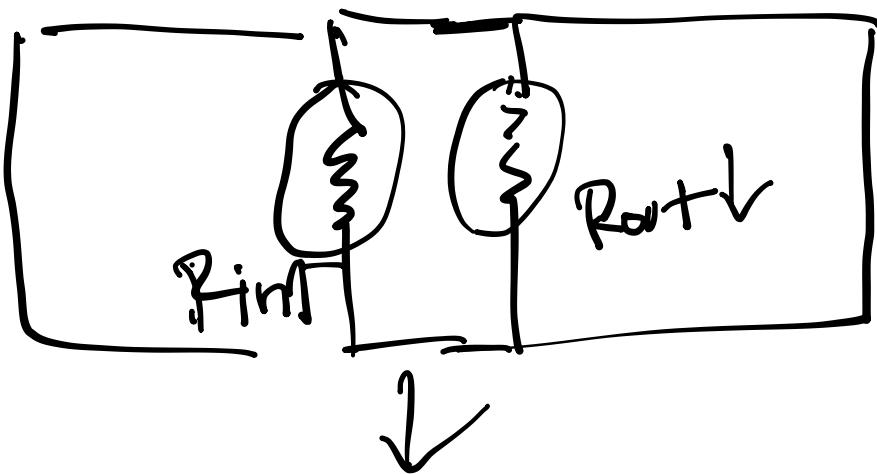


→ Biasing

→ Amplifier (E).

→ Emitter follower / buffer.

→ 2 stage amplifier.



Buffer  
(Gain = 1).

• OP-AMP.  $\rightarrow$  Mathematical operations  
(IC) on circuit.

(V.V.G)



(Lab quiz)

Marks Distribution:-

Attendance — 10%.

Assignment — 70%.

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100%.