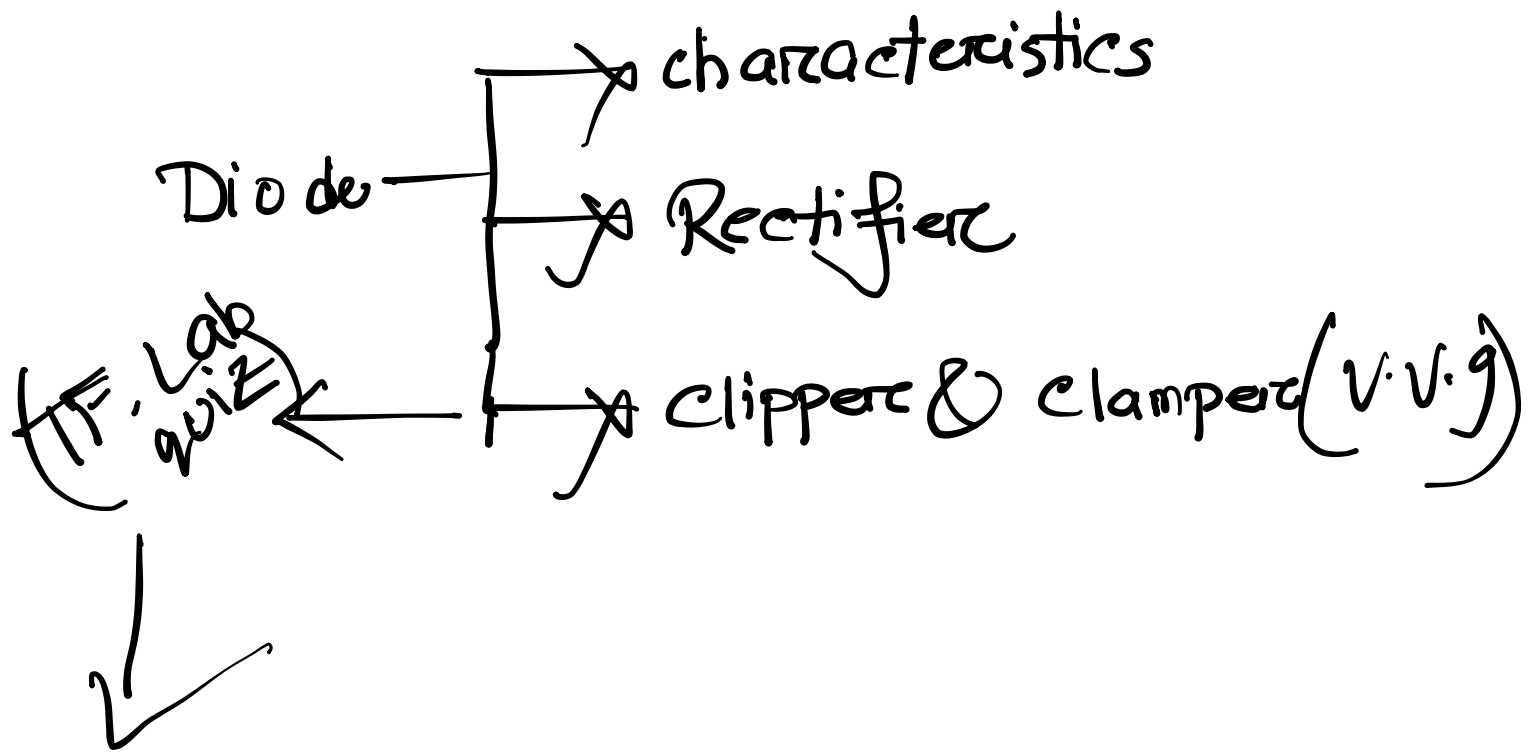


Experiments:-

- ① Diode $\rightarrow 3$
- ② BJT
- ③ OP AMP

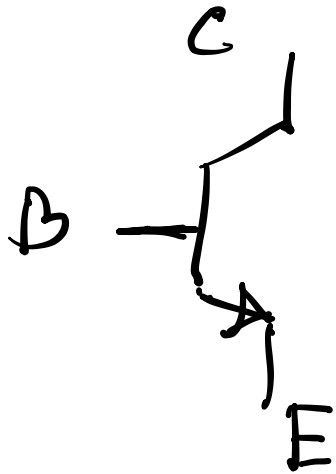


Variations

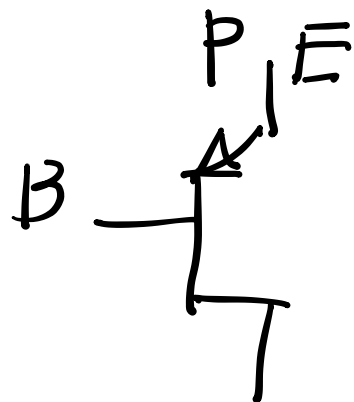
\rightarrow Design problems
(circuit \rightarrow output)
(output \rightarrow circuit)

(important)
(graphs)

BJT



(NPN)



(PNP)

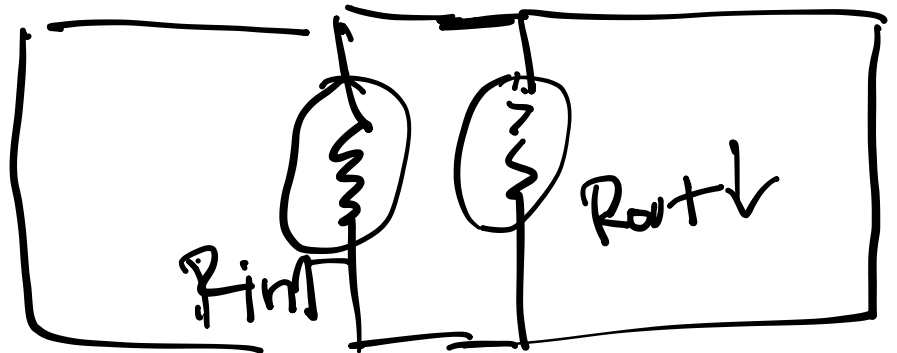
characteristics $\begin{cases} \rightarrow CB \\ \rightarrow CE \end{cases}$
 (fix one terminal &
 I-V for other 2)

Biassing

Amplifier (CE).

Emitter follower/buffer.

2 stage Amplifier.



Buffer
 (Gain = 1).

OP-AMP. \rightarrow Mathematical operations
(IC) on circuit.

(V.V.9)

\downarrow
(Lab quiz)

Marks Distribution:-

Attendance — 10%.

স্বাক্ষর — 90%.

100%.