



# **CSE 205: DIGITAL LOGIC DESIGN**

# TEXTBOOK

- Digital Design (5<sup>th</sup> Edition)
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# DIGITAL SYSTEMS

- Why study digital logic design
  - To design and build a digital system
- Digital Systems
  - Example: digital cameras, digital telephones, digital television, digital computers...
  - Used in communication, business transactions, traffic control, space guidance, medical treatment, weather monitoring, the Internet ...



# BINARY LOGIC

- In a digital system, all signals take on discrete values.
  - Also referred as states
- Most modern digital systems operate on 2 discrete states
  - Binary logic system
    - Deals with binary variables and a set of logical operations



# BINARY LOGIC

- We represent the two states of binary variable as
  - True and false
  - 1 and 0
  - High and Low
- Three basic logic operations
  - AND:  $x \cdot y = z$  or  $xy = z$
  - OR:  $x + y = z$
  - NOT:  $x' = z$  or  $\overline{x} = z$



# TRUTH TABLE

- Specifies results for all possible input combinations

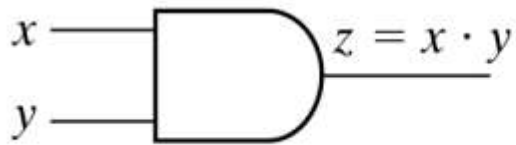
<i>Truth Tables of Logical Operations</i>					
<b>AND</b>			<b>OR</b>		
$x$	$y$	$x \cdot y$	$x$	$y$	$x + y$
0	0	0	0	0	0
0	1	0	0	1	1
1	0	0	1	0	1
1	1	1	1	1	1

$x$	$x'$
0	1
1	0

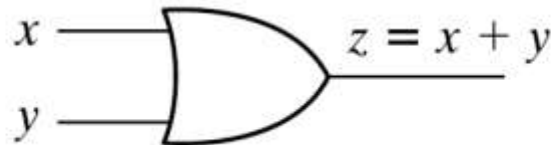


# LOGIC GATES

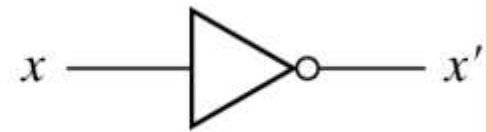
- Logic gates are electronic circuits that operate on one or more input signals to produce an output signal.



(a) Two-input AND gate



(b) Two-input OR gate



(c) NOT gate or inverter

Fig. 1-4 Symbols for digital logic circuits



# LOGIC GATES

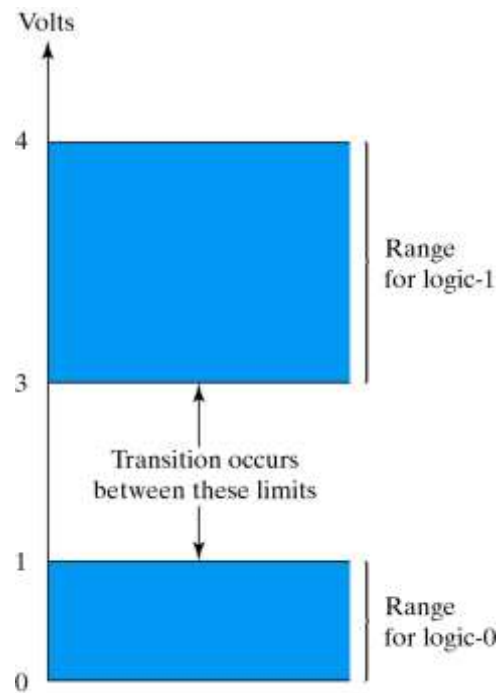
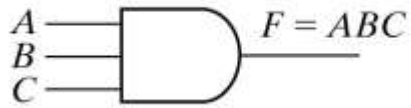


Fig. 1-3 Example of binary signals

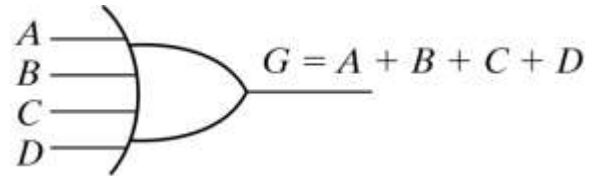




# LOGIC GATES



(a) Three-input AND gate



(b) Four-input OR gate

Fig. 1-6 Gates with multiple inputs



# BOOLEAN FUNCTION

- An algebraic expression
  - Binary variables
  - Constants 0 and 1
  - Logic operation symbols

$$f(x,y,z) = (x + y')z + x'$$

- Some terminology, notation and precedence:
  - $f$  is the name of the function.
  - $(x,y,z)$  are the input variables, each representing 1 or 0.
  - A literal is a single variable within a term, in complemented or uncomplemented form. The function above has four literals:  $x$ ,  $y'$ ,  $z$ , and  $x'$ .
  - Parenthesis has the highest precedence, followed by NOT, AND, and then OR.



# GATE IMPLEMENTATION OF A FUNCTION

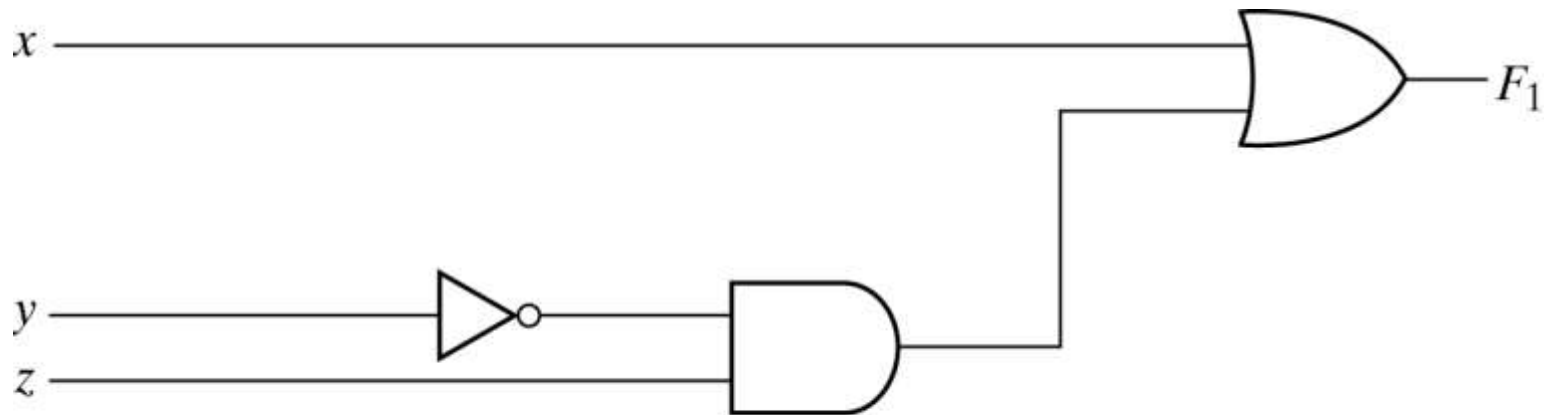
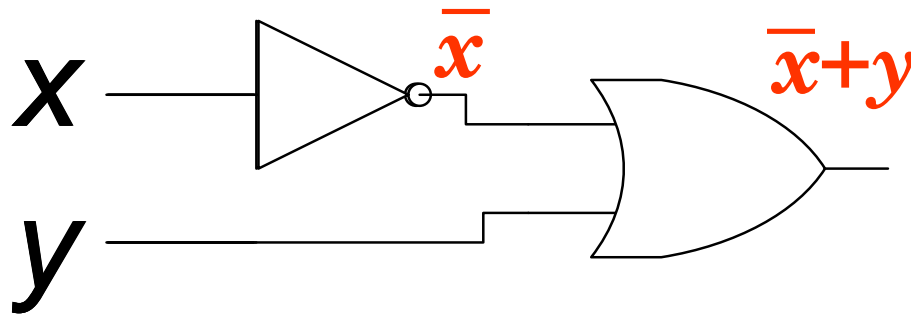


Fig. 2-1 Gate implementation of  $F_1 = x + y'z$



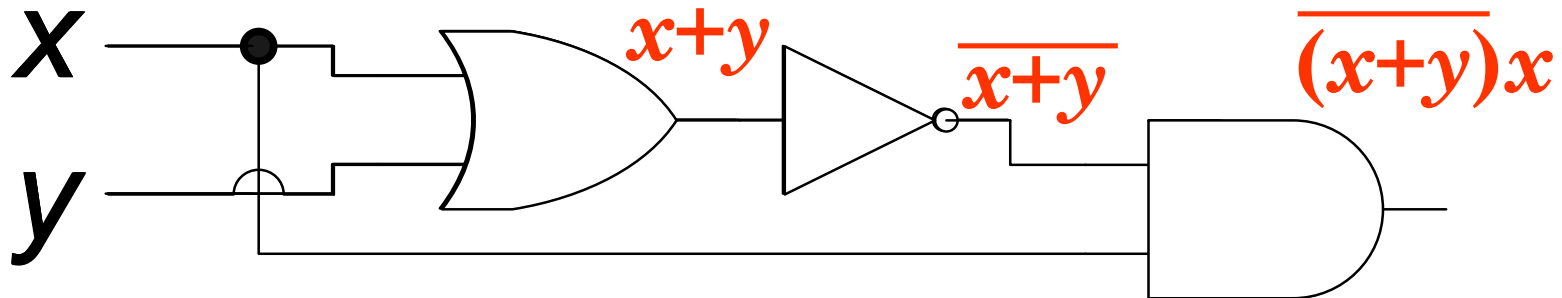
# GATE IMPLEMENTATION OF A FUNCTION

$$f(x,y,z) = x' + y$$



# GATE IMPLEMENTATION OF A FUNCTION

$$f(x,y,z) = \overline{(x + y)}x$$



# BOOLEAN FUNCTION

- A Boolean function can be represented in a truth table.

$$f(x,y,z) = (x + y')z + x'$$



$$f(0,0,0) = (0 + 1)0 + 1 = 1$$

$$f(0,0,1) = (0 + 1)1 + 1 = 1$$

$$f(0,1,0) = (0 + 0)0 + 1 = 1$$

$$f(0,1,1) = (0 + 0)1 + 1 = 1$$

$$f(1,0,0) = (1 + 1)0 + 0 = 0$$

$$f(1,0,1) = (1 + 1)1 + 0 = 1$$

$$f(1,1,0) = (1 + 0)0 + 0 = 0$$

$$f(1,1,1) = (1 + 0)1 + 0 = 1$$



x	y	z	$f(x,y,z)$
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

# BOOLEAN FUNCTION

- A Boolean function can be represented in a truth table.

$$F(x, y, z) = x\bar{z} + y$$

$$F(x, y, z) = x\bar{z} + y$$

x	y	z	$\bar{z}$	$x\bar{z}$	$x\bar{z} + y$
0	0	0	1	0	0
0	0	1	0	0	0
0	1	0	1	0	1
0	1	1	0	0	1
1	0	0	1	1	1
1	0	1	0	0	0
1	1	0	1	1	1
1	1	1	0	0	1

# NUMBER SYSTEMS

- Consists of **TWO Things**:
  - A **BASE** or **RADIX** Value
  - A **SET** of **DIGITS**
    - *Digits are symbols representing all values **less than the radix value**.*
- Example is the Common Decimal System:
  - $\text{RADIX (BASE)} = 10$
  - Digit Set =  $\{0, 1, 2, 3, 4, 5, 6, 7, 8, 9\}$





# DECIMAL NUMBER SYSTEMS

- Consider: 5032.21

$$\begin{aligned}(5032.21)_{10} &= 5 \times (10)^3 + 0 \times (10)^2 + 3 \times (10)^1 + 2 \times (10)^0 + 2 \times (10)^{-1} + 1 \times (10)^{-2} \\ &= 5000 + 0 + 30 + 2 + 0.2 + 0.01\end{aligned}$$

- Other Notation:  $(5032.21)_{10}$
- In general, a number expressed in a base- $r$  system has coefficients multiplied by powers of  $r$ .

$$a_n \cdot r^n + a_{n-1} \cdot r^{n-1} + \dots + a_1 \cdot r^1 + a_0 + a_{-1} \cdot r^{-1} + a_{-2} \cdot r^{-2} + \dots + a_{-m} \cdot r^{-m}$$



# OTHER NUMBER SYSTEMS

- Binary

- Radix =  $(2)_{10}$
- Digit Set =  $\{0,1\}$

- Octal

- Radix =  $(8)_{10}$
- Digit Set =  $\{0,1,2,3,4,5,6,7\}$

- Hexadecimal

- Radix =  $(16)_{10}$
- Digit Set =  $\{0,1,2,3,4,5,6,7,8,9,A,B,C,D,E,F\}$



# BINARY NUMBER SYSTEMS

- Binary
  - Radix =  $(2)_{10}$
  - Digit Set =  $\{0,1\}$

- Binary to Decimal:

$$\begin{aligned}(1101.01)_2 &= 1 \times 2^3 + 1 \times 2^2 + 0 \times 2^1 + 1 \times 2^0 + 0 \times 2^{-1} + 1 \times 2^{-2} \\ &= (13.25)_{10}\end{aligned}$$



# BINARY TO DECIMAL: PRACTICE

a)  $0110_2 = ?$        $6_{10}$

b)  $11010_2 = ?$        $26_{10}$

c)  $0110101_2 = ?$        $53_{10}$

d)  $11010011_2 = ?$        $211_{10}$



# OCTAL NUMBER SYSTEMS

- Octal

- Radix =  $(8)_{10}$
- Digit Set =  $\{0,1,2,3,4,5,6,7\}$

- Octal to Decimal:

$$\begin{aligned}(15.2)_8 &= 1 \times 8^1 + 5 \times 8^0 + 2 \times 8^{-1} \\ &= (13.25)_{10}\end{aligned}$$



# HEXADECIMAL NUMBER SYSTEMS

- Hexadecimal

- Radix =  $(16)_{10}$
- Digit Set =  $\{0,1,2,3,4,5,6,7,8,9,A,B,C,D,E,F\}$

- Hexadecimal to Decimal:

$$\begin{aligned}(D.4)_{16} &= D \times (16)^0 + 4 \times (16)^{-1} \\ &= (13.25)_{10}\end{aligned}$$



# DECIMAL (*INTEGER*) TO BINARY


- Divide the number by the 'Base' (=2)
- Take the remainder (either 0 or 1) as a coefficient
- Take the quotient and repeat the division

**Example:**  $(13)_{10}$

	Quotient	Remainder	Coefficient
$13 / 2 =$	6	1	$a_0 = 1$
$6 / 2 =$	3	0	$a_1 = 0$
$3 / 2 =$	1	1	$a_2 = 1$
$1 / 2 =$	0	1	$a_3 = 1$

**Answer:**  $(13)_{10} = (a_3 a_2 a_1 a_0)_2 = (1101)_2$

MSB                      LSB



# DECIMAL TO BINARY: PRACTICE

a)  $13_{10} = ?$      $1\ 1\ 0\ 1_2$

b)  $22_{10} = ?$      $1\ 0\ 1\ 1\ 0_2$

c)  $43_{10} = ?$      $1\ 0\ 1\ 0\ 1\ 1_2$

d)  $158_{10} = ?$      $1\ 0\ 0\ 1\ 1\ 1\ 1\ 0_2$





# DECIMAL (*FRACTION*) TO BINARY

- Multiply the number by the 'Base' (=2)
- Take the integer (either 0 or 1) as a coefficient
- Take the resultant fraction and repeat the division

**Example:**  $(0.625)_{10}$

		Integer	Fraction	Coefficient
$0.625$	$* 2 =$	$1$	$. 25$	$a_{-1} = 1$
$0.25$	$* 2 =$	$0$	$. 5$	$a_{-2} = 0$
$0.5$	$* 2 =$	$1$	$. 0$	$a_{-3} = 1$

**Answer:**  $(0.625)_{10} = (0.a_{-1}a_{-2}a_{-3})_2 = (0.101)_2$

$\uparrow$                        $\uparrow$   
MSB                      LSB



# DECIMAL TO OCTAL CONVERSION

Example:  $(175)_{10}$

	Quotient	Remainder	Coefficient
$175 / 8 =$	21	7	$a_0 = 7$
$21 / 8 =$	2	5	$a_1 = 5$
$2 / 8 =$	0	2	$a_2 = 2$

Answer:  $(175)_{10} = (a_2 a_1 a_0)_8 = (257)_8$

Example:  $(0.3125)_{10}$

	Integer	Fraction	Coefficient
$0.3125 * 8 =$	2	. 5	$a_{-1} = 2$
$0.5 * 8 =$	4	. 0	$a_{-2} = 4$

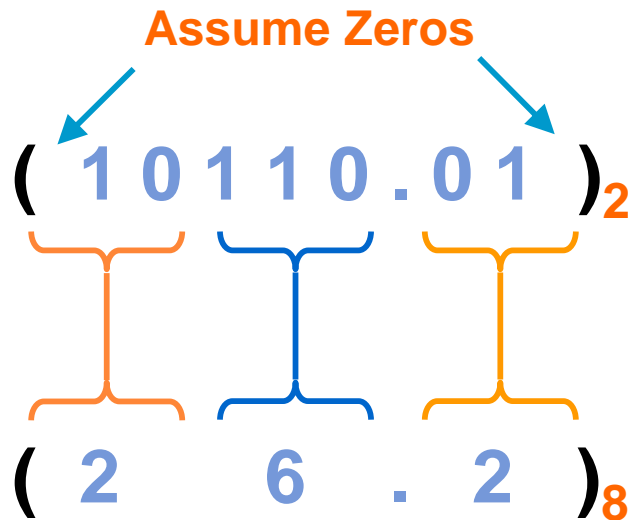
Answer:  $(0.3125)_{10} = (0.a_{-1} a_{-2} a_{-3})_8 = (0.24)_8$



# BINARY – OCTAL CONVERSION

- $8 = 2^3$
- Each group of 3 bits represents an octal digit

**Example:**



Octal	Binary
0	0 0 0
1	0 0 1
2	0 1 0
3	0 1 1
4	1 0 0
5	1 0 1
6	1 1 0
7	1 1 1

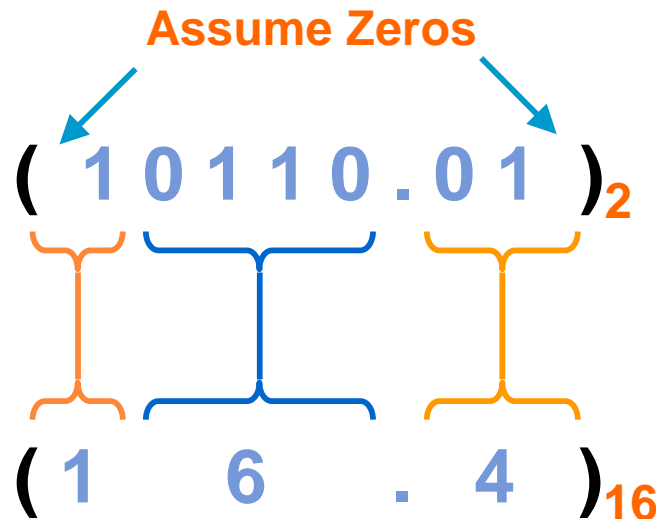
Works **both** ways (Binary to Octal & Octal to Binary)



# BINARY – HEXADECIMAL CONVERSION

- $16 = 2^4$
- Each group of 4 bits represents a hexadecimal digit

**Example:**



Hex	Binary
0	0000
1	0001
2	0010
3	0011
4	0100
5	0101
6	0110
7	0111
8	1000
9	1001
A	1010
B	1011
C	1100
D	1101
E	1110
F	1111

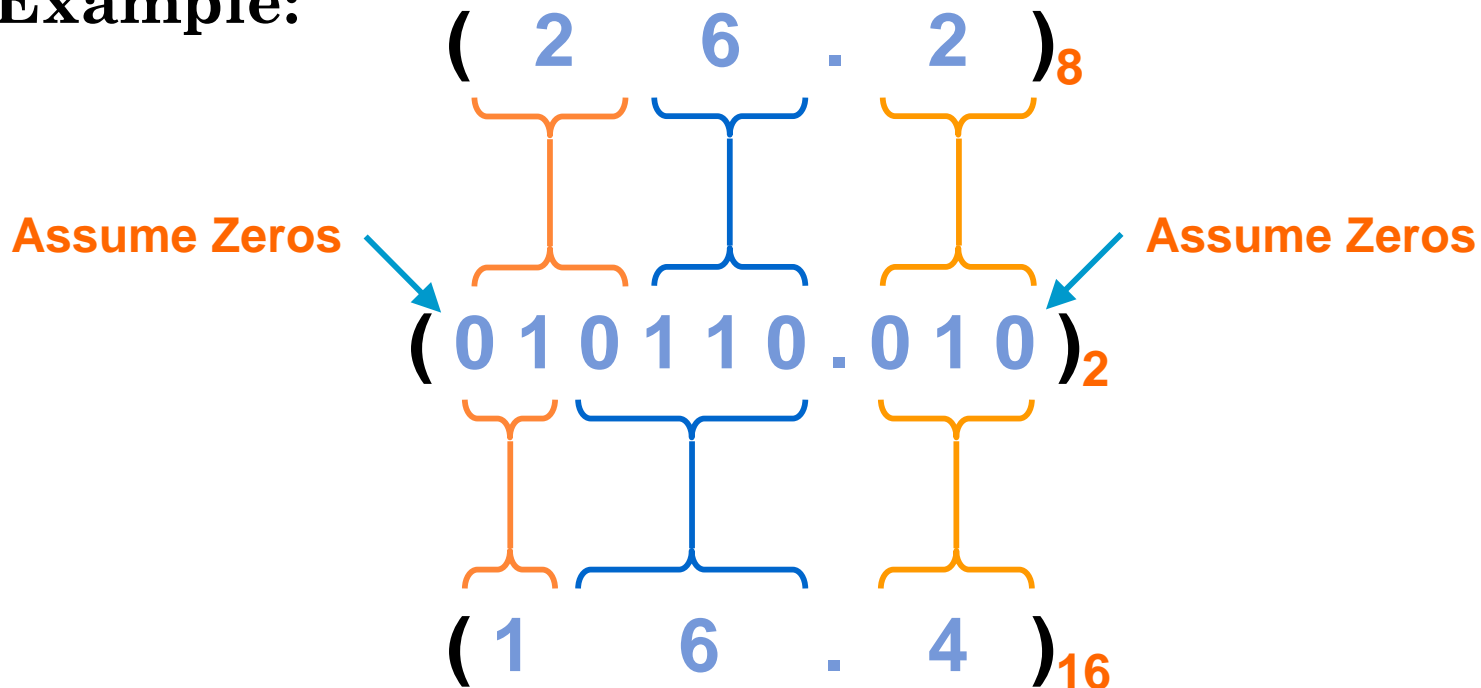
Works **both** ways (Binary to Hex & Hex to Binary)



# OCTAL – HEXADECIMAL CONVERSION

- Convert to **Binary** as an intermediate step


**Example:**



Works **both** ways (Octal to Hex & Hex to Octal) 

# BINARY ADDITION

	1	1	1	1	1	1		
		1	1	1	1	0	1	= 61
+			1	0	1	1	1	= 23
<hr/>								
	1	0	1	0	1	0	0	= 84

  $\geq (2)_{10}$



# BINARY SUBTRACTION

$$\begin{array}{rcccccc}
 & & 1 & & 2 & & \\
 & 0 & \cancel{2} & 2 & 0 & 0 & 2 \quad \swarrow = (10)_2 \\
 \cancel{1} & 0 & 0 & \cancel{1} & \cancel{1} & 0 & 1 \quad = 77 \\
 - & & & 1 & 0 & 1 & 1 & 1 \quad = 23 \\
 \hline
 & 0 & 1 & 1 & 0 & 1 & 1 & 0 \quad = 54
 \end{array}$$



# COMPLEMENTS

- They are used to simplify the subtraction operation
- Two types (for each *base-r* system)
  - Diminishing radix complement ( $r-1$ 's complement)
  - Radix complement ( $r$ 's complement)

For  $n$ -digit number  $N$

$$(r^n - 1) - N \longrightarrow r-1\text{'s complement}$$

$$r^n - N \longrightarrow r\text{'s complement}$$





# DIMINISHED RADIX COMPLEMENT

## ○ Example for 6-digit decimal numbers:

- 9's complement is  $(r^n - 1) - N = (10^6 - 1) - N = 999999 - N$
- 9's complement of 546700:

$$999999 - 546700 = 453299$$

## ○ Example for 7-digit binary numbers:

- 1's complement is  $(r^n - 1) - N = (2^7 - 1) - N = 1111111 - N$
- 1's complement of 1011000:

$$1111111 - 1011000 = 0100111$$



# RADIX COMPLEMENT

- The  $r$ 's complement of an  $n$ -digit number  $N$  in base  $r$  is defined as  $r^n - N$  for  $N \neq 0$  and as 0 for  $N = 0$ .

- The  $r$ 's complement can also be obtained by adding 1 to the  $(r - 1)$ 's complement, since

$$r^n - N = [(r^n - 1) - N] + 1.$$

- Decimal Number:  $10^n - N$

- Example: 10's complement of 246700 is 753300



# RADIX COMPLEMENT (BINARY NUMBER)

- Take 1's complement then add 1

OR

- Toggle all bits to the left of the first '1' from the right

- **Example:**

$$\begin{array}{r} 1\ 0\ 1\ 1\ 0\ 0\ 0\ 0 \\ 0\ 1\ 0\ 0\ 1\ 1\ 1\ 1 \\ + \phantom{0000000} 1 \\ \hline 0\ 1\ 0\ 1\ 0\ 0\ 0\ 0 \end{array}$$

$$1\ 0\ 1\ 1\ 0\ 0\ 0\ 0$$

$$0\ 1\ 0\ 1\ 0\ 0\ 0\ 0$$



# SUBTRACTION WITH COMPLEMENTS

- $M - N$ 
  - Add  $M$  to  $r$ 's complement of  $N$ 
    - $Sum = M + (r^n - N) = M - N + r^n$
  - If  $M > N$ ,  $Sum$  will have an end carry  $r^n$ , discard it
  - If  $M < N$ ,  $Sum$  will not have an end carry and
    - $Sum = r^n - (N - M)$  ( $r$ 's complement of  $N - M$ )
    - So  $M - N = - (r\text{'s complement of } Sum)$



# SUBTRACTION WITH COMPLEMENTS

- 65438 - 5623

	65438
10's complement of 05623	<u>+94377</u>
	159815
Discard end carry $10^5$	<u>-100000</u>
Answer	59815



# SUBTRACTION WITH COMPLEMENTS

- 5623 - 65438

$$\begin{array}{r} 05623 \\ 10\text{'s complement of } 65438 \quad + \quad \underline{34562} \\ \hline \quad \quad \quad \rightarrow \quad 40185 \end{array}$$

There is no  
end carry.

Therefore, the answer is:

$$- (10\text{'s complement of } 40185) = - 59815.$$



# SUBTRACTION WITH COMPLEMENTS

- 10110010 - 10011111

	10110010
2's complement of 10011111	<u>+01100001</u>
	100010011
Discard end carry $2^8$	<u>- 100000000</u>
Answer	00010011



# SUBTRACTION WITH COMPLEMENTS

- $10011111 - 10110010$

$$\begin{array}{r} 10011111 \\ 2\text{'s complement of } 10110010 \quad + \underline{01001110} \\ \hline \text{→ } 11101101 \end{array}$$

There is no  
end carry.

Therefore, the answer is

$$Y - X = - (2\text{'s complement of } 11101101) = - 00010011.$$





# SUBTRACTION WITH COMPLEMENTS

- Subtraction of unsigned numbers can also be done by means of the  $(r - 1)$ 's complement.
- Remember that the  $(r - 1)$ 's complement is one less than the  $r$ 's complement.
- $10110010 - 10011111$

	10110010
1's complement of 10011111	<u>+01100000</u>
	100010010
End-around carry	<u>+</u> 1
Answer	00010011



# SUBTRACTION WITH COMPLEMENTS

- $10011111 - 10110010$

$$\begin{array}{r} 10011111 \\ 1\text{'s complement of } 10110010 \quad +01001101 \\ \hline \end{array} \quad \begin{array}{r} 10011111 \\ +01001101 \\ \hline 11101100 \end{array}$$

There is no  
end carry.

Therefore, the answer is

$$Y - X = - (1\text{'s complement of } 11101100) = - 00010011.$$



# SIGNED BINARY NUMBERS

- To represent negative integers, we need a notation for negative values.
- It is customary to represent the sign with a bit placed in the leftmost position of the number since binary digits.
- The convention is to make the **sign bit 0 for positive** and **1 for negative**.
- Example:

Signed-magnitude representation:	10001001
Signed-1's-complement representation:	11110110
Signed-2's-complement representation:	11110111



# SIGNED BINARY NUMBERS

## ARITHMETIC ADDITION

+ 5    00000101

+11    00001011

+16    00010000

+ 5    00000101

-11    11110101

-6    11111010

Discard

- 5    11111011

+11    00001011

+6    100000110

- 5    11111011

-11    11110101

-16    111110000

Discard



# SIGNED BINARY NUMBERS

## ARITHMETIC SUBTRACTION

- In 2's-complement form:

1. Take the 2's complement of the subtrahend (including the sign bit) and add it to the minuend (including sign bit).
2. A carry out of sign-bit position is discarded.



$$(\pm A) - (+B) = (\pm A) + (-B)$$

$$(\pm A) - (-B) = (\pm A) + (+B)$$

- Example:

$$(-6) - (-13) \quad \longrightarrow \quad (11111010 - 11110011)$$

$$\quad \longrightarrow \quad (11111010 + 00001101)$$

$$\quad \longrightarrow \quad 00000111 (+7)$$



# BINARY CODES

- Generally Two Types:

1. Alphanumeric Codes
2. Numeric Codes

- Alphanumeric Codes: ASCII Code (7-bit)  
EBCDIC Code (8-bit)
- Numeric Codes: Weighted and Un-Weighted
- Weighted Codes: 8-4-2-1, 2-4-2-1, 3-3-2-1, etc
- Un-Weighted Codes: Excess-3 and Gray Codes  
(**reflected binary code (RBC)**)



# BINARY CODE: BCD CODE

- A number with  $k$  decimal digits will require  $4k$  bits in BCD.
- A decimal number in BCD is the same as its equivalent binary number only when the number is between 0 and 9.

**Table 1.4**  
*Binary-Coded Decimal (BCD)*

Decimal Symbol	BCD Digit
0	0000
1	0001
2	0010
3	0011
4	0100
5	0101
6	0110
7	0111
8	1000
9	1001



# BINARY CODE: BCD CODE

- BCD Addition

4	0100	4	0100	8	1000
<u>+5</u>	<u>+0101</u>	<u>+8</u>	<u>+1000</u>	<u>+9</u>	<u>+1001</u>
9	1001	12	1100	17	10001
			<u>+0110</u>		<u>+0110</u>
			10010		10111





# BINARY CODE: BCD CODE

- Example:

- Consider the addition of  $184 + 576 = 760$  in BCD:

BCD	1	1		
	0001	1000	0100	184
	<u>+ 0101</u>	<u>0111</u>	<u>0110</u>	+576
Binary sum	0111	10000	1010	
Add 6	<u>        </u>	<u>0110</u>	<u>0110</u>	<u>        </u>
BCD sum	0111	0110	0000	760



## BINARY CODE: BCD CODE

- Decimal Arithmetic:  $(+375) + (-240) = +135$

0	375
<u>+9</u>	<u>760</u>
0	135



# BINARY CODES

## OTHER DECIMAL CODES

**Table 1.5**  
*Four Different Binary Codes for the Decimal Digits*

<b>Decimal Digit</b>	<b>BCD 8421</b>	<b>2421</b>	<b>Excess-3</b>	<b>8, 4, -2, -1</b>
0	0000	0000	0011	0000
1	0001	0001	0100	0111
2	0010	0010	0101	0110
3	0011	0011	0110	0101
4	0100	0100	0111	0100
5	0101	1011	1000	1011
6	0110	1100	1001	1010
7	0111	1101	1010	1001
8	1000	1110	1011	1000
9	1001	1111	1100	1111
Unused bit combi- nations	1010	0101	0000	0001
	1011	0110	0001	0010
	1100	0111	0010	0011
	1101	1000	1101	1100
	1110	1001	1110	1101
	1111	1010	1111	1110



# BINARY CODE: GRAY CODE

**Table 1.6**  
*Gray Code*

<b>Gray Code</b>	<b>Decimal Equivalent</b>
0000	0
0001	1
0011	2
0010	3
0110	4
0111	5
0101	6
0100	7
1100	8
1101	9
1111	10
1110	11
1010	12
1011	13
1001	14
1000	15



# BINARY CODES: ASCII CHARACTER CODE

- American Standard Code for Information Interchange (Refer to Table 1.7)
- A popular code used to represent information sent as character-based data.
- It uses 7-bits to represent:
  - 94 Graphic printing characters.
  - 34 Non-printing characters.
- Some non-printing characters are used for text format (e.g. BS = Backspace, CR = carriage return).
- Other non-printing characters are used for record marking and flow control (e.g. STX and ETX start and end text areas).



# BINARY CODES: ASCII CHARACTER CODE

**Table 1.7**

*American Standard Code for Information Interchange (ASCII)*

$b_4b_3b_2b_1$	$b_7b_6b_5$							
	000	001	010	011	100	101	110	111
0000	NUL	DLE	SP	0	@	P	`	p
0001	SOH	DC1	!	1	A	Q	a	q
0010	STX	DC2	“	2	B	R	b	r
0011	ETX	DC3	#	3	C	S	c	s
0100	EOT	DC4	\$	4	D	T	d	t
0101	ENQ	NAK	%	5	E	U	e	u
0110	ACK	SYN	&	6	F	V	f	v
0111	BEL	ETB	‘	7	G	W	g	w
1000	BS	CAN	(	8	H	X	h	x
1001	HT	EM	)	9	I	Y	i	y
1010	LF	SUB	*	:	J	Z	j	z
1011	VT	ESC	+	;	K	[	k	{
1100	FF	FS	,	<	L	\	l	
1101	CR	GS	—	=	M	]	m	}
1110	SO	RS	.	>	N	^	n	~
1111	SI	US	/	?	O	—	o	DEL



# BINARY CODES: ERROR-DETECTING CODE

- To detect errors in data communication and processing, an eighth bit is sometimes added to the ASCII character to indicate its parity.
- A **parity bit** is an extra bit included with a message to make the total number of 1's either even or odd.
- Example:
  - Consider the following two characters and their even and odd parity:

	With even parity	With odd parity
ASCII A = 1000001	01000001	11000001
ASCII T = 1010100	11010100	01010100



# LOGIC GATES


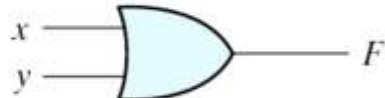


Name	Graphic symbol	Algebraic function	Truth table															
AND		$F = xy$	<table><tr><th><math>x</math></th><th><math>y</math></th><th><math>F</math></th></tr><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></table>	$x$	$y$	$F$	0	0	0	0	1	0	1	0	0	1	1	1
$x$	$y$	$F$																
0	0	0																
0	1	0																
1	0	0																
1	1	1																
OR		$F = x + y$	<table><tr><th><math>x</math></th><th><math>y</math></th><th><math>F</math></th></tr><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></table>	$x$	$y$	$F$	0	0	0	0	1	1	1	0	1	1	1	1
$x$	$y$	$F$																
0	0	0																
0	1	1																
1	0	1																
1	1	1																
Inverter		$F = x'$	<table><tr><th><math>x</math></th><th><math>F</math></th></tr><tr><td>0</td><td>1</td></tr><tr><td>1</td><td>0</td></tr></table>	$x$	$F$	0	1	1	0									
$x$	$F$																	
0	1																	
1	0																	
Buffer		$F = x$	<table><tr><th><math>x</math></th><th><math>F</math></th></tr><tr><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td></tr></table>	$x$	$F$	0	0	1	1									
$x$	$F$																	
0	0																	
1	1																	

Figure 2.5 Digital logic gates





# LOGIC GATES

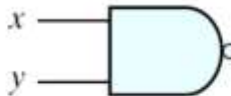

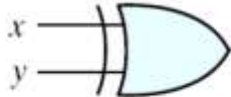
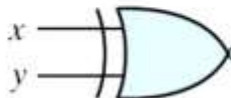
NAND		$F = (xy)'$	<table><tr><th><math>x</math></th><th><math>y</math></th><th><math>F</math></th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	$x$	$y$	$F$	0	0	1	0	1	1	1	0	1	1	1	0
$x$	$y$	$F$																
0	0	1																
0	1	1																
1	0	1																
1	1	0																
NOR		$F = (x + y)'$	<table><tr><th><math>x</math></th><th><math>y</math></th><th><math>F</math></th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	$x$	$y$	$F$	0	0	1	0	1	0	1	0	0	1	1	0
$x$	$y$	$F$																
0	0	1																
0	1	0																
1	0	0																
1	1	0																
Exclusive-OR (XOR)		$F = xy' + x'y$ $= x \oplus y$	<table><tr><th><math>x</math></th><th><math>y</math></th><th><math>F</math></th></tr><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	$x$	$y$	$F$	0	0	0	0	1	1	1	0	1	1	1	0
$x$	$y$	$F$																
0	0	0																
0	1	1																
1	0	1																
1	1	0																
Exclusive-NOR or equivalence		$F = xy + x'y'$ $= (x \oplus y)'$	<table><tr><th><math>x</math></th><th><math>y</math></th><th><math>F</math></th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></table>	$x$	$y$	$F$	0	0	1	0	1	0	1	0	0	1	1	1
$x$	$y$	$F$																
0	0	1																
0	1	0																
1	0	0																
1	1	1																

Figure 2.5 Digital logic gates



## PRACTICE:

### TRUTH TABLE TO BOOLEAN FUNCTION

$x$	$y$	$z$	$F$
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

$$\begin{aligned} F &= xyz + xyz' + x'yz + x'yz' \\ &= y \end{aligned}$$



# SYLLABUS

- Chapter 1 (Excluding Section 1.8)

