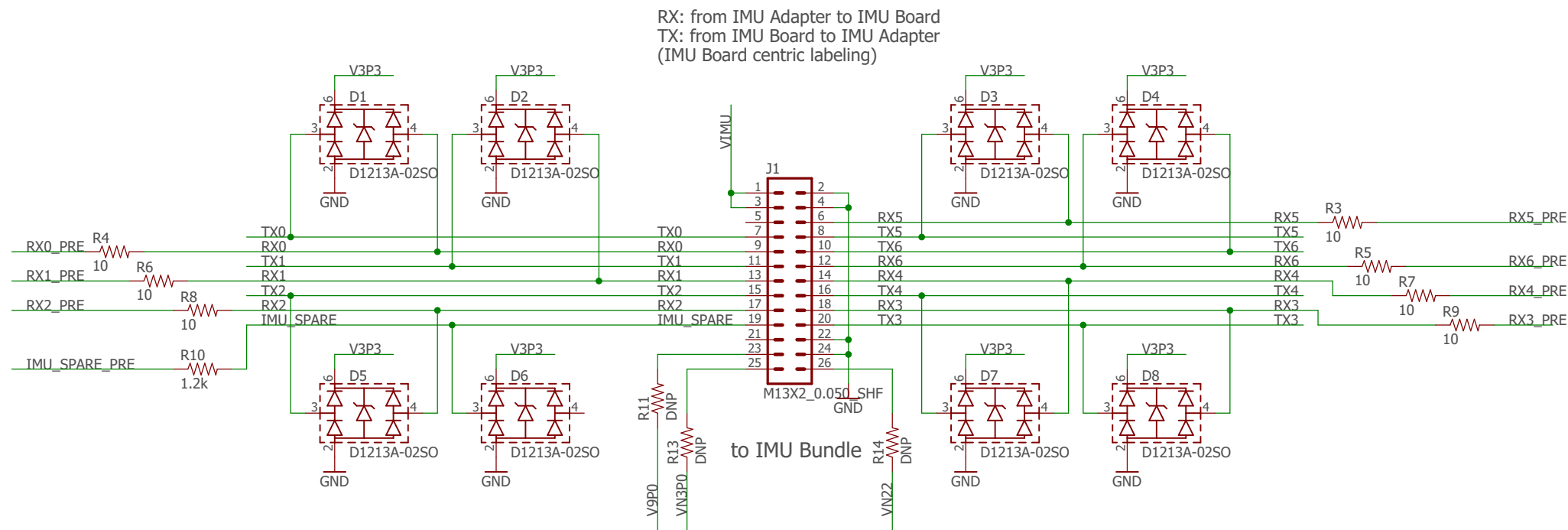
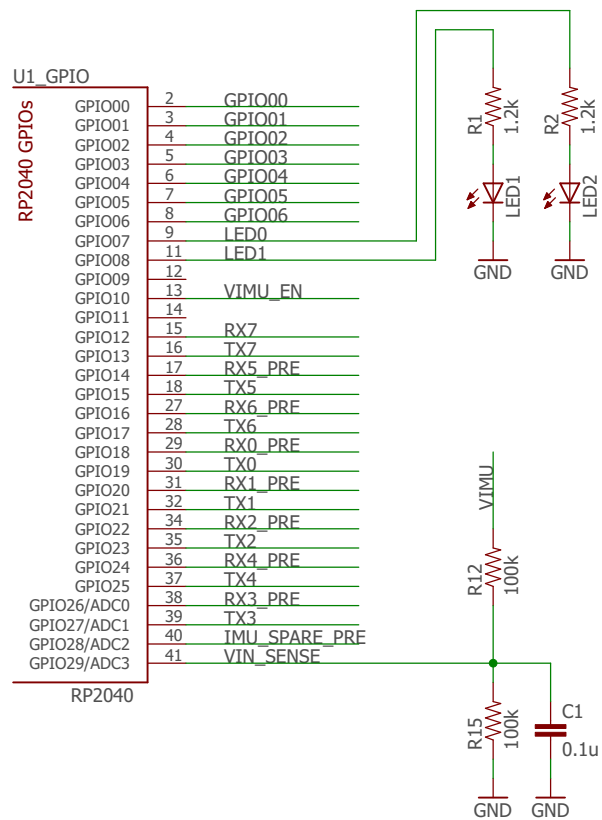
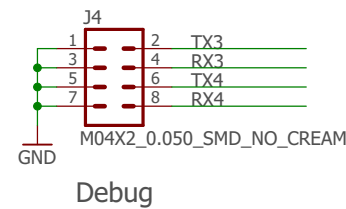
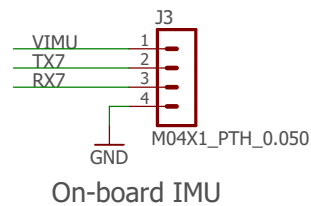
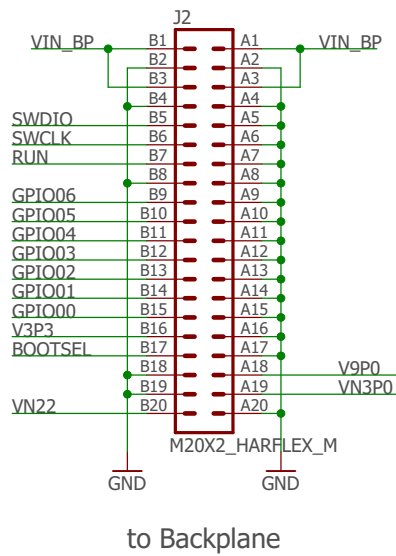


RP2040 / Backplane



Alt. Name (FPGA)
DETB_RUN
DETB_RESERVED
DETB_STATUS_SELECT
DETB_END_CYC
DETB_TRIGGER
DETB_SELECT
UART_FPGA_TO_DETB
UART_DETB_TO_FPGA



Voltage Regulators

