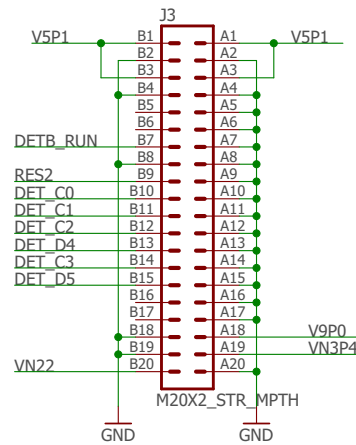
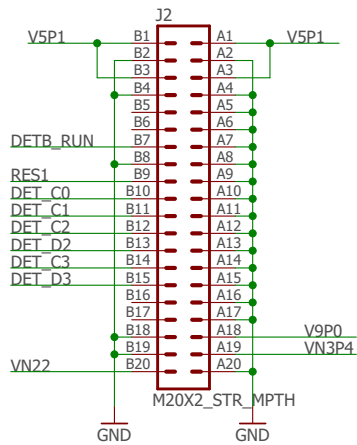
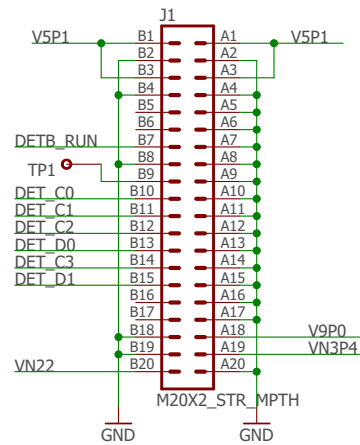


# Six Layer Detector Adapter Backplane

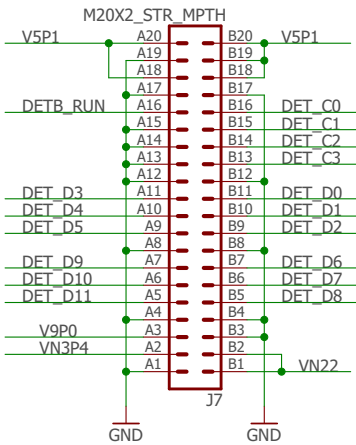
Alt. Name (FPGA)	Alt. Name (RP2040)
DETB_RUN	
DETB_RESERVED	GPIO06
DETB_STATUS_SELECT	GPIO05
DETB_END_CYC	GPIO04
DETB_TRIGGER	GPIO03
DETB_SELECT	GPIO02
UART_FPGA_TO_DETB	GPIO01
UART_DETB_TO_FPGA	GPIO00

from Detector Adapter Board

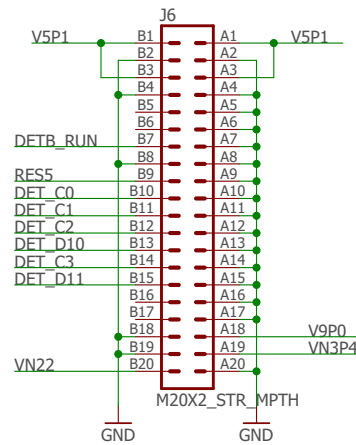
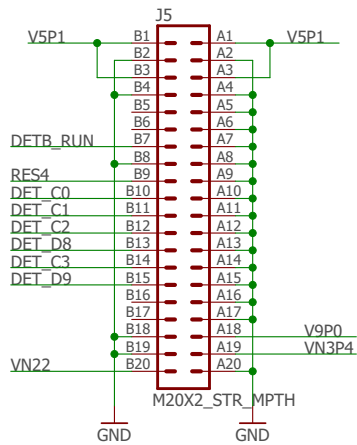
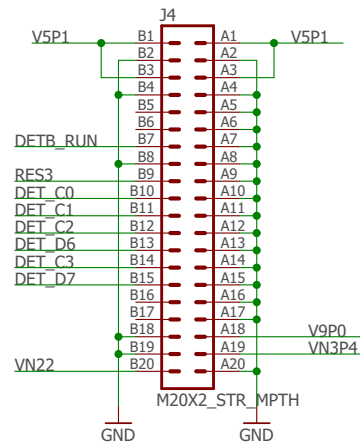


from Control Board

Alt. Name (FPGA)
DETB_RUN
UART_DETB_TO_FPGA1
DETB_SELECT2
UART_DETB_TO_FPGA2
UART_DETB_TO_FPGA4
DETB_SELECT5
UART_DETB_TO_FPGA5



Alt. Name (FPGA)
DETB_STATUS_SELECT
DETB_END_CYC
DETB_TRIGGER
UART_FPGA_TO_DETB
DETB_SELECT0
UART_DETB_TO_FPGA0
DETB_SELECT1
DETB_SELECT3
UART_DETB_TO_FPGA3
DETB_SELECT4



debugging / testing

