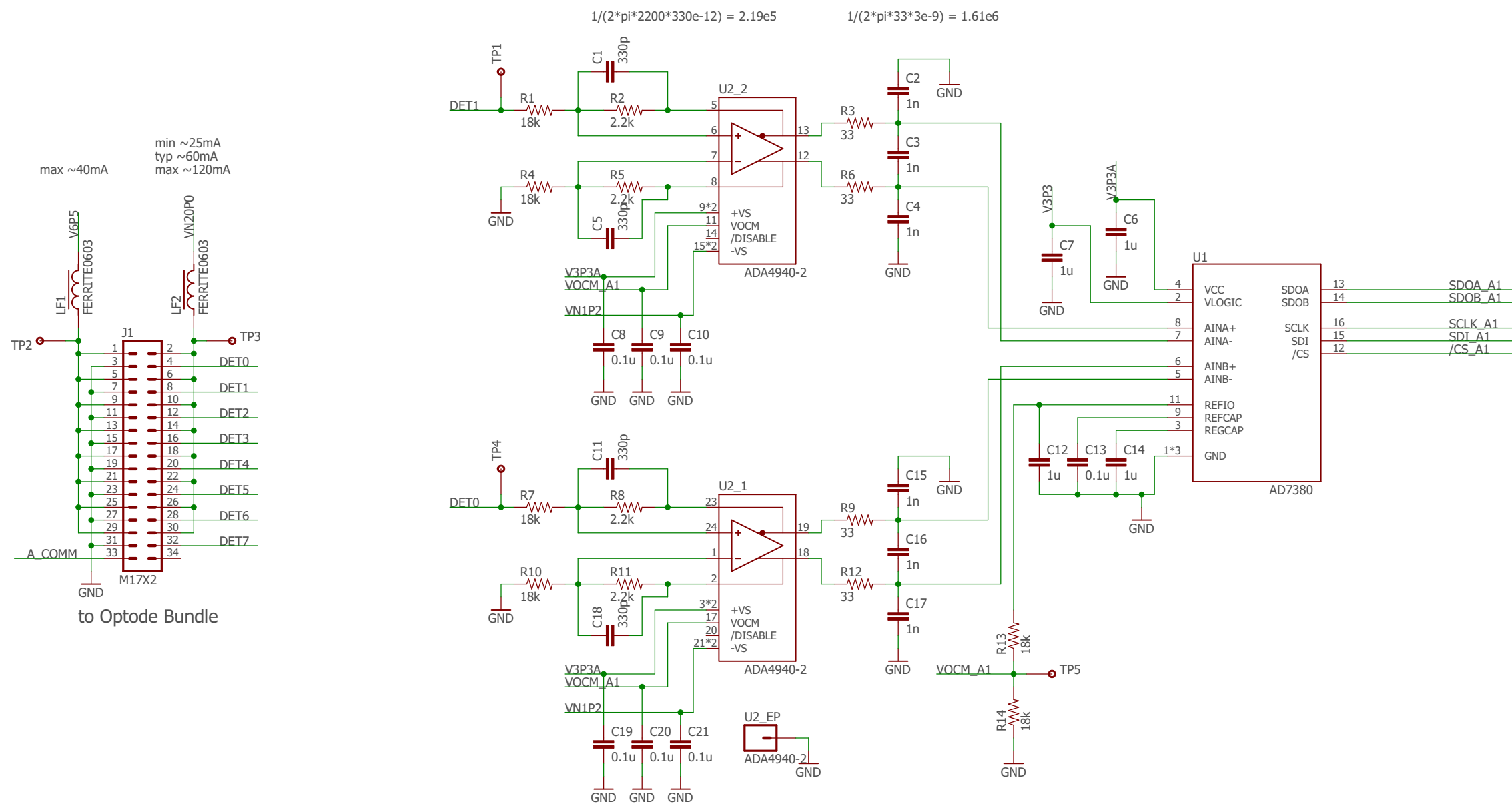
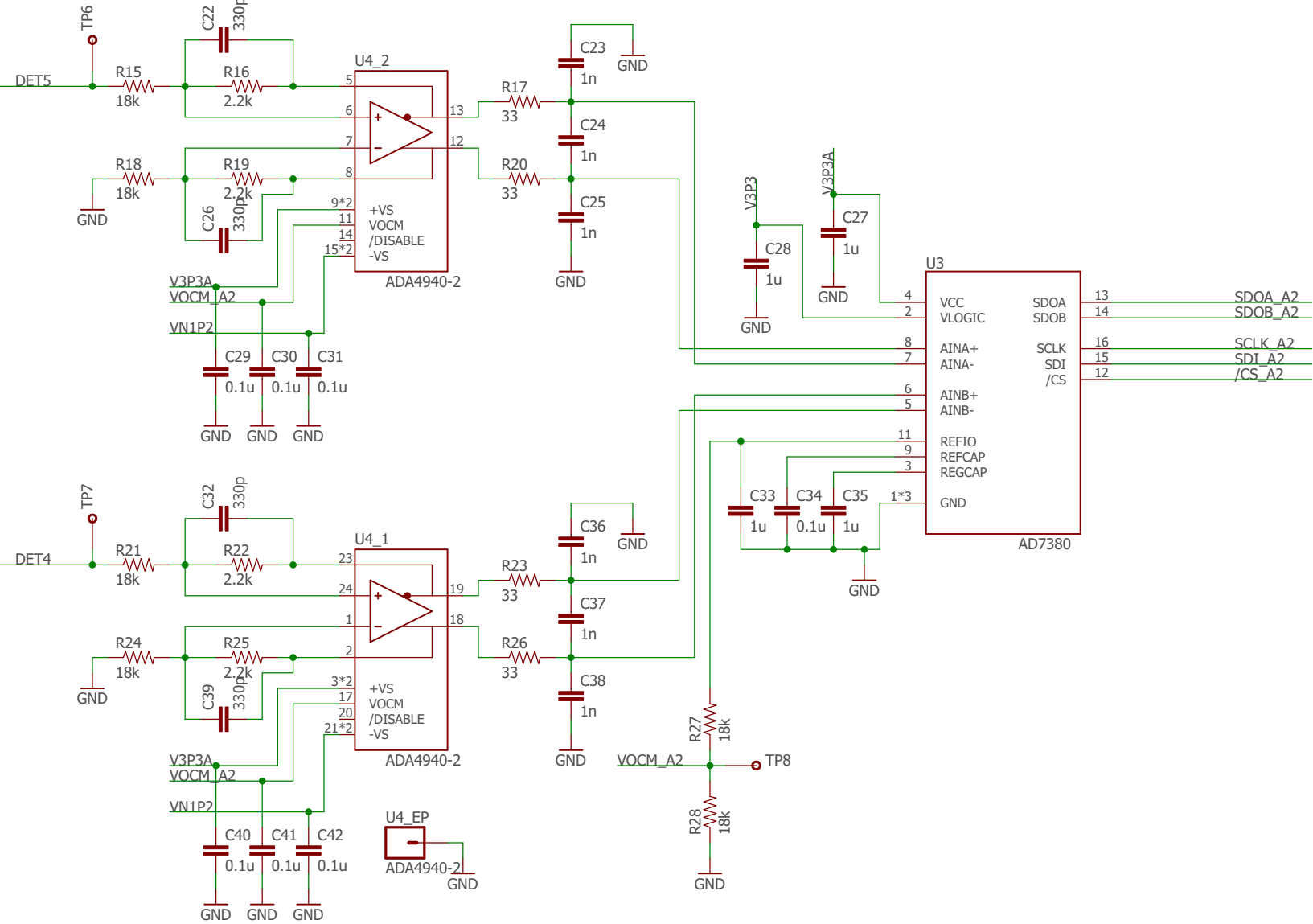


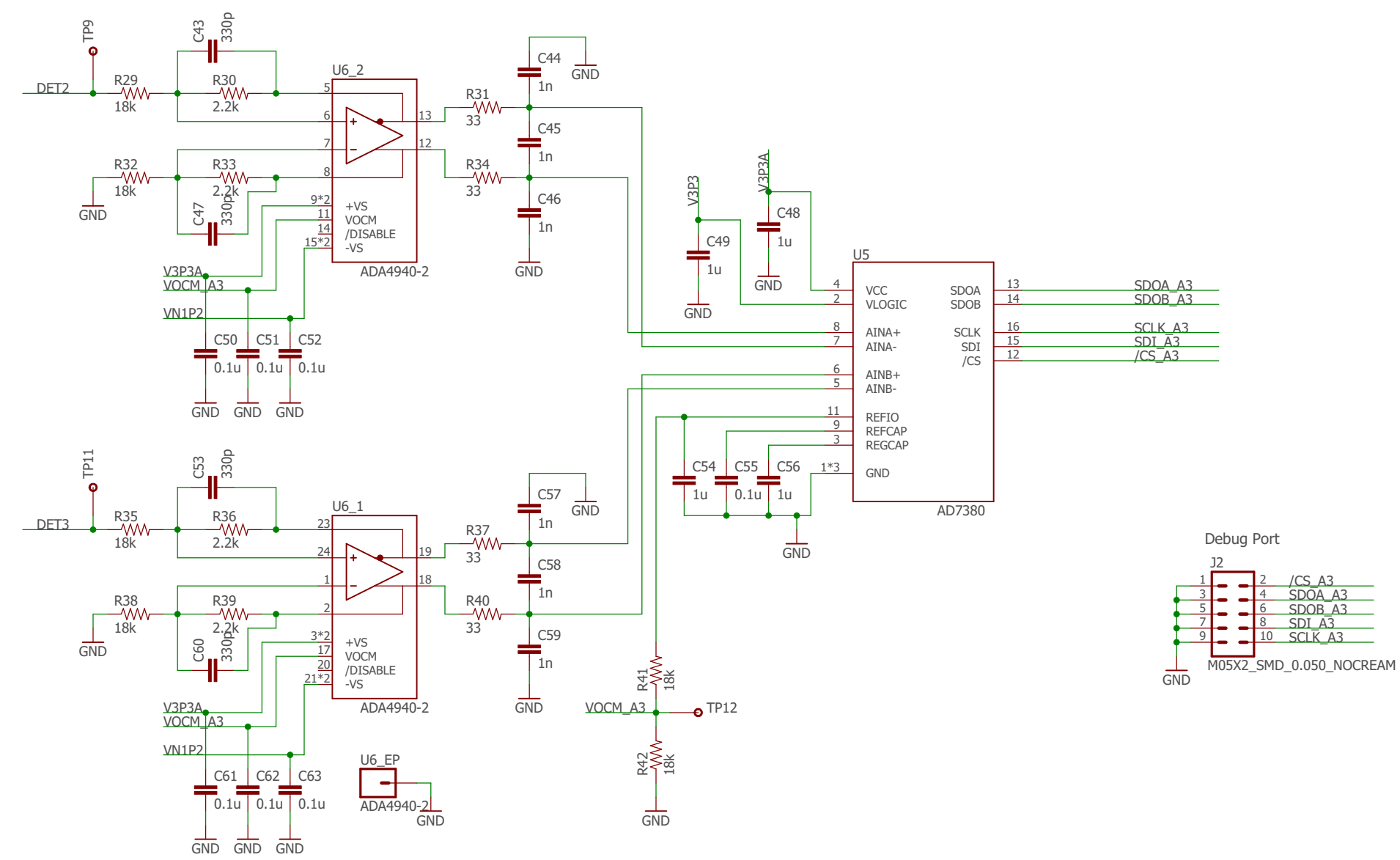
ADC A1 - Det0/1



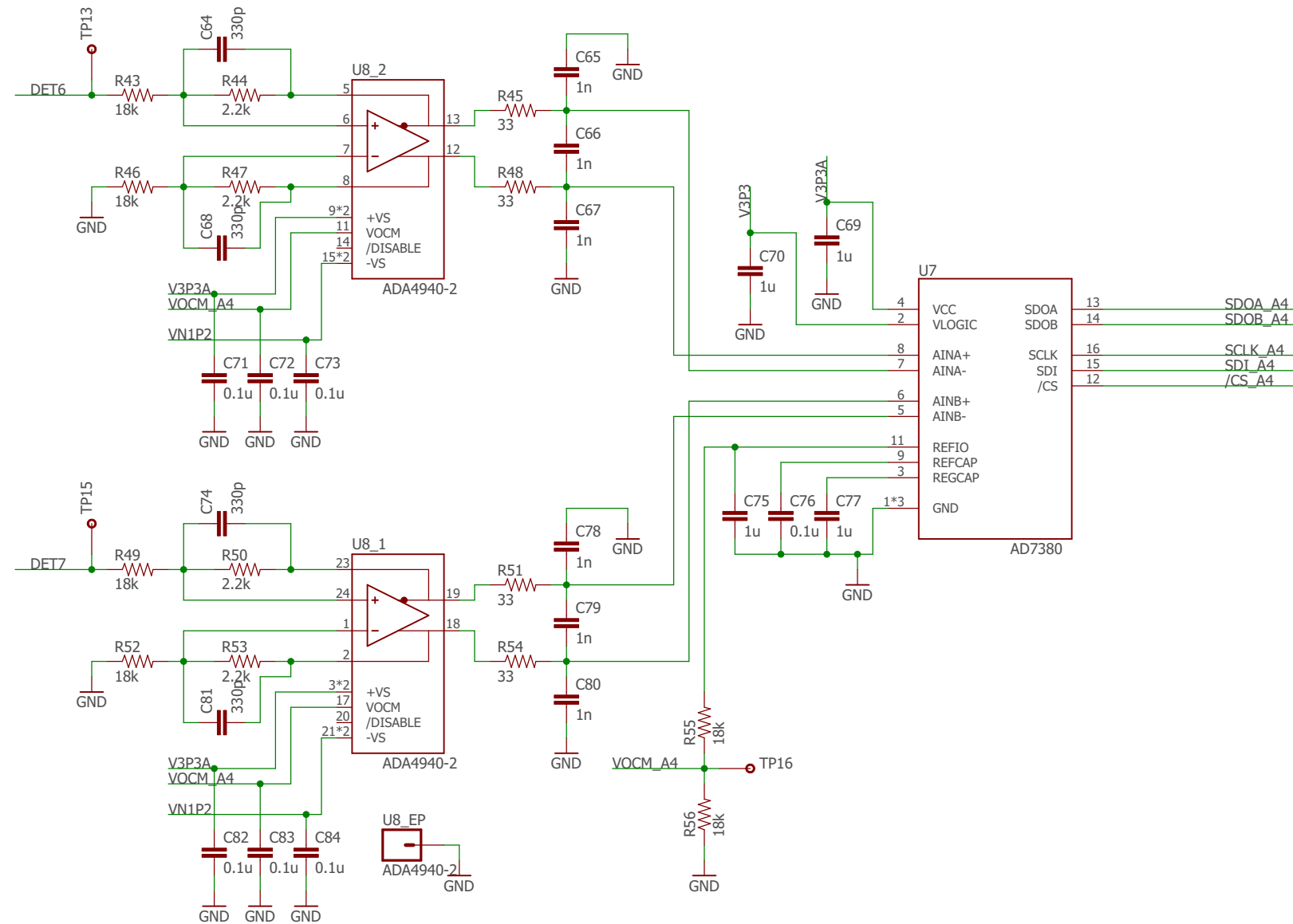
ADC A2 - Det4/5



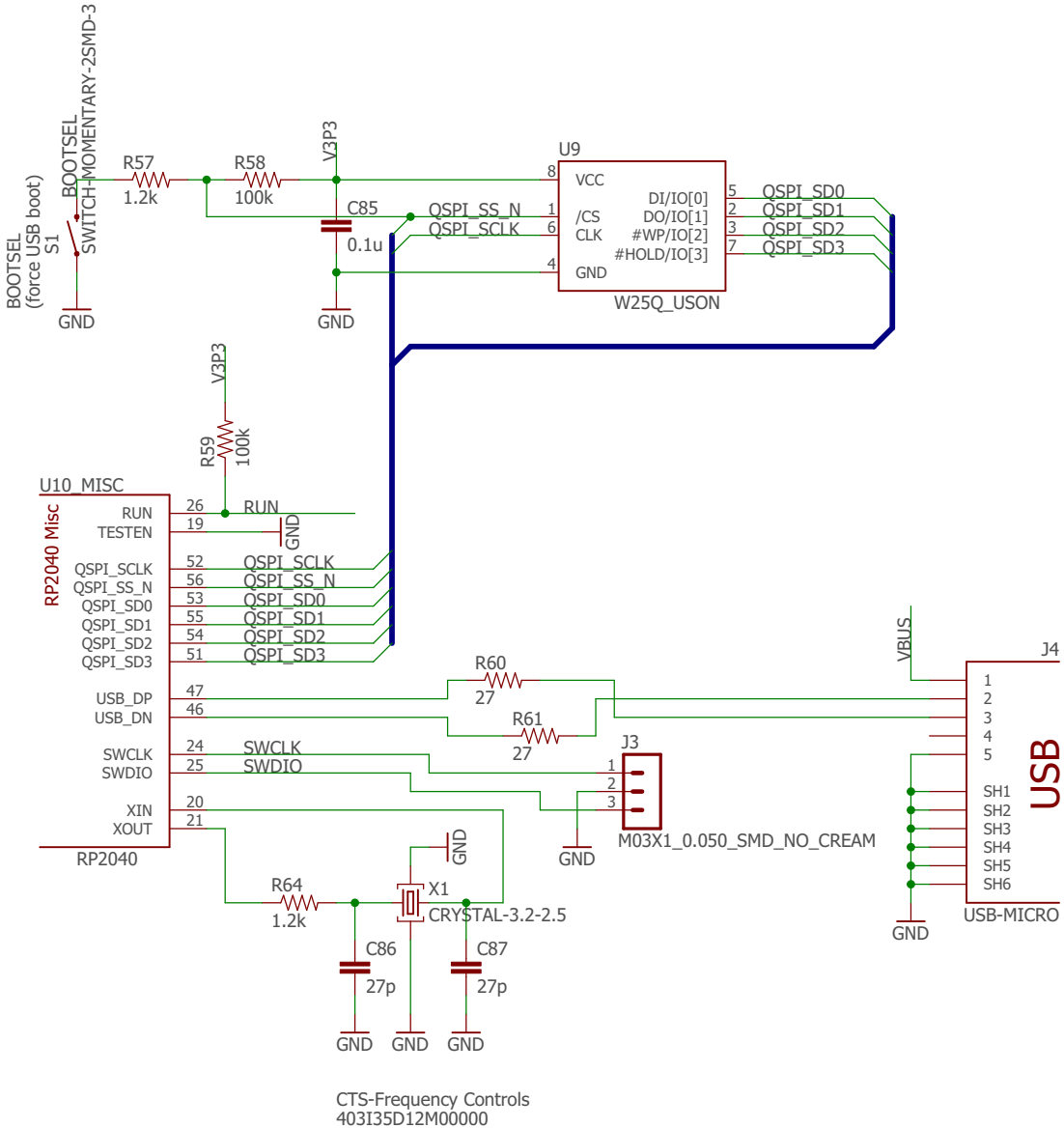
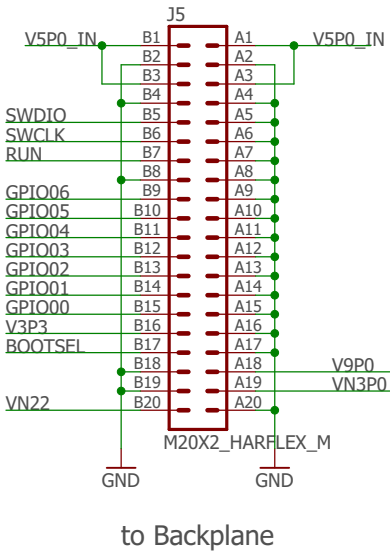
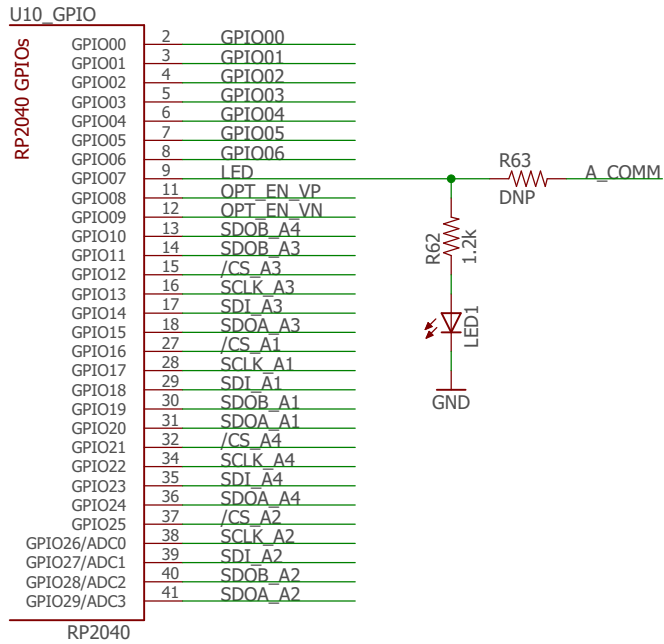
ADC A3 - Det2/3



ADC A4 - Det6/7



Auxillary Circuits / Backplane



Alt. Name (FPGA)
DET_B_RESERVED
DET_B_STATUS_SELECT
DET_B_END_CYC
DET_B_TRIGGER
DET_B_SELECT
UART_FPGA_TO_DET_B
UART_DET_B_TO_FPGA

Voltage Regulators

