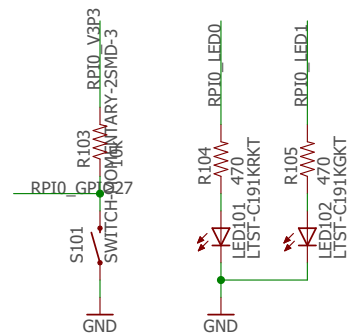
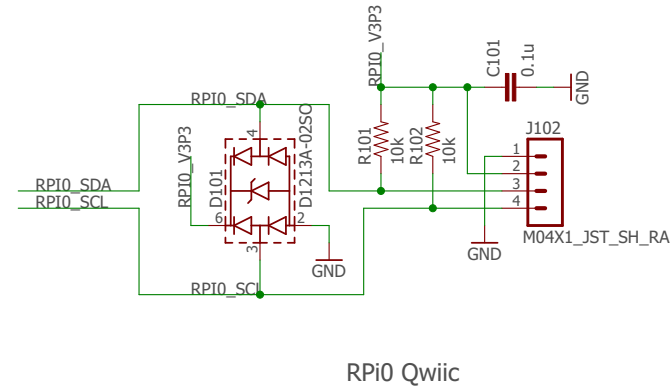
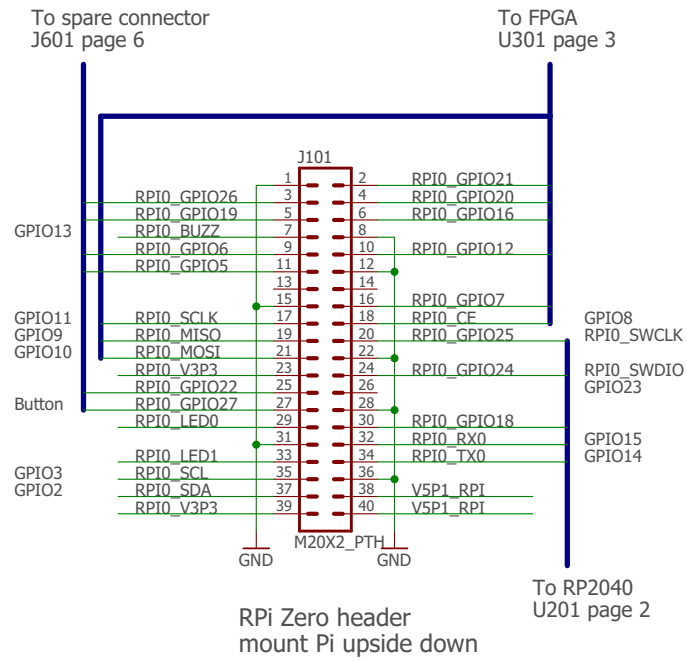
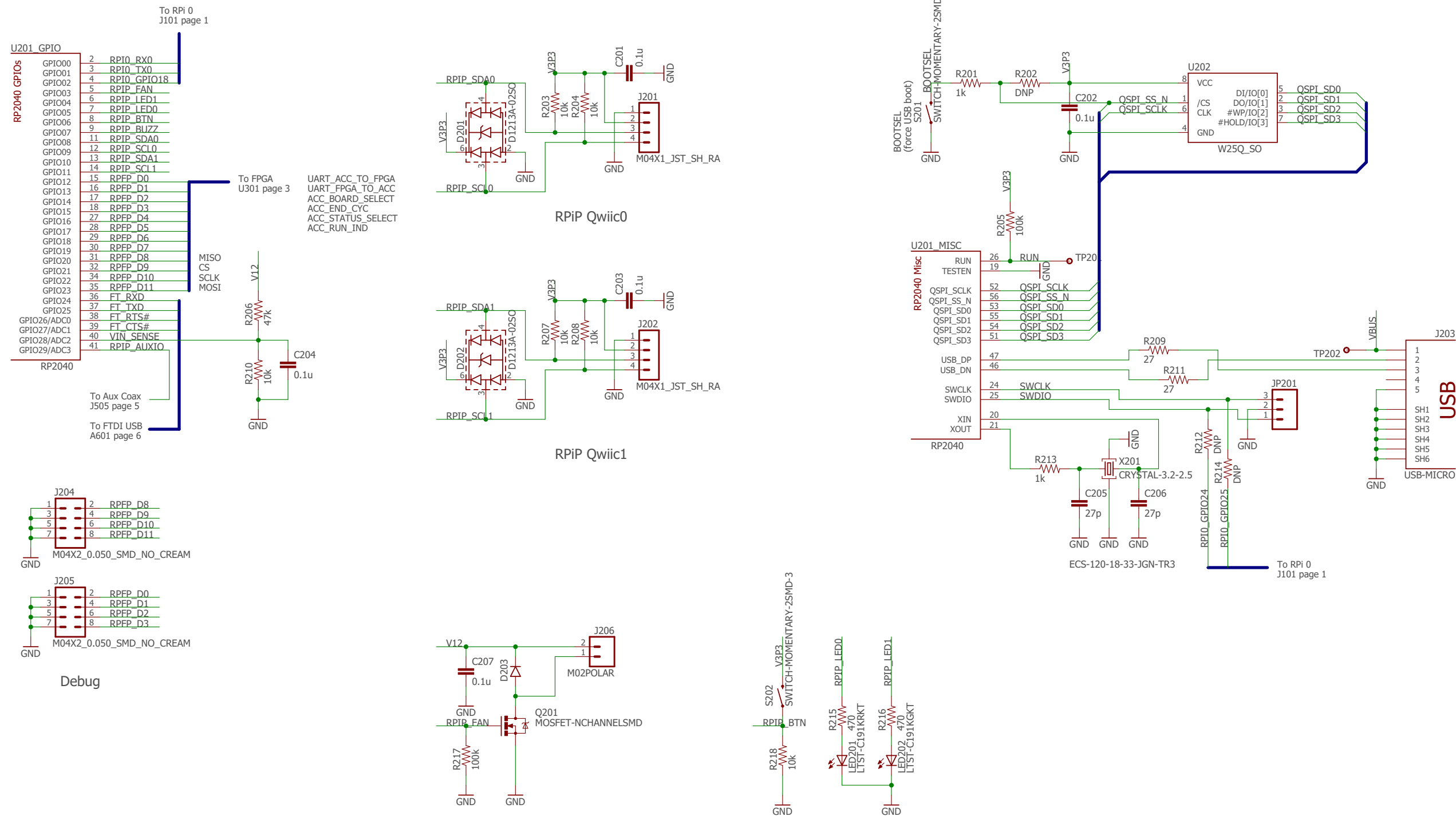


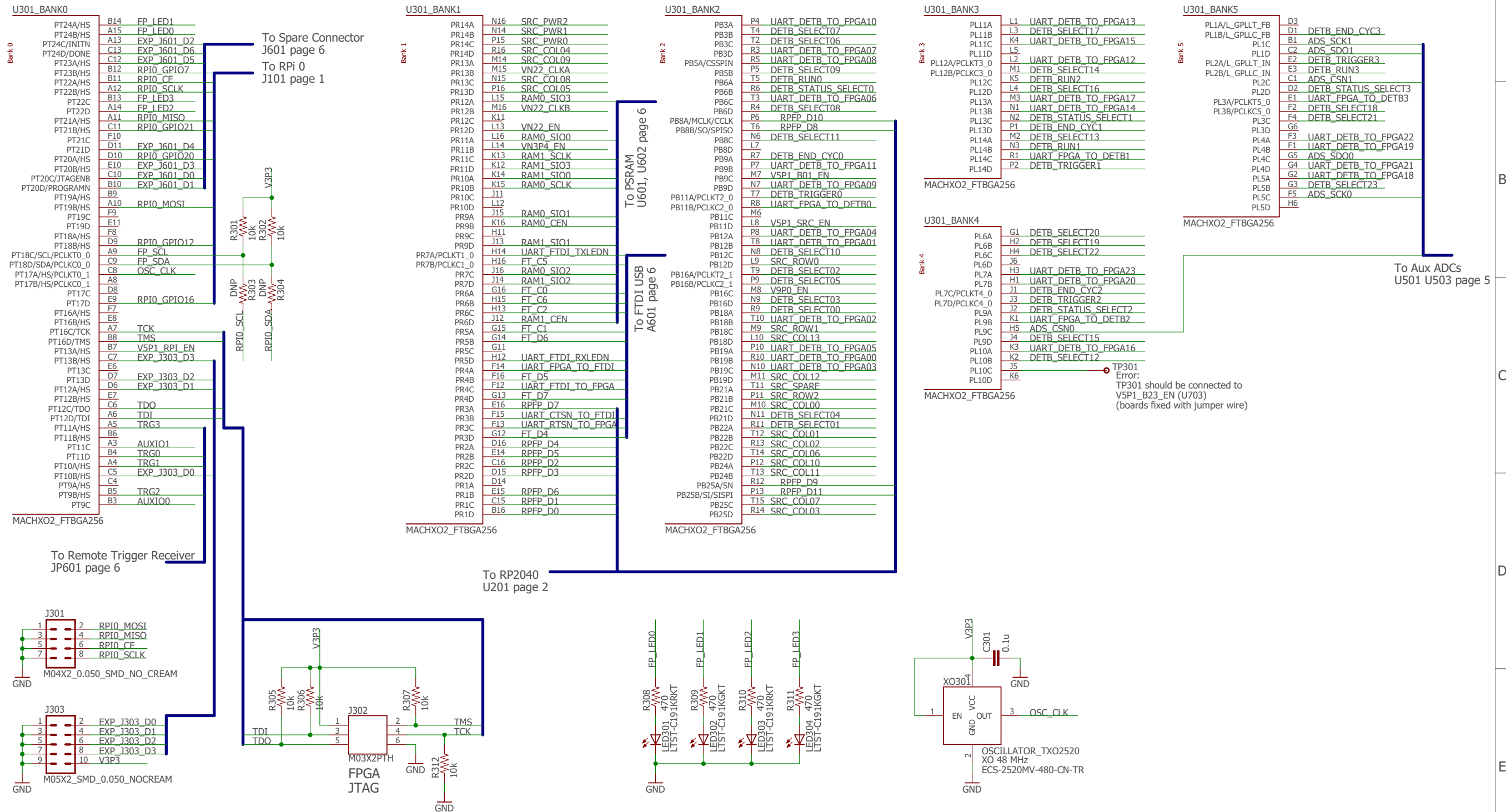
# RPi Zero 2 W



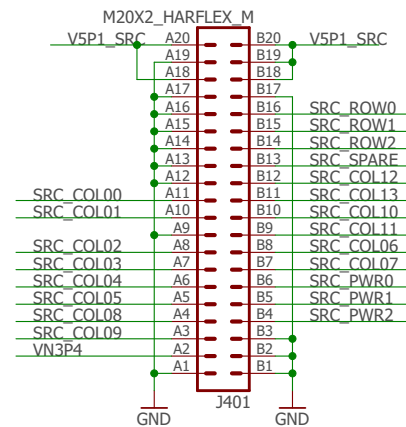
# RP2040 MCU (RPI Pico)



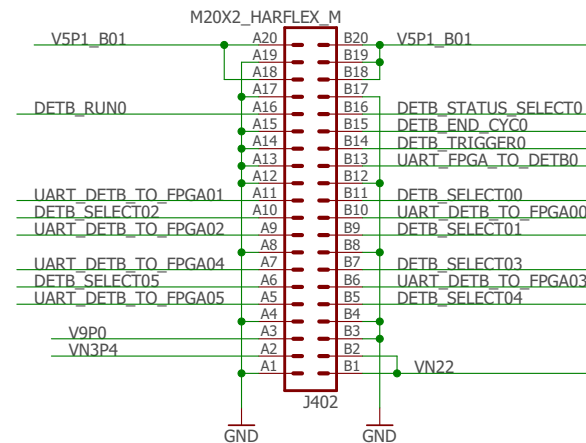
# FPGA



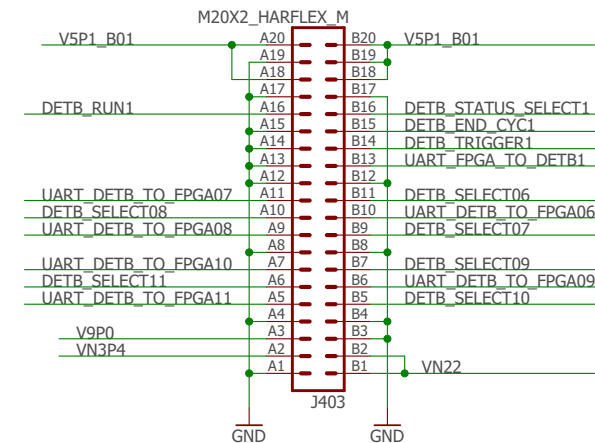
# Backplane Connectors



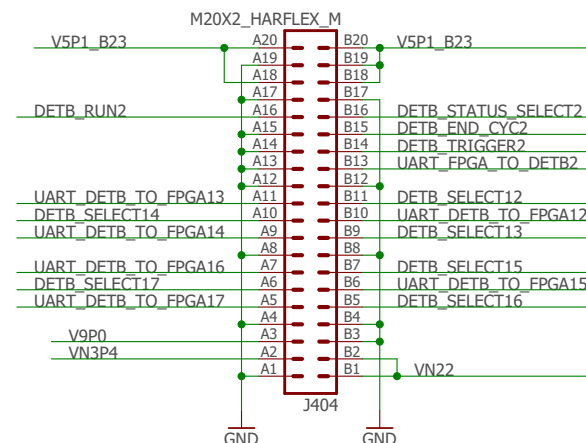
Source Backplane



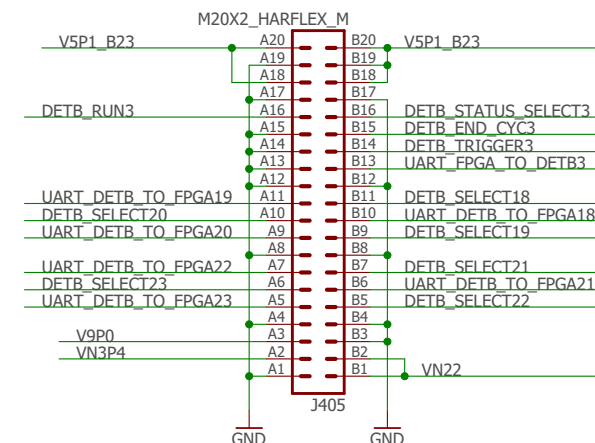
Detector Backplane B0



Detector Backplane B1

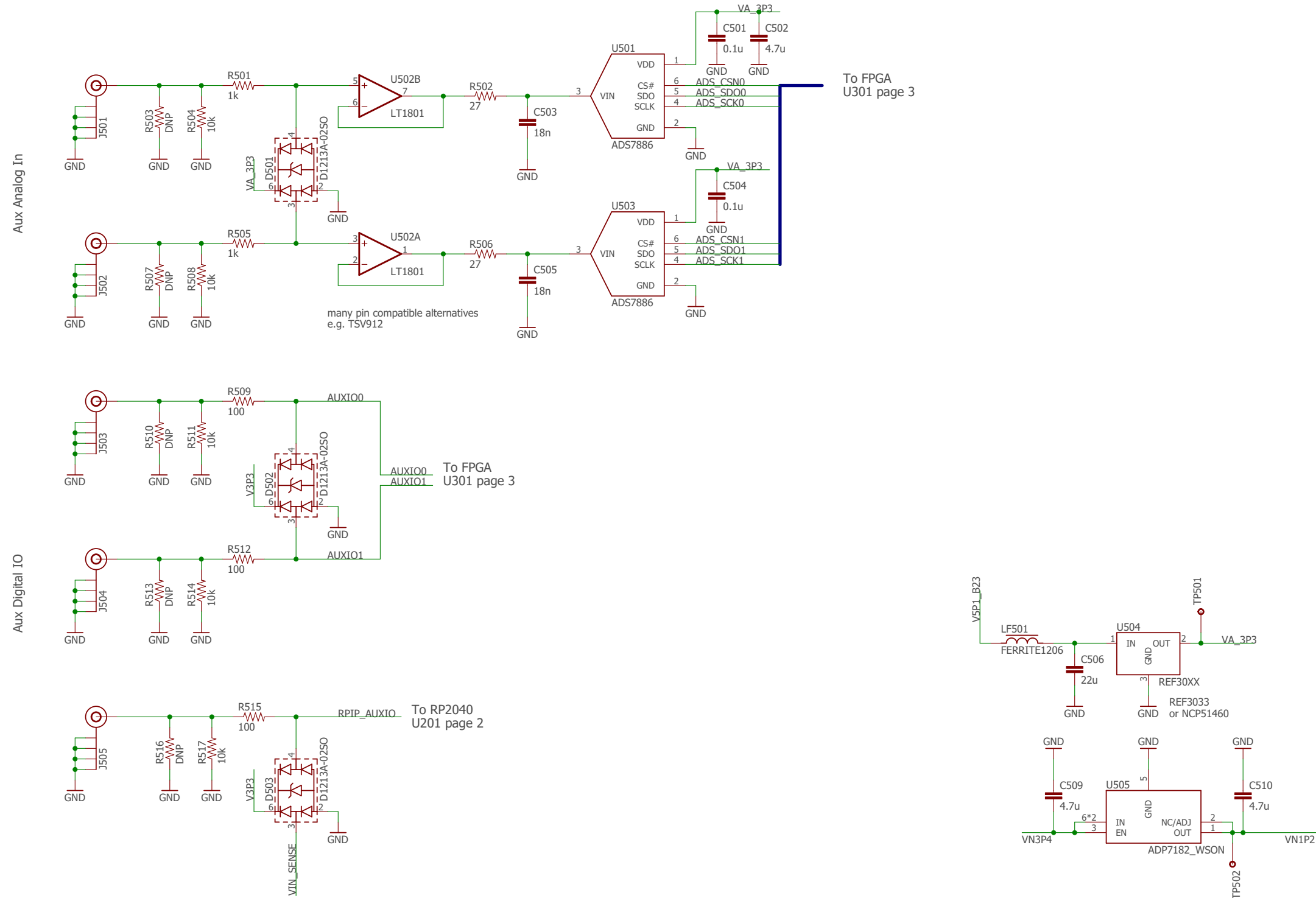


Detector Backplane B2

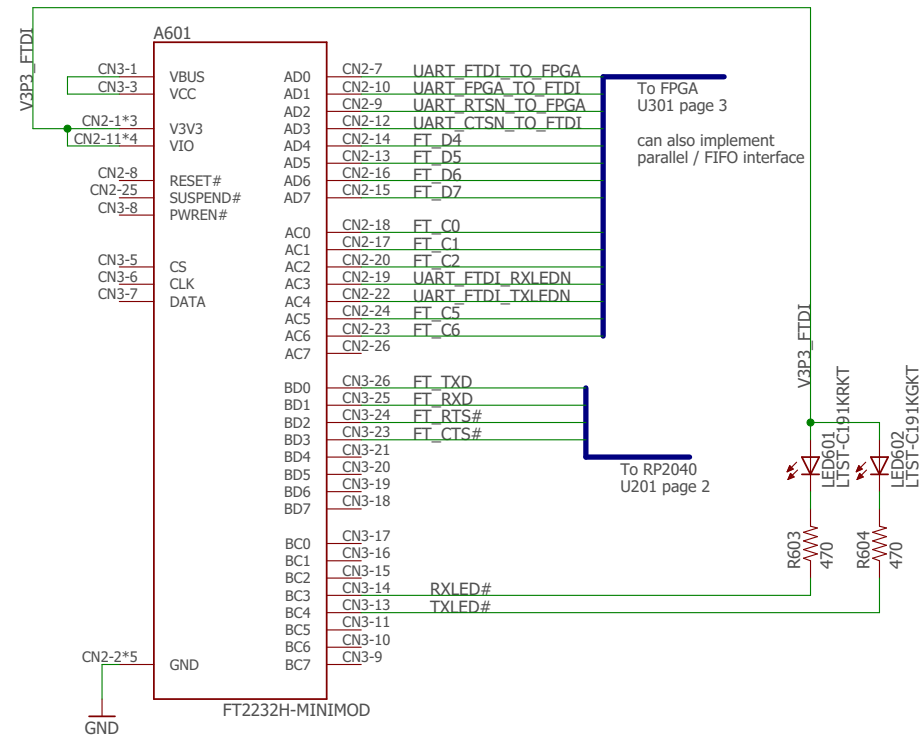


Detector Backplane B3

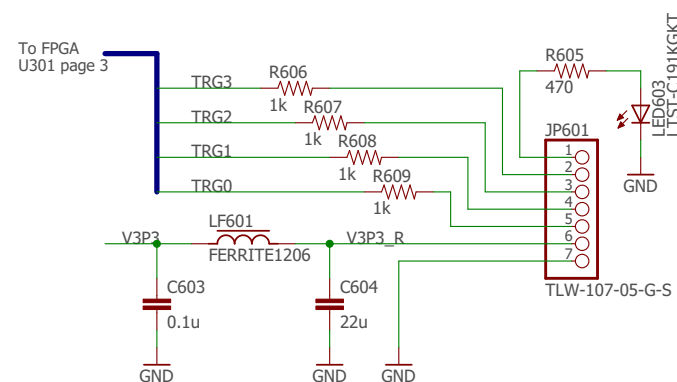
# Aux I/O



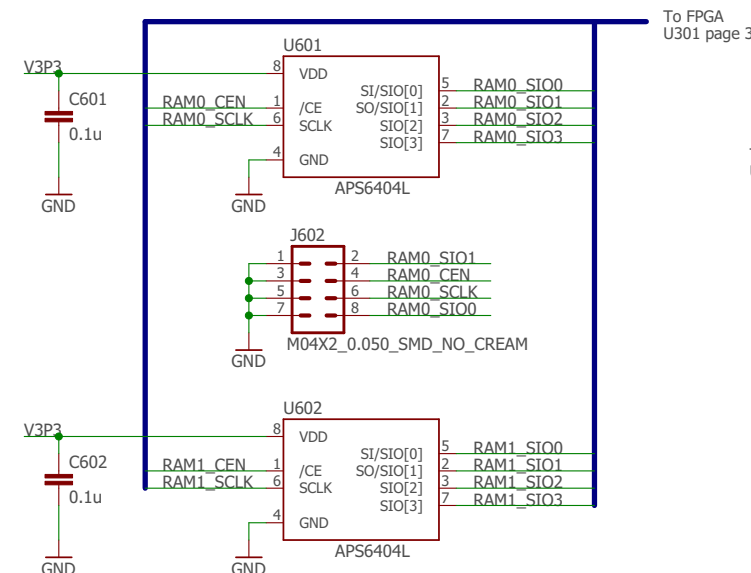
# Misc Circuits



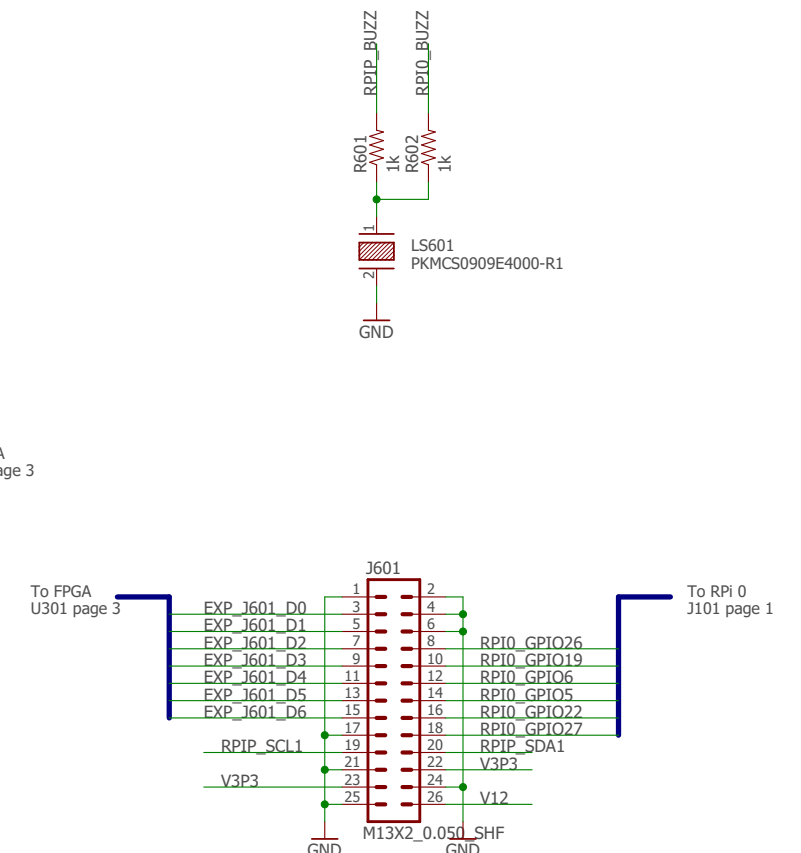
FTDI USB Interface



Remote Trigger Receiver (AvL / QiaChip)

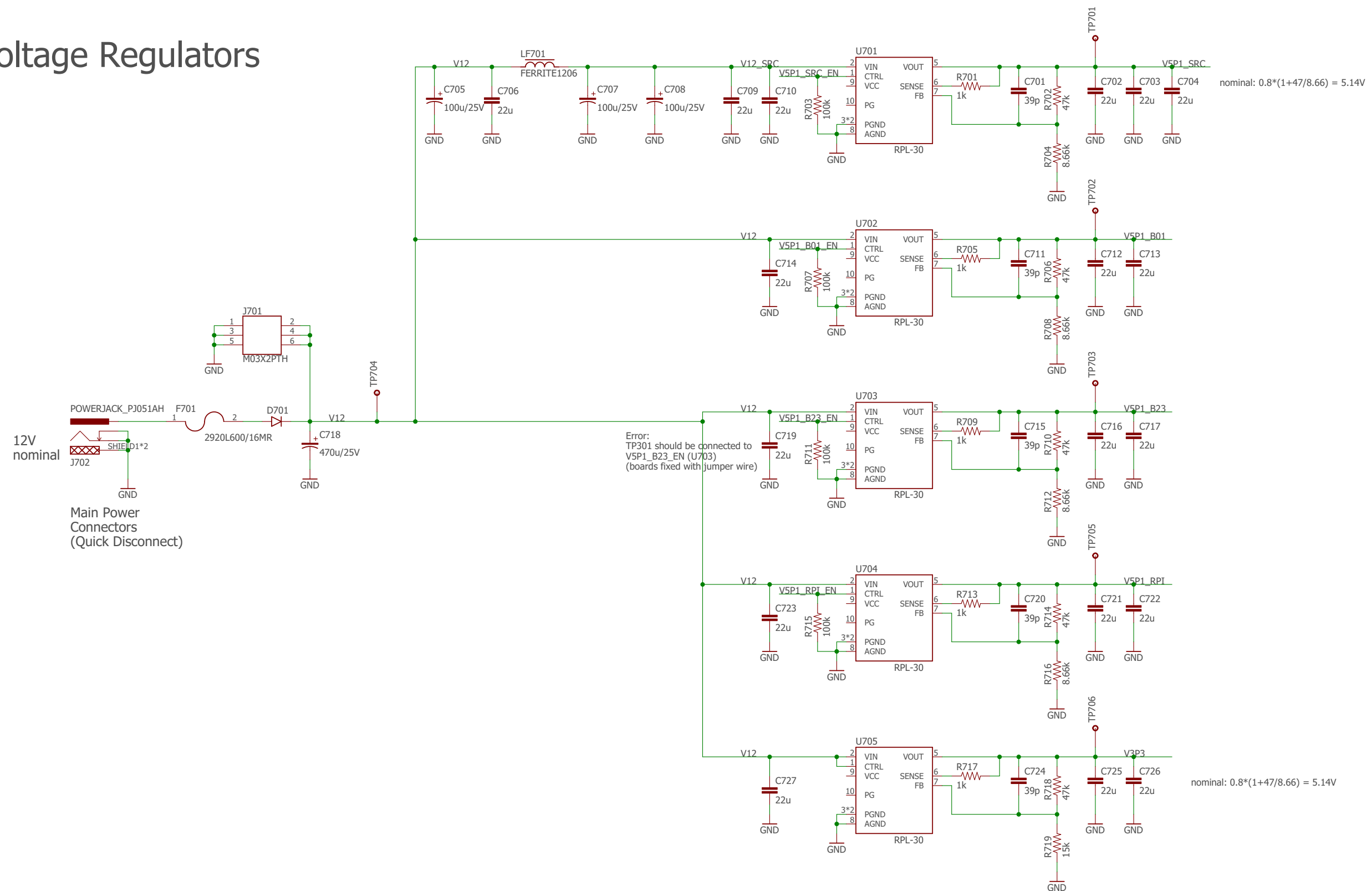


PSRAM with debug connector

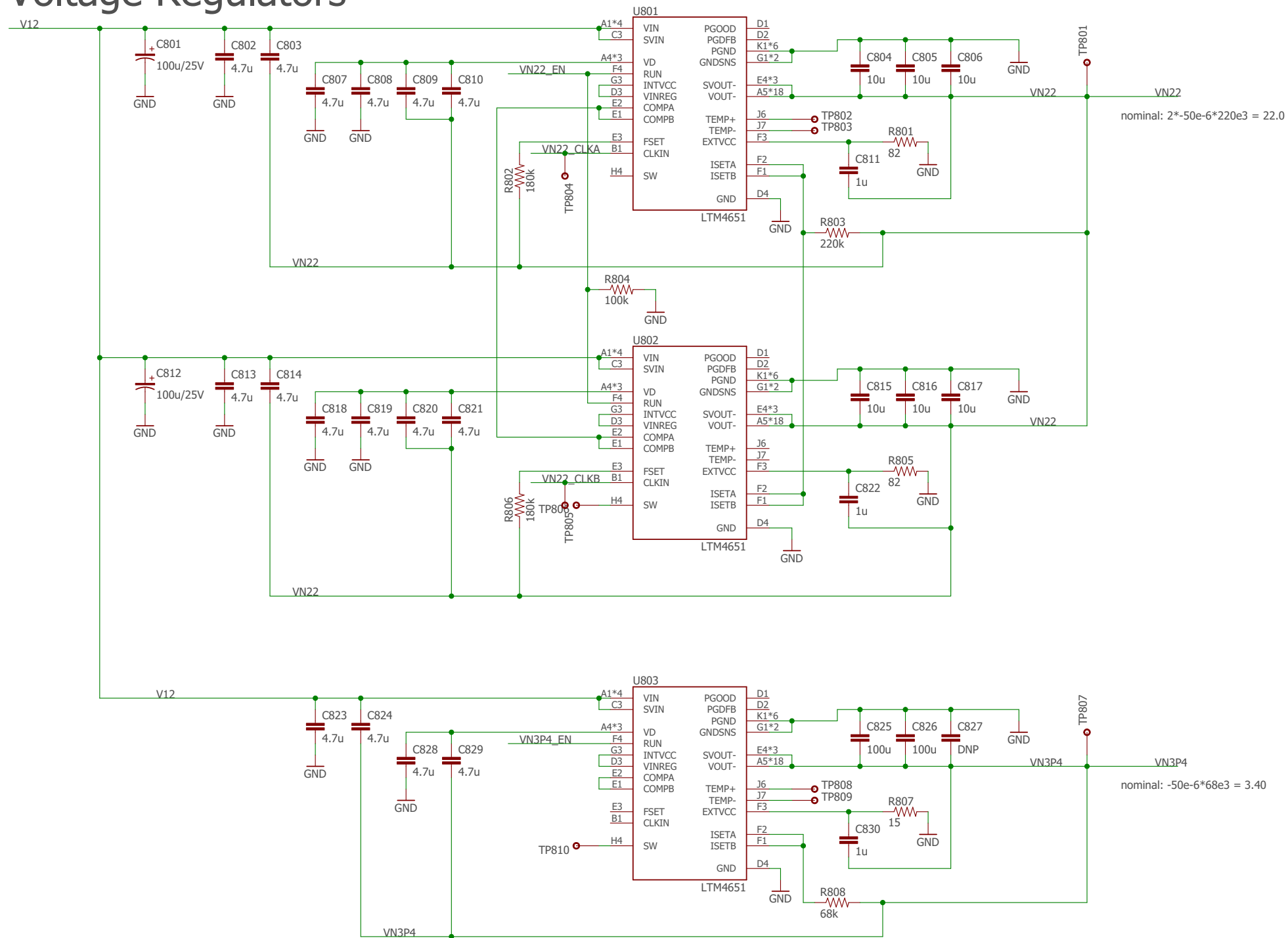


Spare

# Voltage Regulators

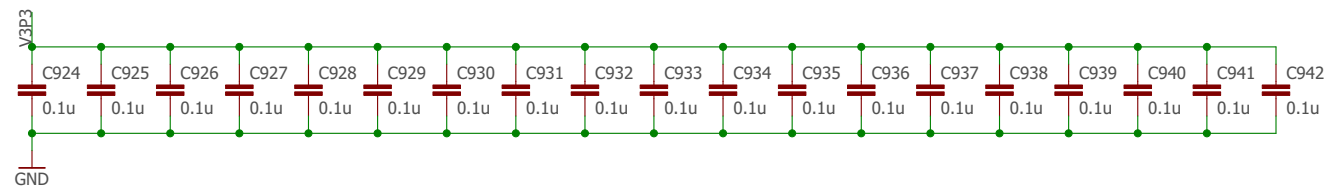
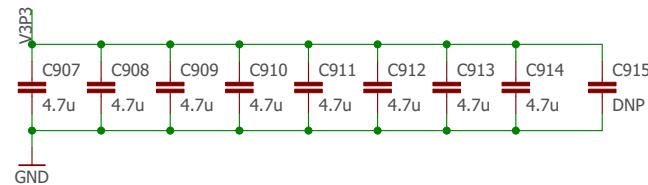
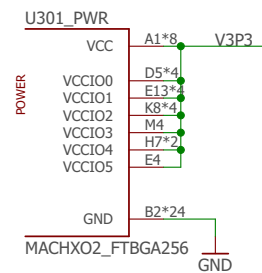
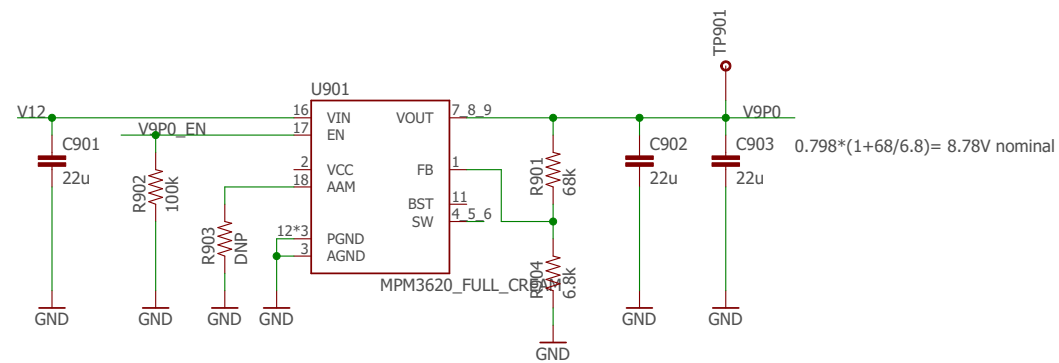


# Negative Voltage Regulators

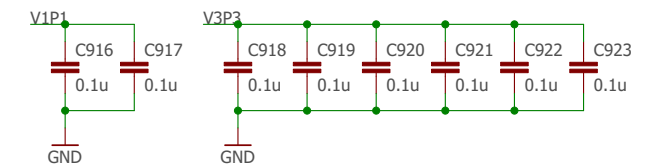
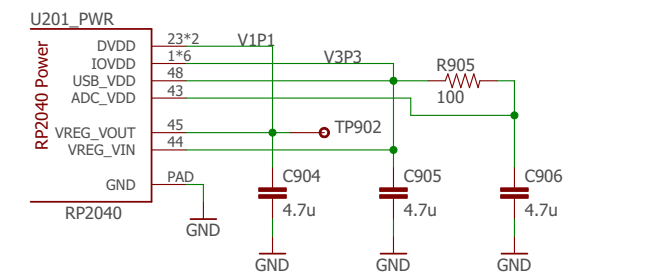




# Misc Power



FPGA Decoupling



MCU Decoupling

