

1. What is the purpose of the NVIC peripheral?
To handle interrupts, from enabling them to configuring priorities of the interrupts
2. What is the difference between interrupt tail-chaining and nesting?
Tail-chaining handles interrupts in succession based on priority while Nesting allows higher priority interrupts to be handled first, even if an interrupt is already being handled.
3. In what file are the CMSIS libraries that control the NVIC?
core_cm0.h file, after the peripheral structure and register definitions.
4. What is the purpose of the EXTI peripheral?
Allows non-peripheral sources to trigger interrupts.
5. What is the purpose of the SYSCFG pin multiplexers?
Controls which pins connect to the EXTI inputs, which only has 16 input lines.
6. What file has the defined names for interrupt numbers?
stm32f072xb.h file, in the IRQn_Type enumeration.
7. What file has the Vector table implementation?
startup_stm32f072xb.s