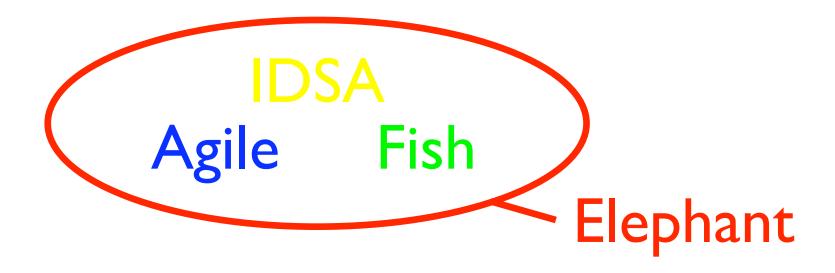
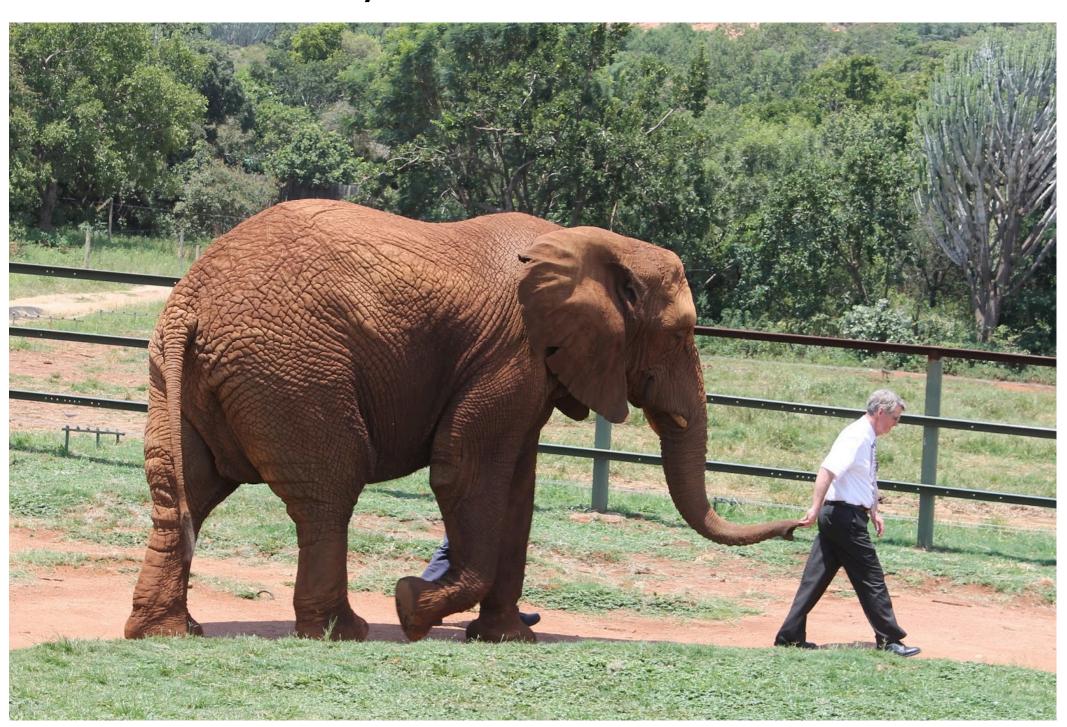
# The Elephant Supernova Code

ELEPHANT = ELEgant Parallel Hydrodynamics with Approximate Neutrino Transport



## University of Basel 2005-2015

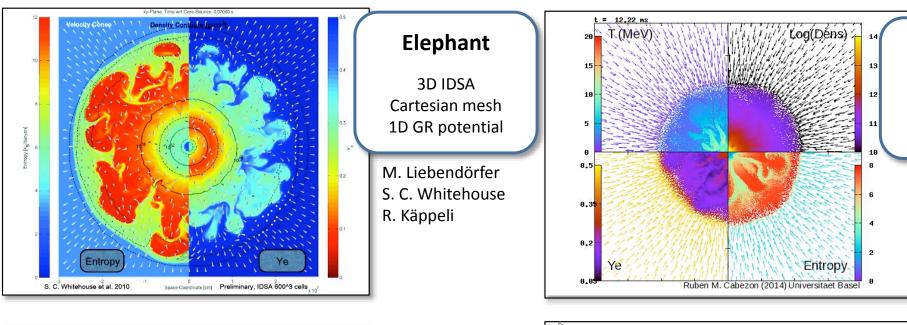


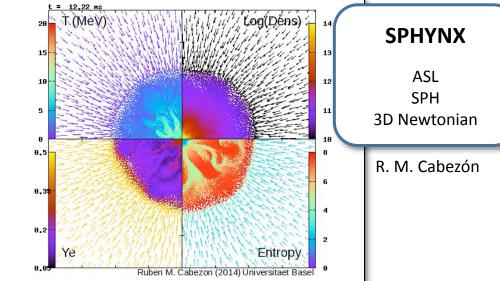
### EuroHack 2015

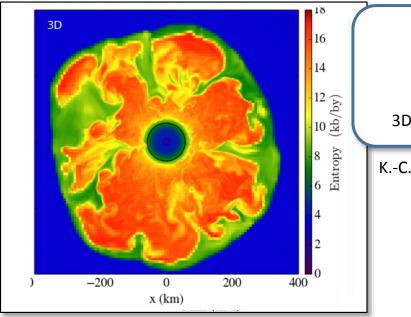


### Four independent 3D supernova codes





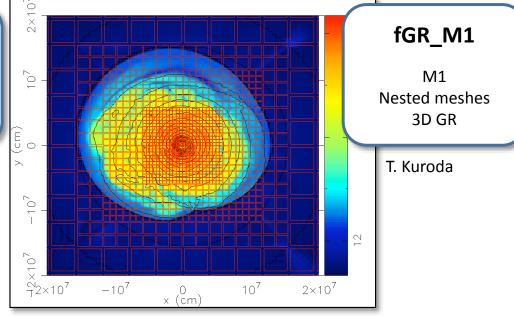


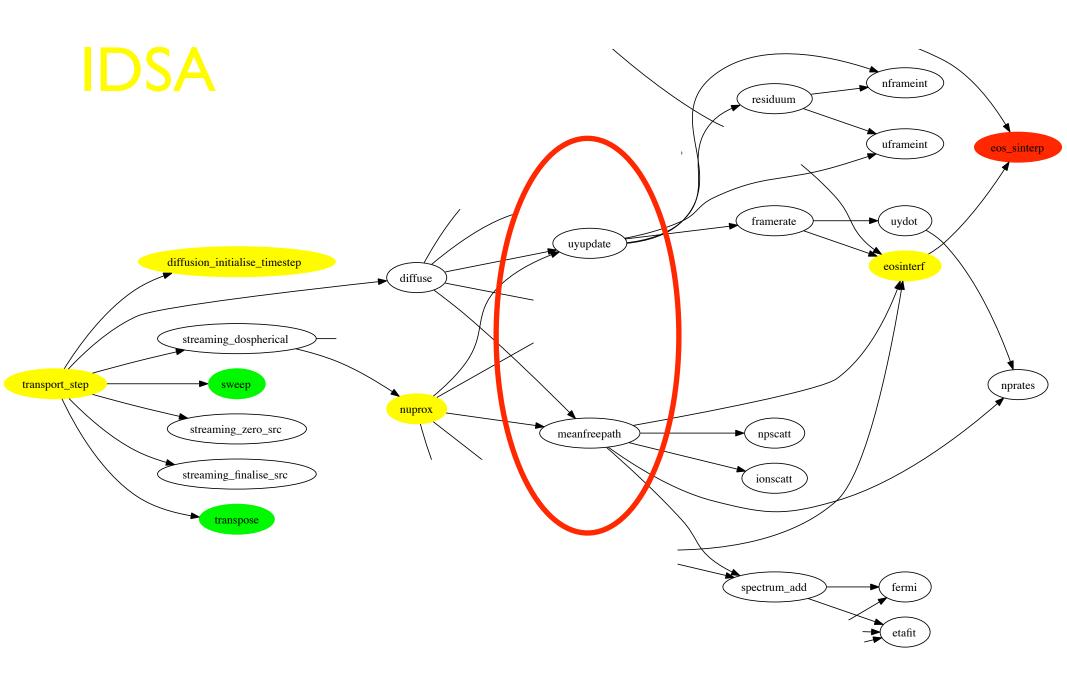


#### **FLASH**

3D IDSA **AMR** 3D Newtonian

K.-C. Pan







#### **Observation**

- ~90% of time spent in single-cell calls to meanfreepath()
   and uyupdate()
- Operations on cell i are logically complicated, but neither compute-intensive nor dependent on other cells.

### Goal

- Accelerate meanfreepath() and uyupdate() using GPU.
- Call with an array of cells instead of single cells.

#### **Problem**

• Many failed attempts to inline routines with -ipafrom=...



#### Solution

- Integrate independent idsa-subroutines into one idsa\_module.f90.
- Create new possibility to call with array arguments.

### Goal

 Compiler can inline all idsa-routines without special makefile.

#### **Problem**

- perftools do not work for our code.
- First run on GPU: Incorrect results! Race-condition?



### Solution

- Debugging with OpenMP instead of OpenACC. Race-condition found and fixed.
- perftools not compatible with -K trap=fp (thanks fgp!)

#### **Problem**

- Runs on GPU deliver correct results, but very slow.
   Huge data load time for equation of state table.
- We don't see what the GPU is doing.

#### Goal

- Load tables once at the beginning of the simulation.
- Investigate what the GPU is doing, speed up...



### Solution

- Learned about many environment variables and compile options to monitor compilation and code execution.
- nvvp viewer shows details about GPU usage.

### Goal

• Optimise the GPU usage for the cell arrays.

### **Problem**

- Copyin/out dominates computation.
- Hidden synchronisation with async(...) clause.



#### Solution

- Manage data transfer manually and split into streams to hide memory copies during calculations.
- No solution for hidden async() synchronisation.

### Goal

- Measure and compare performance.
- Include new idsa\_module.f90 in FLASH and ELEPHANT.

### **Problem**

- Not many, just requires more work :-)
- Can we hide further mem-copies by reordering work?

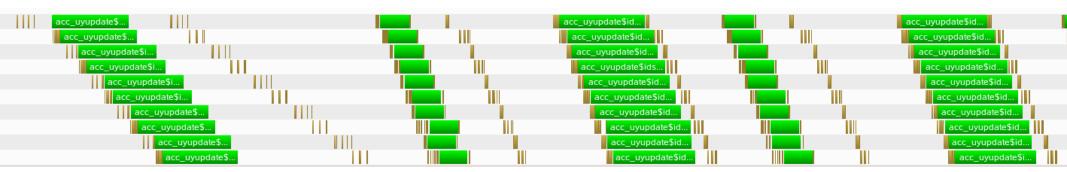
### Results



### Starting with 2 streams...



### Getting exited about 10 streams...



### Leaving it at 2 streams.



```
Before (1 CPU):
                           Imb.
                                   Calls Group
Time%
            Time
                   Imb.
                   Time
                                              Function
                          Time%
 100.0% | 0.676340 |
                                 | 1628147.0 | USER
  17.1%
          0.115953
                                     252450.0 | nprates$idsa module
  16.9%
          0.114248
                                     201960.0 | uydot$idsa module
                                     141955.0 | framerate$idsa module
  11.5% | 0.077870 |
                                      50490.0 | uyupdate$idsa module
  10.7% | 0.072137
                                      50490.0 | meanfreepath$idsa module
   8.6% | 0.057929
                                     252450.0 | idsa eos$idsa module
   6.7% | 0.045360
                                      50490.0 | ionscatt$idsa module
   5.9% | 0.039914
                                    50490.0 | idsa spectrum$idsa module
   5.6% | 0.037816
                                      91465.0 | residuum$idsa module
   5.0% | 0.033892
                                     100980.0 | etafit$idsa module
   4.4% 0.029494
                                     192445.0 | nframeint$idsa module
   2.3%
          0.015779
                                      50490.0 | npscatt$idsa module
   1.6%
          0.011071
```

### After (GPU):

	Time%					roup
ı	Time   Time%   Function					
İ	100.0%   0.055065       397.0   USER					
	46.7%	0.025741			18.0	acc_uyupdate\$idsa_moduleACC_ASYNC_COPY@1i.238
ı	27.9%	0.015390			18.0	acc_meanfreepath\$idsa_moduleACC_ASYNC_COPY@1i.158
	11.8%	0.006503			18.0	acc_uyupdate\$idsa_moduleACC_ASYNC_COPY@1i.222
- 1	4.8%	0.002629			18.0	acc_meanfreepath\$idsa_moduleACC_ASYNC_KERNEL@li.147
	2.5%	0.001359			18.0	acc_meanfreepath\$idsa_moduleACC_ASYNC_COPY@1i.144
j	1.2%	0.000670			18.0	acc_uyupdate\$idsa_moduleACC_ASYNC_KERNEL@1i.224

### Conclusion



- We would never have been able to do this alone!
   Interdisciplinary work is crucial.
- Compared to other interdisciplinary efforts, this format works amazingly well!
- What we haven't achieved in 2 years has happened within one week. Speedup >= 104!
- Documentation of options to monitor and debug code compilation and performance.
- Simple walk-through example with explicit module list, environment setting, makefile, batch scripts, etc.
- Searchable collection of error messages and solut's.