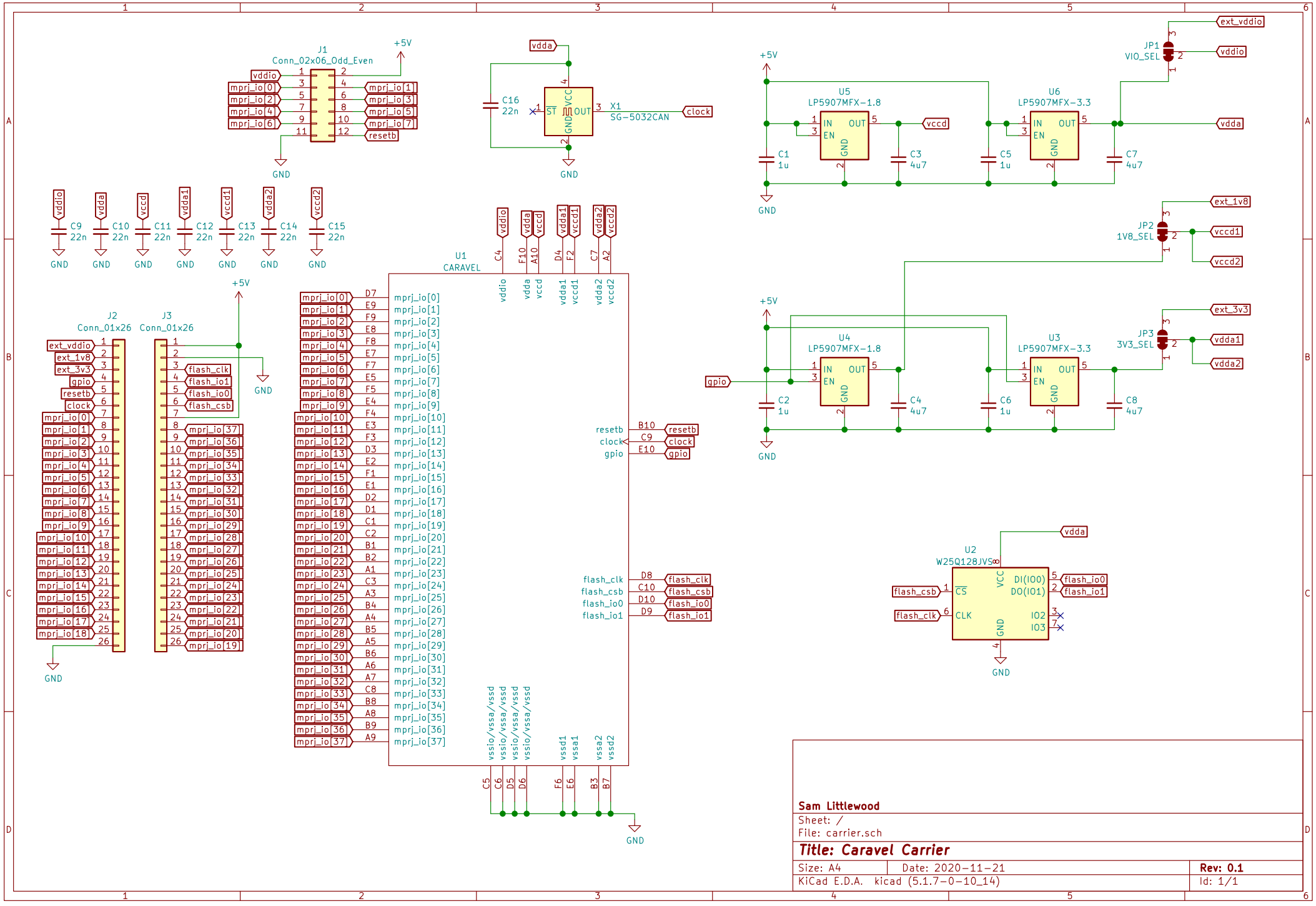
J1
Conn_02x06_Odd_Even
vddio
mprj_io[0]
mprj_io[2]
mprj_io[4]
mprj_io[6]
mprj_io[1]
mprj_io[3]
mprj_io[5]
mprj_io[7]
resetb
+5V
GND
C16
22n
vdda
X1
SG-5032CAN
clock
C9 22n
C10 22n
C11 22n
C12 22n
C13 22n
C14 22n
C15 22n
vccd
vccd1
vccd2
vdda1
vdda2
U1
CARAVEL
J2
Conn_01x26
J3
Conn_01x26
ext_vddio
ext_1v8
ext_3v3
gpio
flash_clk
flash_io1
flash_io0
flash_csb
U5
LP5907MFX-1.8
U6
LP5907MFX-3.3
U4
LP5907MFX-1.8
U3
LP5907MFX-3.3
C1 1u
C2 1u
C3 4u7
C4 4u7
C5 1u
C6 1u
C7 4u7
C8 4u7
JP1
VIO_SEL
JP2
1V8_SEL
JP3
3V3_SEL
U2
W25Q128JVS
CS
DI(IO0)
DO(IO1)
CLK
IO2
IO3
flash_io0
flash_io1
flash_clk
flash_csb
resetb
clock
gpio
vssio/vssa/vssd
vssd1
vssa1
vssa2
vssd2
Sam Littlewood
Sheet: /
File: carrier.sch
Title: Caravel Carrier
Size: A4
Date: 2020-11-21
Rev: 0.1
KiCad E.D.A. kicad (5.1.7-0-10_14)
Id: 1/1