

190050032

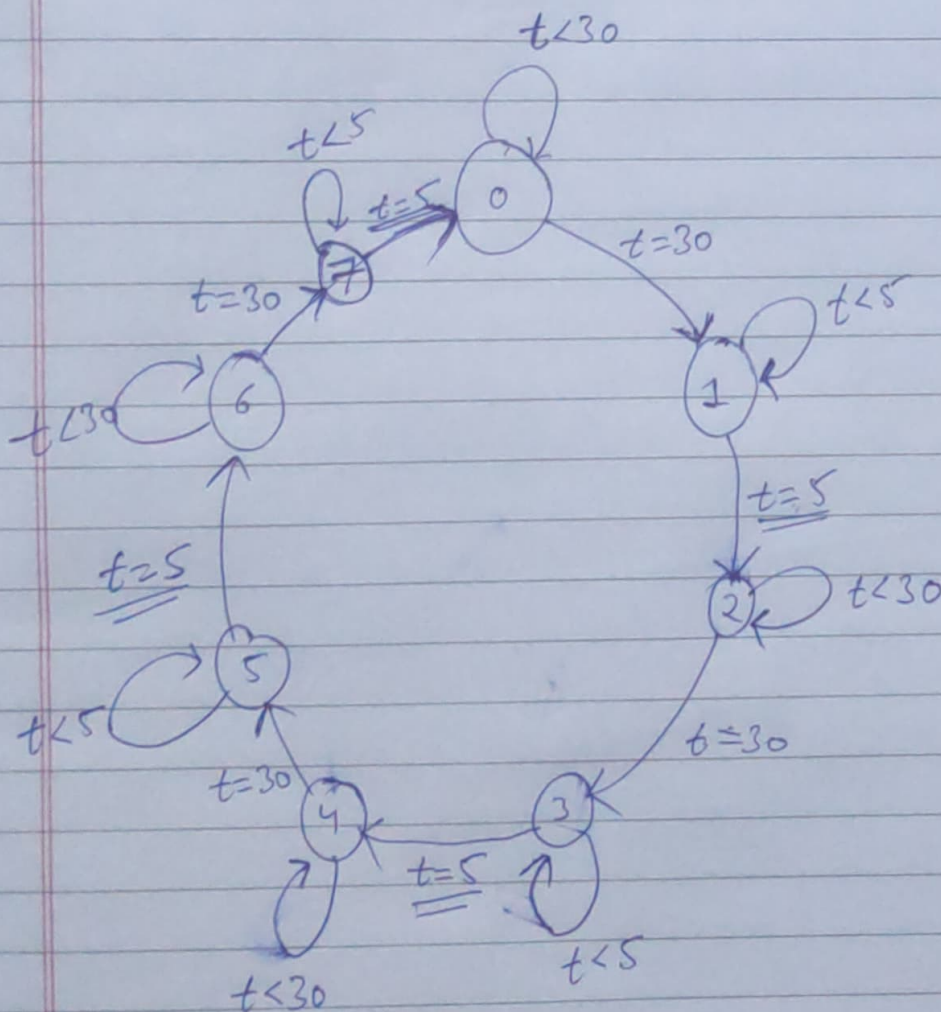
190050073

classmate

Date _____

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State	Road 0	Road 1	Road 2	Road 3	Representation	Delay
0	G	R	R	R	000	30
1	Y	R	R	R	001	5
2	R	G	R	R	010	30
3	R	Y	R	R	011	5
4	R	R	G	R	100	30
5	R	R	Y	R	101	5
6	R	R	R	G	110	30
7	R	R	R	Y	111	5



So we are counting clock pulse in the 't' variable and in the state diagrams we are showing all the possible states that the system and variable 't' can achieve.

Also, when moving from one state to another we are reinitializing ~~to~~ variable 't' to zero again. And so, in each state our 't' starts incrementing from 0.