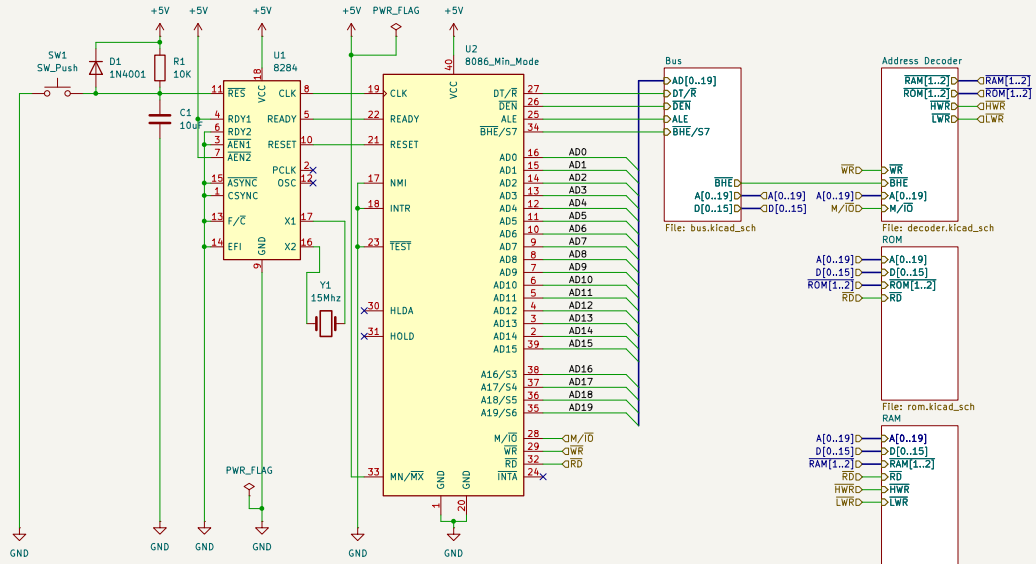
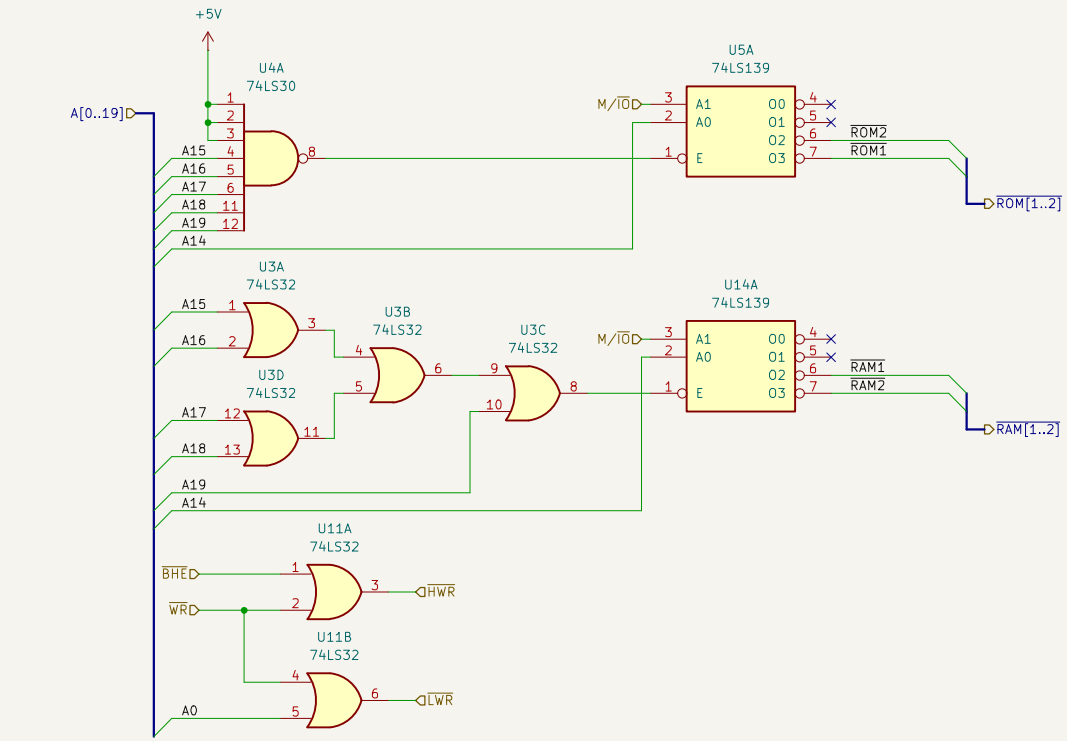
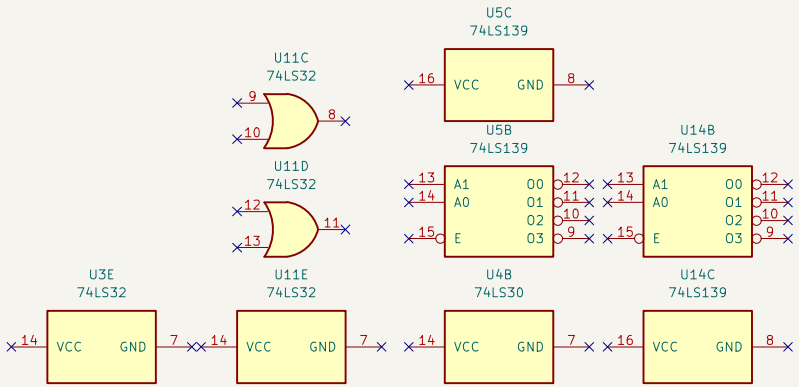


CPE 3104 – Microprocessors		Group 4 TTh 4:30–7:30PM LBCEAC2 TC
Designers Jhon Fil Tizon Christian Jay Gallardo		
Sheet: / File: Memory Interfacing.kicad_sch		
Title: Team 3 LE4_2: Memory Interfacing		
Size: A3	Date: 2025-10-25	**Rev: 1**
KiCad E.D.A. 9.0.2		Id: 1/5





CPE 3104 – Microprocessors Group 4 TTh 4:30–7:30PM LBCEAC2 TC

Designers

Jhon Fil Tizon

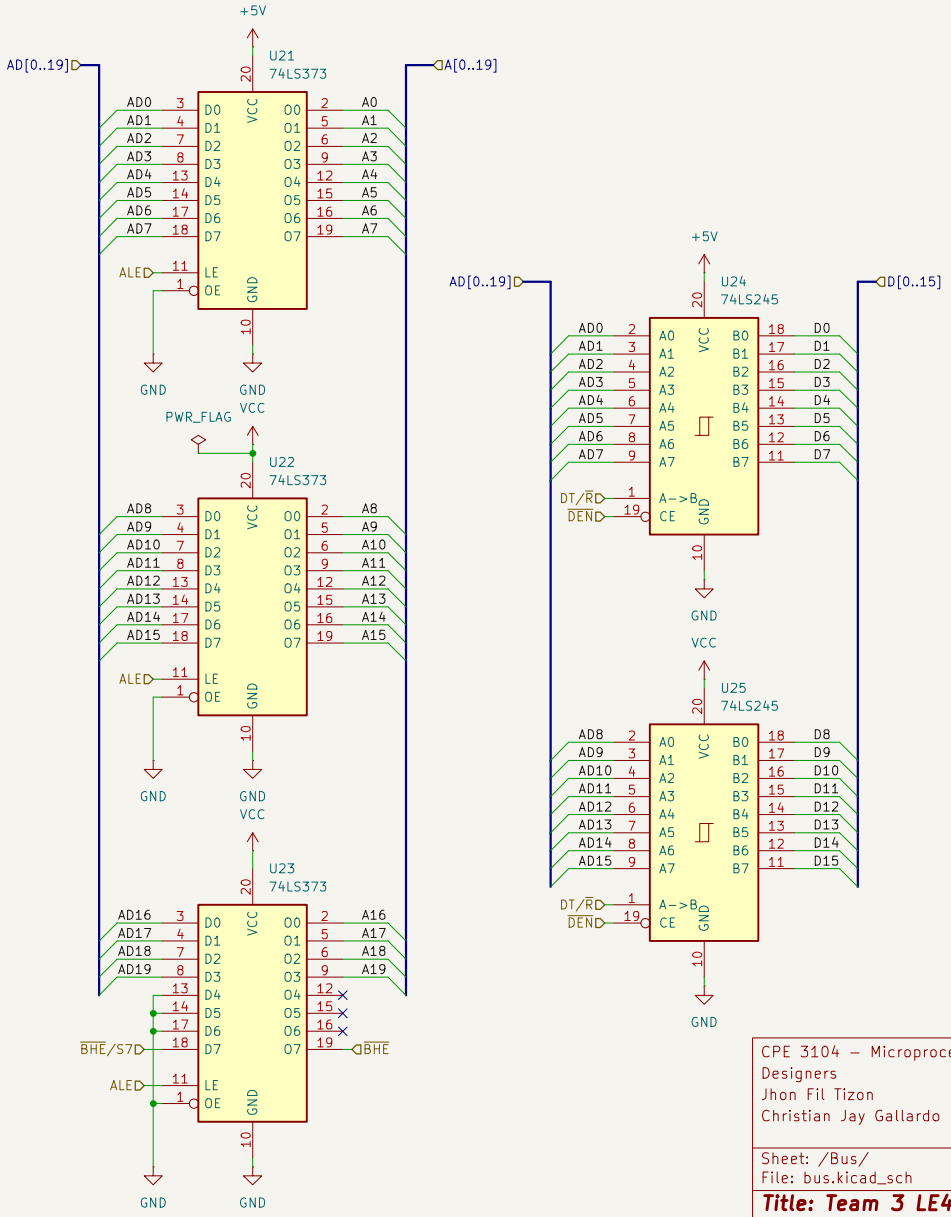
Christian Jay Gallardo

Sheet: /Address Decoder/

File: decoder.kicad_sch

Title: Team 3 LE4_2: Memory Interfacing

Size: A4	Date: 2025-10-25	**Rev: 1**
KiCad E.D.A. 9.0.2		Id: 2/5



CPE 3104 – Microprocessors Group 4 TTh 4:30–7:30PM LBCEAC2 TC

Designers

Jhon Fil Tizon

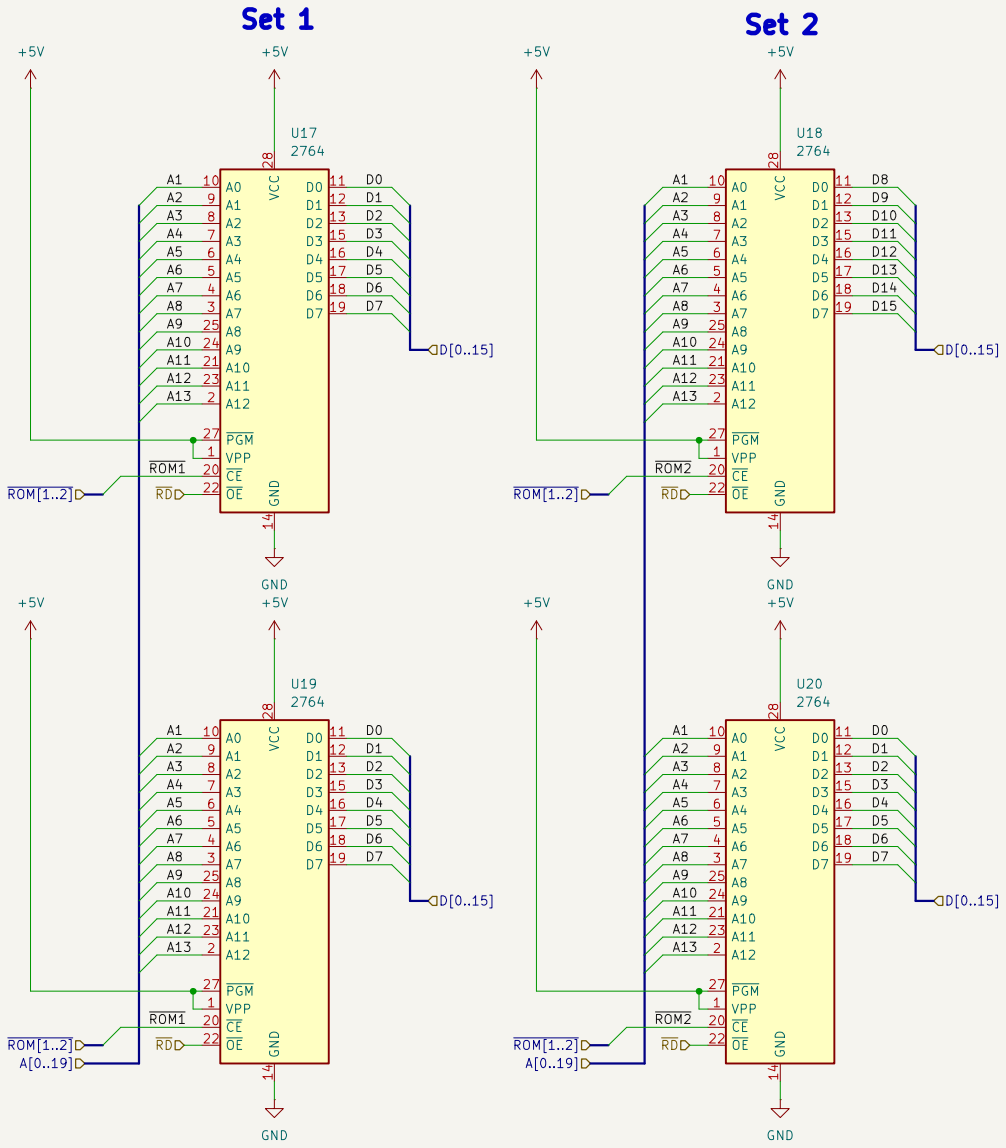
Christian Jay Gallardo

Sheet: /Bus/

File: bus.kicad_sch

Title: Team 3 LE4_2: Memory Interfacing

Size: A4	Date: 2025-10-25	**Rev: 1**
KiCad E.D.A. 9.0.2		Id: 3/5



CPE 3104 – Microprocessors Group 4 TTh 4:30–7:30PM LBCEAC2 TC

Designers

Jhon Fil Tizon

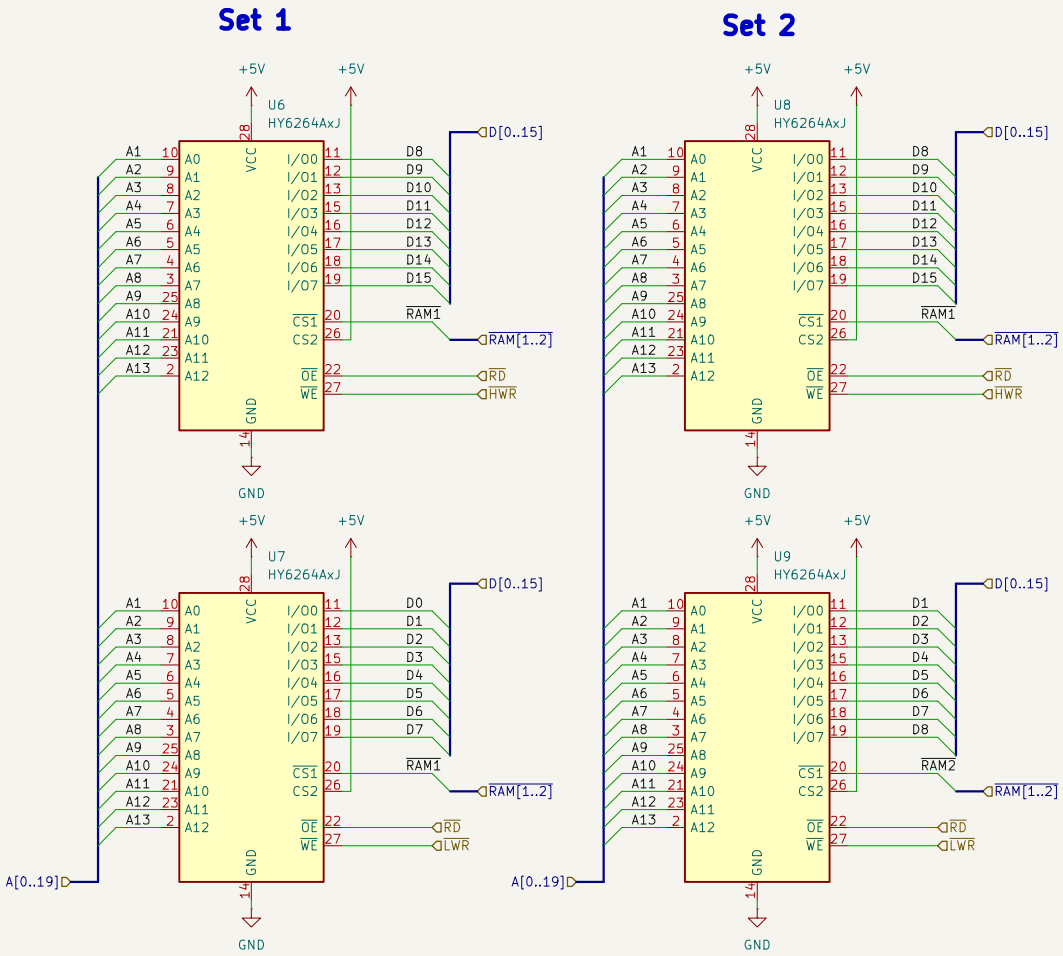
Christian Jay Gallardo

Sheet: /ROM/

File: rom.kicad_sch

Title: Team 3 LE4_2: Memory Interfacing

Size: A4	Date: 2025-10-25	**Rev: 1**
KiCad E.D.A. 9.0.2		Id: 4/5



CPE 3104 – Microprocessors Group 4 TTh 4:30–7:30PM LBCEAC2 TC

Designers

Jhon Fil Tizon

Christian Jay Gallardo

Root

Sheet: /RAM/

File: ram.kicad_sch

Title: Team 3 LE4_2: Memory Interfacing

Size: A4	Date: 2025-10-25	**Rev: 1**
KiCad E.D.A. 9.0.2		Id: 5/5