

# Short Quiz | Introduction to Microprocessors and Intel 8086

- Due Oct 7 at 5pm
- Points 20
- Questions 20
- Available until Oct 7 at 5pm
- Time Limit 20 Minutes

## Instructions



*Let's see how much you have recalled on the Introduction to Microprocessors and Intel 8086.  
Choose the correct answer.*

This quiz was locked Oct 7 at 5pm.

## Attempt History

	Attempt	Time	Score
LATEST	<a href="#">Attempt 1</a>	14 minutes	17.67 out of 20

❗ Correct answers are hidden.

Score for this quiz: 17.67 out of 20

Submitted Oct 7 at 4:54pm

This attempt took 14 minutes.

Incorrect



Question 1

0 / 1 pts

If the instruction requires to write data to the memory, what will the CPU do first?

- ☐ Fetch the next instruction.
- ☐ Reads the data from the bus.
- ☐ Puts the data on the bus.

☐ None of the choices.



### Question 2

1 / 1 pts

Which of the following unit of the Intel 8086 Microprocessor contains the Arithmetic and Logic Unit (ALU)?

☐ Bus Interface Unit

☒ Execution Unit



### Question 3

1 / 1 pts

A 16-bit microprocessor has:

☐ 16-bit control bus

☒ 16-bit data bus

☐ 16-bit instruction register

☐ 16-bit instruction pointer



### Question 4

1 / 1 pts

A type of microprocessor designed to process the analog signals into a digital form.

☐ Analog to Digital Converter

☒ Digital Signal Processor

☐ Symbolic Processors

☐ Graphics Processor



### Question 5

1 / 1 pts

Which of the following is the correct order of the operation of the CPU?

☐ Decode-Fetch-Execute

☐ Execute-Decode-Fetch

☐ Fetch-Execute-Decode

☒ Fetch-Decode-Execute



### Question 6

1 / 1 pts

The Intel 8086 is a \_\_\_\_\_ microprocessor.

☐ 16-bit

☐ 8-bit

☐ 64-bit

☐ 32-bit



### Question 7

1 / 1 pts

Which of the following is the co-inventor of the Integrated Circuit?

☐ Robert Noyce

☐ Steve Jobs

☐ William Sockley

☐ John Bardeen



### Question 8

1 / 1 pts

Which of the following devices William Shockley is credited inventing?

☐ Thermistor

☐ Integrated Circuit

☐ Microprocessor

☐ Transistor

Partial



### Question 9

0.67 / 1 pts

Match the components of the microprocessor on the left to its description on the right.

Control Unit

Fetches and executes the instr 

Registers

Stores the computer program. 

ALU

Performs arithmetic and logic 



### Question 10

1 / 1 pts

A silicon chip which includes an ALU, register circuits and control circuits.

- ☐ Amplifier
- ☐ Microcontroller
- ☐ Integrated Circuit
- ☒ Microprocessor



### Question 11

1 / 1 pts

**True or False:** Pipelining is an architecture that allows simultaneous fetch and decode.

- ☒ True
- ☐ False



### Question 12

1 / 1 pts

Which of the following CPU architecture requires more instructions to perform an operation?

- ☐ Complex Instruction Set Computer
- ☒ Reduced Instruction Set Computer



### Question 13

0 / 1 pts

Which architecture utilizes less transistors?

- ☐ Reduced Instruction Set Computer
- ☒ Complex Instruction Set Computer



### Question 14

1 / 1 pts

The Intel 4004 is...

- ☐ The first microcontroller.
- ☐ The first graphics processor.
- ☒ The first microprocessor.
- ☐ The first integrated circuit.



Question 15	
1 / 1 pts	
Which of the following belongs to the fifth generation of microprocessors?	
<input checked="" type="checkbox"/>	AMD Ryzen 5
<input type="checkbox"/>	Zilog Z80
<input type="checkbox"/>	Intel 80486
<input checked="" type="checkbox"/>	Intel Core i5
⋮	
Question 16	
1 / 1 pts	
Which of the following connects the different components of the microprocessor?	
<input type="radio"/>	Wire
<input checked="" type="radio"/>	Bus
<input type="radio"/>	Registers
<input type="radio"/>	Train
⋮	
Question 17	
1 / 1 pts	
In the basic bus system architecture, the connection of the memory and I/O are bi-directional. Choose the best statement explaining why.	
<input type="radio"/>	Signal should flow in a bi-directional manner.
<input type="radio"/>	The CPU will send and receive control signals to the memory and I/O.
<input checked="" type="radio"/>	CPU reads and writes data to memory and I/O.
<input type="radio"/>	None of the choices.
⋮	
Question 18	
1 / 1 pts	
Match the CPU operation on the left to its description on the right.	
Fetch	
<div>Reads the instruction from the</div>	
Decode	

Identifies the operation to be performed

Execute

Performs data movement, arithmetic, and logic operations



Question 19

1 / 1 pts

**True or False:** The *von Neumann* architecture is the architecture where modern CPUs are based.

☐ True

☐ False



Question 20

1 / 1 pts

**True or False:** The term "x86" was derived from the fact that it was developed in 1986.

☐ True

☐ False

Quiz Score: 17.67 out of 20