

Seminar II

**IMPLEMENTASI FILTER SPASIAL LINEAR PADA VIDEO *STREAM*
MENGUNAKAN *FPGA HARDWARE ACCELERATOR***



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MAKASSAR**

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BAB I

PENDAHULUAN

1.1 Latar Belakang

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BAB II

TINJAUAN PUSTAKA

2.1 Landasan Teori

BAB III
METODE PENELITIAN

BAB IV

HASIL DAN PEMBAHASAN

BAB V

KESIMPULAN DAN SARAN

5.1 Kesimpulan

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5.2 Saran

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Tabel 5.1: ARM Average Blur Percobaan 1

PID	VIRT	RES	SHR	Status	CPU(%)	MEM(%)	Time(s)
3629	389184	127288	55152	S	0.0	25.0	0:24.78
3629	389184	127288	55152	S	0.0	25.0	0:24.78
3629	389184	127288	55152	S	0.0	25.0	0:24.78
3629	390084	128256	56028	R	15.0	25.2	0:24.87
3629	393684	128712	56484	R	98.3	25.3	0:25.46
3629	394584	128712	56484	R	100.0	25.3	0:26.06
3629	394584	128712	56484	R	98.3	25.3	0:26.65
3629	394584	128712	56484	R	100.0	25.3	0:27.26
3629	394584	128712	56484	R	100.0	25.3	0:27.86
3629	394584	128712	56484	R	98.3	25.3	0:28.45
3629	394584	128712	56484	R	100.0	25.3	0:29.05
3629	394584	128712	56484	R	98.4	25.3	0:29.65
3629	394584	128712	56484	R	100.0	25.3	0:30.25
3629	394584	128712	56484	R	100.0	25.3	0:30.85
3629	394584	128712	56484	R	100.0	25.3	0:31.45
3629	394584	128712	56484	R	100.0	25.3	0:32.05
3629	394584	128712	56484	R	100.0	25.3	0:32.66
3629	394584	128712	56484	R	100.0	25.3	0:33.26
3629	394584	128712	56484	R	100.0	25.3	0:33.86
3629	394584	128712	56484	R	100.0	25.3	0:34.46
3629	394584	128712	56484	R	100.0	25.3	0:35.06
3629	394584	128712	56484	R	100.0	25.3	0:35.67
3629	394584	128712	56484	S	73.3	25.3	0:36.11
3629	394584	128712	56484	S	0.0	25.3	0:36.11

DAFTAR PUSTAKA

Putra, Darma (2010). *Pengolahan Citra Digital*. ISBN-13: 978-979-29-1443-6. Jl. Beo 38-40, Yogyakarta 55281: Penerbit Andi.