

## 270 Lab 5

### Screenshots #7

The screenshot shows the Q15spin simulator interface. The 'Int Regs [16]' window is open, displaying the state of the integer registers. The 'Text' window shows the assembly code, with line 19 highlighted. The registers show the following values:

Register	Value
PC	400038
EPC	400024
Cause	24
BadVAddr	0
Status	3000ff10
HI	0
LO	0
R0 [r0]	0
R1 [a1]	10010000
R2 [v0]	4
R3 [v1]	0
R4 [a0]	1
R5 [a1]	7ffff600
R6 [a2]	7ffff604
R7 [a3]	0
R8 [t0]	ffffff04
R9 [t1]	11
R10 [t2]	0
R11 [t3]	0
R12 [t4]	0
R13 [t5]	0
R14 [t6]	0
R15 [t7]	0
R16 [a0]	0
R17 [a1]	0
R18 [a2]	0
R19 [a3]	0
R20 [a4]	0
R21 [a5]	0
R22 [a6]	0
R23 [a7]	0
R24 [a8]	0
R25 [a9]	0
R26 [a0]	0
R27 [a1]	0

The assembly code in the 'Text' window shows the following instructions:

```
[00400000] 8fa40000 lw $t0, 0($t0) ; 183: lw $a0 0($sp) # argc
[00400004] 27a50004 addiu $t1, $t0, 4 ; 184: addiu $a1 $sp 4 # argv
[00400008] 24a60004 addiu $t2, $t1, 4 ; 185: addiu $a2 $a1 4 # envp
[0040000c] 00a41080 sll $t2, $t1, 2 ; 186: sll $v0 $a0 2
[00400010] 00c23021 addu $t2, $t2, $t1 ; 187: addu $a2 $a2 $v0
[00400014] 0c100009 jal 0x00400024 [main] ; 188: jal main
[00400018] 00000000 nop ; 189: nop
[0040001c] 3402000a ori $t2, $t0, 10 ; 191: li $v0 10
[00400020] 0000000c syscall ; 192: syscall # syscall 10 (exit)
[00400024] x3c01100 lui $t1, 4097 ; 18: lw $t0, num1 # temp = num1
[00400028] 8c280000 lw $t0, 0($t1) ; 19: add $t1, $t0, $t0 # accum = temp
[0040002c] 00084820 add $t0, $t0, $t0 ; 20: lw $t0, num2 # temp = num2
[00400030] 3c011001 lui $t1, 4097 ; 21: add $t1, $t1, $t1 # accum = accum + temp
[00400034] 8c280004 lw $t0, 4($t1) ; 22: lw $t0, num3 # temp = num3
[00400038] 01284820 add $t0, $t0, $t0 ; 23: add $t1, $t1, $t1 # accum = accum + temp
[0040003c] 3c011001 lui $t1, 4097 ; 24: sw $t1, sum # temp = sum
[00400040] 8c29000c sw $t1, 12($t1) ; 25: addi $v0, $t0, 1 # $v0 = code for 'print-int'
[00400044] 20020001 addi $t2, $t0, 1 ; 26: add $a0, $t0, $t1 # $a0 = accum
[00400048] 00092020 add $t4, $t0, $t0 ; 27: syscall # syscall($v0) prints $a0
[0040004c] 0000000c syscall ; 28: jr $ra # return control to the simulator
[00400050] 03a00008 jr $t1
```

Registers \$t0 and \$t1 after line 19 is sum.asm. (First number added)

The screenshot shows the Q15spin simulator interface. The 'Int Regs [10]' window is open, displaying the state of the integer registers. The 'Text' window shows the assembly code, with line 24 highlighted. The registers show the following values:

Register	Value
PC	4194376
EPC	4194340
Cause	36
BadVAddr	0
Status	80537164
HI	0
LO	0
R0 [r0]	0
R1 [a1]	268500992
R2 [v0]	4
R3 [v1]	0
R4 [a0]	1
R5 [a1]	2147481292
R6 [a2]	2147481300
R7 [a3]	276
R8 [t0]	276
R9 [t1]	258
R10 [t2]	0
R11 [t3]	0
R12 [t4]	0
R13 [t5]	0
R14 [t6]	0
R15 [t7]	0
R16 [a0]	0
R17 [a1]	0
R18 [a2]	0
R19 [a3]	0
R20 [a4]	0
R21 [a5]	0
R22 [a6]	0
R23 [a7]	0
R24 [a8]	0
R25 [a9]	0
R26 [a0]	0
R27 [a1]	0

The assembly code in the 'Text' window shows the following instructions:

```
[00400000] 8fa40000 lw $t0, 0($t0) ; 183: lw $a0 0($sp) # argc
[00400004] 27a50004 addiu $t1, $t0, 4 ; 184: addiu $a1 $sp 4 # argv
[00400008] 24a60004 addiu $t2, $t1, 4 ; 185: addiu $a2 $a1 4 # envp
[0040000c] 00a41080 sll $t2, $t1, 2 ; 186: sll $v0 $a0 2
[00400010] 00c23021 addu $t2, $t2, $t1 ; 187: addu $a2 $a2 $v0
[00400014] 0c100009 jal 0x00400024 [main] ; 188: jal main
[00400018] 00000000 nop ; 189: nop
[0040001c] 3402000a ori $t2, $t0, 10 ; 191: li $v0 10
[00400020] 0000000c syscall ; 192: syscall # syscall 10 (exit)
[00400024] x3c01100 lui $t1, 4097 ; 18: lw $t0, num1 # temp = num1
[00400028] 8c280000 lw $t0, 0($t1) ; 19: add $t1, $t0, $t0 # accum = temp
[0040002c] 00084820 add $t0, $t0, $t0 ; 20: lw $t0, num2 # temp = num2
[00400030] 3c011001 lui $t1, 4097 ; 21: add $t1, $t1, $t1 # accum = accum + temp
[00400034] 8c280004 lw $t0, 4($t1) ; 22: lw $t0, num3 # temp = num3
[00400038] 01284820 add $t0, $t0, $t0 ; 23: add $t1, $t1, $t1 # accum = accum + temp
[0040003c] 3c011001 lui $t1, 4097 ; 24: sw $t1, sum # temp = sum
[00400040] 8c29000c sw $t1, 12($t1) ; 25: addi $v0, $t0, 1 # $v0 = code for 'print-int'
[00400044] 20020001 addi $t2, $t0, 1 ; 26: add $a0, $t0, $t1 # $a0 = accum
[00400048] 00092020 add $t4, $t0, $t0 ; 27: syscall # syscall($v0) prints $a0
[0040004c] 0000000c syscall ; 28: jr $ra # return control to the simulator
[00400050] 03a00008 jr $t1
```

Registers \$t0 and \$t1 after line 24 is sum.asm. (Second and third number added)

## Screenshots #8

The screenshot shows the QSPIN simulator interface. The 'FP Regs' tab is selected, displaying the following register values:

Register	Value
PC	4194352
RPC	4194340
Cause	36
BadVAddr	0
Status	805371664
HI	0
LO	0
R0 [r0]	0
R1 [a1]	268500992
R2 [v0]	4
R3 [v1]	0
R4 [a0]	1
R5 [a1]	2147481292
R6 [a2]	2147481300
R7 [a3]	0
R8 [t0]	184
R9 [t1]	184
R10 [t2]	0
R11 [t3]	0
R12 [t4]	0
R13 [t5]	0
R14 [t6]	0
R15 [t7]	0
R16 [a0]	0
R17 [a1]	0
R18 [a2]	0
R19 [a3]	0
R20 [a4]	0
R21 [a5]	0
R22 [a6]	0
R23 [a7]	0
R24 [a8]	0
R25 [a9]	0
R26 [a0]	0
R27 [a1]	0

The 'Text' tab is also visible, showing assembly code. The code is divided into two segments: 'User Text Segment' and 'Kernel Text Segment'. The 'User Text Segment' contains instructions for saving registers, adding values to registers, and printing the result. The 'Kernel Text Segment' contains instructions for saving registers, adding values to registers, and printing the result.

## Registers \$t0 and \$t1 after line 19 is sum2.asm. (First number added)

The screenshot shows the QSPIN simulator interface. The 'FP Regs' tab is selected, displaying the following register values:

Register	Value
PC	4194376
RPC	4194340
Cause	36
BadVAddr	0
Status	805371664
HI	0
LO	0
R0 [r0]	0
R1 [a1]	268500992
R2 [v0]	4
R3 [v1]	0
R4 [a0]	1
R5 [a1]	2147481292
R6 [a2]	2147481300
R7 [a3]	0
R8 [t0]	93
R9 [t1]	67
R10 [t2]	0
R11 [t3]	0
R12 [t4]	0
R13 [t5]	0
R14 [t6]	0
R15 [t7]	0
R16 [a0]	0
R17 [a1]	0
R18 [a2]	0
R19 [a3]	0
R20 [a4]	0
R21 [a5]	0
R22 [a6]	0
R23 [a7]	0
R24 [a8]	0
R25 [a9]	0
R26 [a0]	0
R27 [a1]	0

The 'Text' tab is also visible, showing assembly code. The code is divided into two segments: 'User Text Segment' and 'Kernel Text Segment'. The 'User Text Segment' contains instructions for saving registers, adding values to registers, and printing the result. The 'Kernel Text Segment' contains instructions for saving registers, adding values to registers, and printing the result.

## Registers \$t0 and \$t1 after line 23 is sum2.asm. (Second and third number added)

**Questions:**

1. The immediate 0x22220000 will be loaded into \$t6.
2. The instruction lui means to load upper immediate which shifts the immediate value to the left and add zeros to the right to make the value 32-bits long.
3. Since immediate values only have 16-bits in the instruction word, we need to initialize the most significant 16-bits to be added to the least significant 16-bits.
4. The last jump in the assembly program returns control to the simulator. The jump in QtSpim jumps to the function main address because this is the start of the program.