

270 Lab 5

Screenshots #7

QtSpim

Registers [16]

PC	# 400038
EPC	# 400024
Cause	# 24
BadVAddr	# 0
Status	# 3000ff10
H1	= 0
LO	= 0
R0 [r0]	= 0
R1 [r1]	= 10010000
R2 [r0]	= 4
R3 [r1]	= 0
R4 [r0]	= 1
R5 [r1]	= 7ffff6cc
R6 [r2]	= 7ffff6d4
R7 [r3]	= 0
R8 [r4]	= 0xffffffff
R9 [t1]	= 11
R10 [t2]	= 0
R11 [t3]	= 0
R12 [t4]	= 0
R13 [t5]	= 0
R14 [t6]	= 0
R15 [t7]	= 0
R16 [t8]	= 0
R17 [t9]	= 0
R18 [t10]	= 0
R19 [t11]	= 0
R20 [t12]	= 0
R21 [t13]	= 0
R22 [t14]	= 0
R23 [t15]	= 0
R24 [t16]	= 0
R25 [t17]	= 0
R26 [t18]	= 0
R27 [t19]	= 0

Data

```
[00400000] 8fa40000 lw $4, 0($29) ; 183: lw $a0 $0($sp) # argc
[00400004] 27fa50004 addi $5, $29, 4 ; 184: addiu $a1 $sp 4 # argv
[00400008] 24fa50004 addi $6, $29, 4 ; 185: addiu $a2 $a1 4 # envp
[00400012] 30fa50004 lui $1, 4097 ; 186: lui $v0 $a2
[00400016] 00c00000 jal 0x004000024 [main] ; 187: addu $a2 $a2 $v0
[00400018] 00000000 nop ; 188: jal main
[0040001c] 00000000 add $2, $0, 10 ; 189: add $t1, $0, $t0 # accum = temp
[00400020] 0000000c syscall ; 190: sw $t1, sum($t0) # sum = accum + temp
[00400024] 3c310001 lui $1, 4097 ; 191: add $t1, $0, $t0 # accum = temp
[00400028] 8c280000 lw $8, 0($1) ; 192: lw $t0, num2($temp) # temp = num2
[00400032] 3c310001 lui $1, 4097 ; 193: add $t1, $t1, $t0 # accum = accum + temp
[00400036] 8c280000 lw $8, 0($1) ; 194: sw $t1, sum($sum) # sum = accum
[00400040] 00c0000c ac29000c add $9, $9, $8 ; 195: add $t1, $t1, $t0 # accum = accum + temp
[00400044] 01284820 add $9, $9, $8 ; 196: add $t1, $t1, $t0 # accum = accum + temp
[00400048] 3c310001 lui $1, 4097 ; 197: add $t1, $t1, $t0 # accum = accum + temp
[0040004c] ac29000c add $9, $9, $8 ; 198: add $t1, $t1, $t0 # accum = accum + temp
[00400050] 00092020 add $4, $4, $9 ; 199: add $t1, $t1, $t0 # accum = accum + temp
[00400054] 00092020 add $4, $4, $9 ; 200: add $t1, $t1, $t0 # accum = accum + temp
[00400058] 0000000c syscall ; 201: add $t1, $t1, $t0 # accum = accum + temp
[0040005c] 03e00008 j $31 ; 202: jr $ra # return control to the simulator
```

User Text Segment [00400000..00440000]

Registers [10]

PC	# 4194376
EPC	# 4194340
Cause	# 36
BadVAddr	# 0
Status	# 805371664
H1	= 0
LO	= 0
R0 [r0]	= 0
R1 [r1]	= 0x85000992
R2 [r0]	= 4
R3 [r1]	= 0
R4 [r0]	= 0
R5 [r1]	= 2147481292
R6 [r2]	= 2147481300
R7 [r3]	= 0
R8 [r4]	= 276
R9 [r5]	= 258
R10 [r6]	= 0
R11 [r7]	= 0
R12 [r8]	= 0
R13 [r9]	= 0
R14 [r10]	= 0
R15 [r11]	= 0
R16 [r12]	= 0
R17 [r13]	= 0
R18 [r14]	= 0
R19 [r15]	= 0
R20 [r16]	= 0
R21 [r17]	= 0
R22 [r18]	= 0
R23 [r19]	= 0
R24 [r20]	= 0
R25 [r21]	= 0
R26 [r22]	= 0
R27 [r23]	= 0

Data

```
[00000180] 0001d821 addu $27, $0, $1 ; 90: move $k1 $at # Save $at
[00000184] 3c310001 lui $1, -28672 ; 92: sw $v0 $1 # Not re-entrant and we can't trust $sp
[00000188] 8c280000 lw $8, 0($1) ; 93: sw $a0 $2 # But we need to use these registers
[00000192] 3c310001 lui $1, 28672 ; 94: sw $t1, sum($sum) # sum = accum
[00000196] 8c280000 lw $8, 0($1) ; 95: mfc0 $k0 $13 # Cause register
[0000019a] 00202000 add $4, $4, $13 ; 96: srli $a0 $k0 2 # Extract ExcCode Field
[0000019c] 30840010 andi $4, $4, 31 ; 97: andi $a0 $a0 0x1f
[000001a0] 34020004 ori $2, $0, 4 ; 101: li $v0 4 # syscall 4 (print_int)
[000001a4] 03e049000 lui $4, -28672 [_m1_] ; 102: la $a0 __m1_
```

Kernel Text Segment [80000000..80010000]

Registers \$t0 and \$t1 after line 19 is sum.asm. (First number added)

QtSpim

Registers [10]

PC	# 4194376
EPC	# 4194340
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R3 [r1]	= 0
R4 [r0]	= 0
R5 [r1]	= 2147481292
R6 [r2]	= 2147481300
R7 [r3]	= 0
R8 [r4]	= 276
R9 [r5]	= 258
R10 [r6]	= 0
R11 [r7]	= 0
R12 [r8]	= 0
R13 [r9]	= 0
R14 [r10]	= 0
R15 [r11]	= 0
R16 [r12]	= 0
R17 [r13]	= 0
R18 [r14]	= 0
R19 [r15]	= 0
R20 [r16]	= 0
R21 [r17]	= 0
R22 [r18]	= 0
R23 [r19]	= 0
R24 [r20]	= 0
R25 [r21]	= 0
R26 [r22]	= 0
R27 [r23]	= 0

Data

```
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[00000184] 3c310001 lui $1, -28672 ; 92: sw $v0 $1 # Not re-entrant and we can't trust $sp
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```

User Text Segment [00400000..00440000]

Registers [10]

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EPC	# 4194340
Cause	# 36
BadVAddr	# 0
Status	# 805371664
H1	= 0
LO	= 0
R0 [r0]	= 0
R1 [r1]	= 0x85000992
R2 [r0]	= 4
R3 [r1]	= 0
R4 [r0]	= 0
R5 [r1]	= 2147481292
R6 [r2]	= 2147481300
R7 [r3]	= 0
R8 [r4]	= 276
R9 [r5]	= 258
R10 [r6]	= 0
R11 [r7]	= 0
R12 [r8]	= 0
R13 [r9]	= 0
R14 [r10]	= 0
R15 [r11]	= 0
R16 [r12]	= 0
R17 [r13]	= 0
R18 [r14]	= 0
R19 [r15]	= 0
R20 [r16]	= 0
R21 [r17]	= 0
R22 [r18]	= 0
R23 [r19]	= 0
R24 [r20]	= 0
R25 [r21]	= 0
R26 [r22]	= 0
R27 [r23]	= 0

Data

```
[00000180] 0001d821 addu $27, $0, $1 ; 90: move $k1 $at # Save $at
[00000184] 3c310001 lui $1, -28672 ; 92: sw $v0 $1 # Not re-entrant and we can't trust $sp
[00000188] 8c280000 lw $8, 0($1) ; 93: sw $a0 $2 # But we need to use these registers
[00000192] 3c310001 lui $1, 28672 ; 94: sw $t1, sum($sum) # sum = accum
[00000196] 8c280000 lw $8, 0($1) ; 95: mfc0 $k0 $13 # Cause register
[0000019a] 00202000 add $4, $4, $13 ; 96: srli $a0 $k0 2 # Extract ExcCode Field
[0000019c] 30840010 andi $4, $4, 31 ; 97: andi $a0 $a0 0x1f
[000001a0] 34020004 ori $2, $0, 4 ; 101: li $v0 4 # syscall 4 (print_int)
[000001a4] 03e049000 lui $4, -28672 [_m1_] ; 102: la $a0 __m1_
```

Kernel Text Segment [80000000..80010000]

Registers \$t0 and \$t1 after line 24 is sum.asm. (Second and third number added)

Screenshots #8

Registers \$t0 and \$t1 after line 19 is sum2.asm. (First number added)

```

Registers $t0 and $t1 after line 19 is sum2.asm. (First number added)

User Text Segment [00400000]..[00440000]
PC = 4194352
R0 = 0
R1 = 268500992
R2 = 4
R3 = 0
R4 = 1
R5 = 2147481292
R6 = 2147481300
R7 = 0
R8 = 184
R9 = 111 = 184
R10 = 0
R11 = 0
R12 = 0
R13 = 0
R14 = 0
R15 = 0
R16 = 0
R17 = 0
R18 = 0
R19 = 0
R20 = 0
R21 = 0
R22 = 0
R23 = 0
R24 = 0
R25 = 0
R26 = 0
R27 = 0

Memory and registers cleared

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Single Step

```

Registers \$t0 and \$t1 after line 19 is sum2.asm. (First number added)

Registers \$t0 and \$t1 after line 23 is sum2.asm. (Second and third number added)

```

Registers $t0 and $t1 after line 23 is sum2.asm. (Second and third number added)

User Text Segment [00400000]..[00440000]
PC = 4194376
R0 = 0
R1 = 268500992
R2 = 4
R3 = 0
R4 = 1
R5 = 2147481292
R6 = 2147481300
R7 = 0
R8 = 100
R9 = 67
R10 = 0
R11 = 0
R12 = 0
R13 = 0
R14 = 0
R15 = 0
R16 = 0
R17 = 0
R18 = 0
R19 = 0
R20 = 0
R21 = 0
R22 = 0
R23 = 0
R24 = 0
R25 = 0
R26 = 0
R27 = 0

Memory and registers cleared

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Single Step

```

Questions:

1. The immediate 0x22220000 will be loaded into \$t6.
2. The instruction lui means to load upper immediate which shifts the immediate value to the left and add zeros to the right to make the value 32-bits long.
3. Since immediate values only have 16-bits in the instruction word, we need to initialize the most significant 16-bits to be added to the least significant 16-bits.
4. The last jump in the assembly program returns control to the simulator. The jump in QtSpim jumps to the function main address because this is the start of the program.