

[illegible][illegible]

The diagram illustrates a 3-to-2 channel audio converter circuit. It features three input channels: OUTA, OUTB, and OUTC. These inputs are connected to a network of resistors (R2, R4, R5, R3, R6, R7) and capacitors (C2, C1, C3, C4). The output is a 2-channel signal (LEFT, RIGHT) connected to a CO2 component. The ground is labeled GND_AUDIO.

Diagram of a 10-bit parallel bus J1. The bus is labeled J1 01x10. It has 10 data lines numbered 1 to 10. Line 1 is connected to VCC (indicated by an upward arrow) and line 10 is connected to GND (indicated by a downward arrow). The other lines (2-9) are shown as green lines without connections.

A schematic diagram of a 50 ohm transmission line. It consists of a horizontal green line representing the transmission line. Five green dots are placed along this line, representing connection points. From each dot, a vertical line extends upwards to a red arrow labeled 'VCC' and a vertical line extends downwards to a red arrow labeled 'GND'. Each of these vertical lines contains a capacitor symbol labeled 'C5 100nf', 'C6 100nf', 'C7 100nf', 'C8 100nf', and 'C9 100nf' respectively from left to right.

- H1
MountingHole
- H2
MountingHole
- H3
MountingHole
- H4
MountingHole

Sheet: /	
File: ay-zonic-core.kicad_sch	
Title: AY-ZONIC-Core	
Size: A3	Date: 2025-09-20
KiCad E.D.A. 9.0.4	Rev: 2.0 Id: 1/1