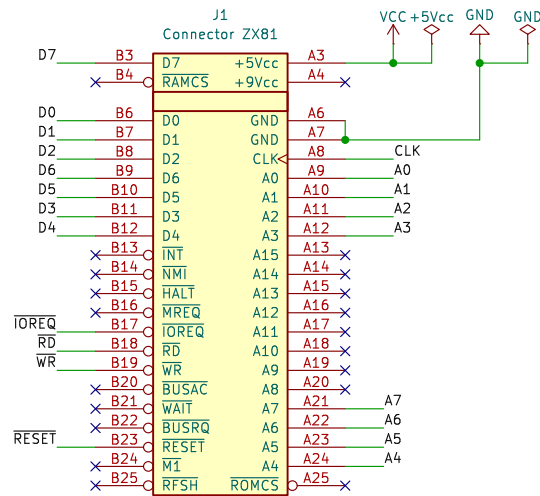
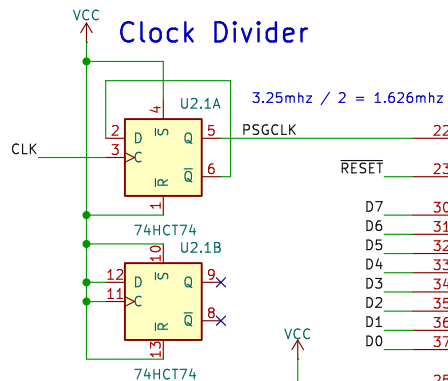


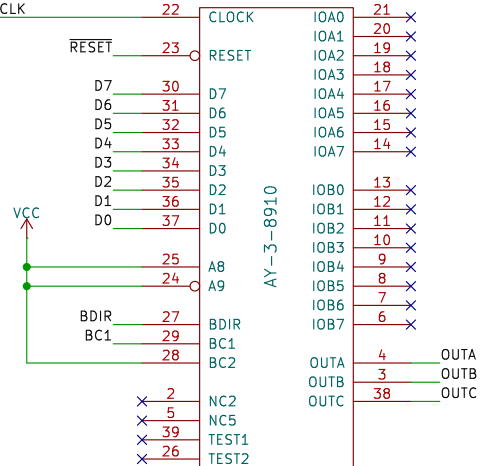
ZX81 Edge Connector



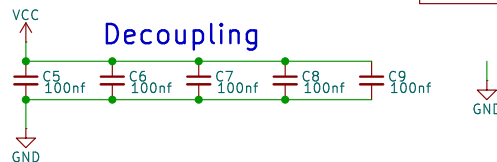
Clock Divider



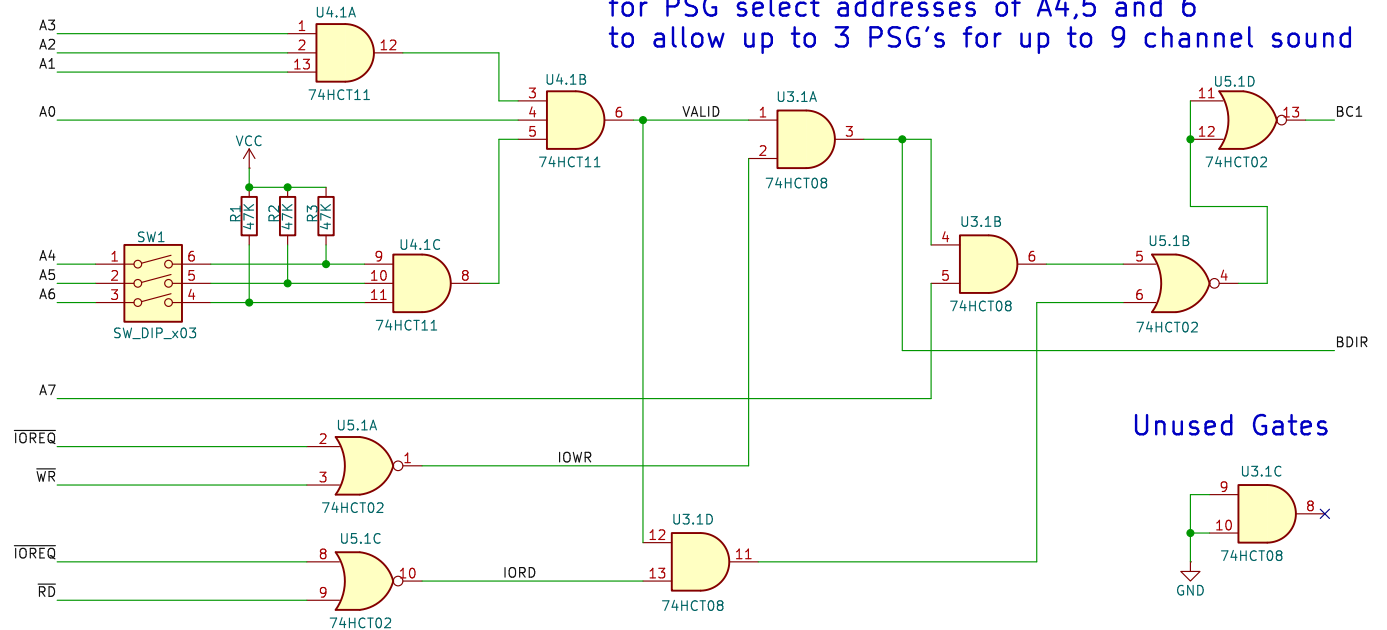
PSG



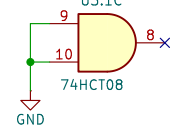
Decoupling



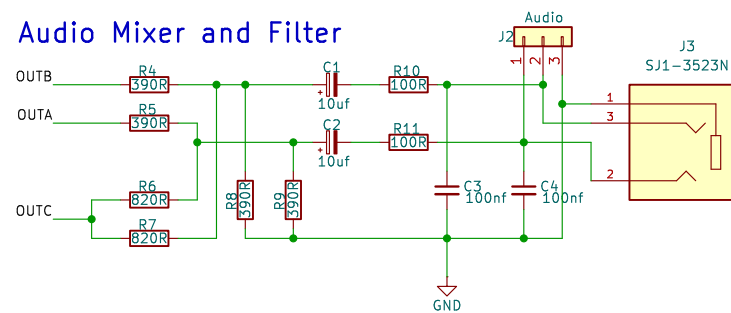
Address Decoding with DIP Switch Options
for PSG select addresses of A4,5 and 6
to allow up to 3 PSG's for up to 9 channel sound



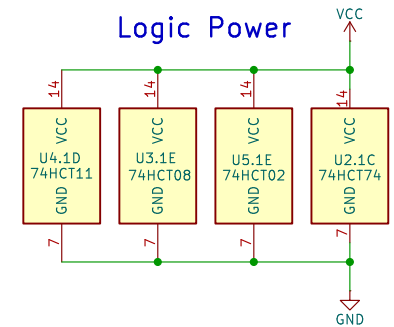
Unused Gates



Audio Mixer and Filter



Logic Power



Channel A is Left	BDIR	BC1		
Channel B is right	0	0		INACTIVE
Channel C (normally noise) is mixed	0	1	/IORD	XX0FH READ PSS
	1	0	/IOWR	XX0FH WRITE PSS
	1	1	/IOWR	XX0FH LATCH ADDRESS
				XX0FH = 0FH = 00001111
				XX0FH = DFH = 10001111

Sheet: /
File: MyZON-X.kicad_sch

Title: JonZON-X	
Size: A4	Date: 2025-08-01

Rev: 1.0
Id: 1/1