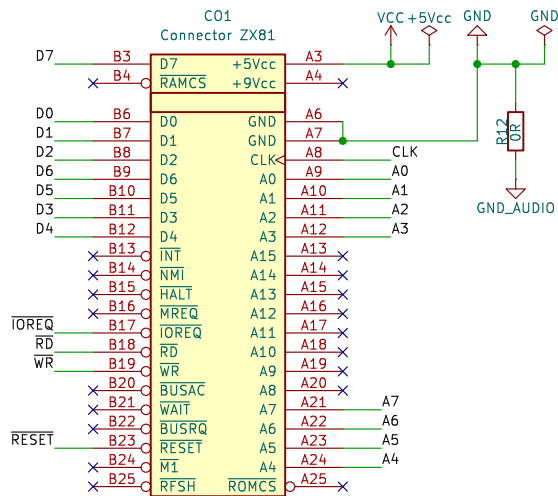
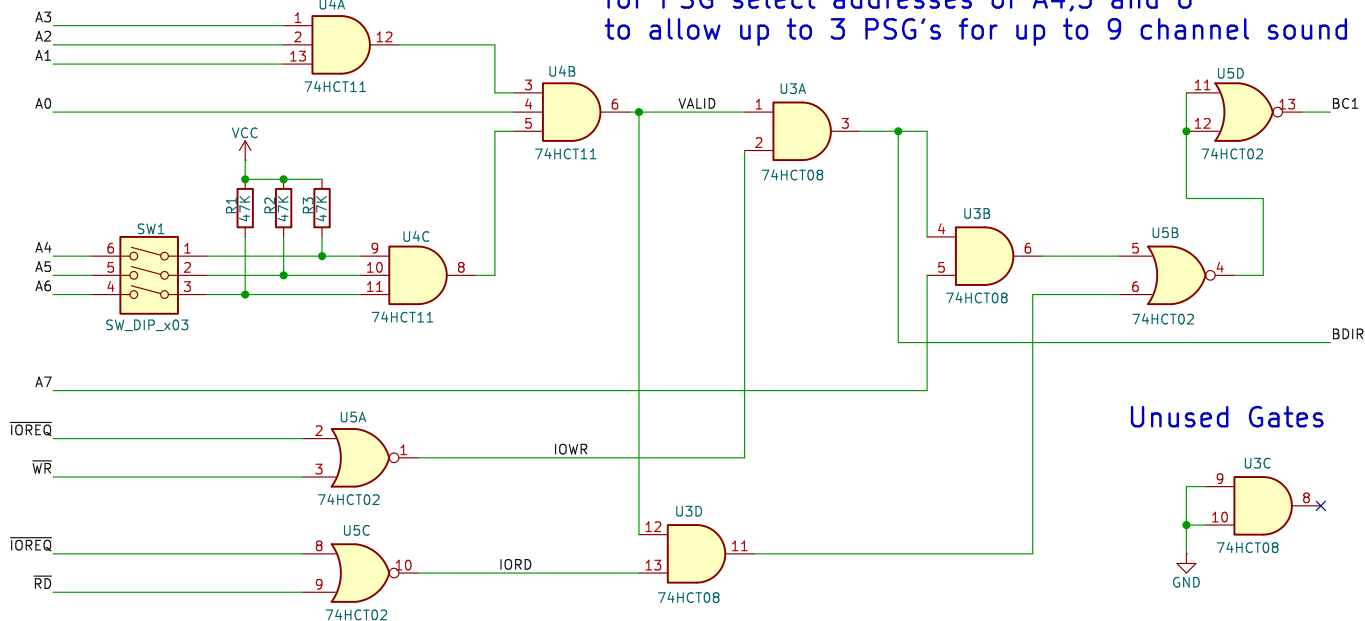


## ZX81 Edge Connector

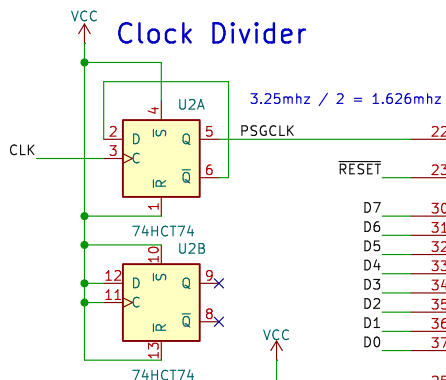


Address Decoding with DIP Switch Options for PSG select addresses of A4,5 and 6 to allow up to 3 PSG's for up to 9 channel sound

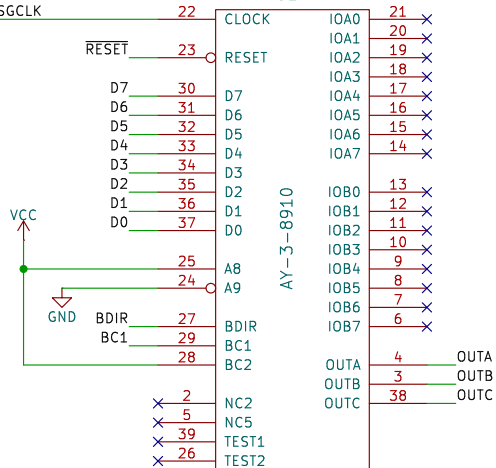


Unused Gates

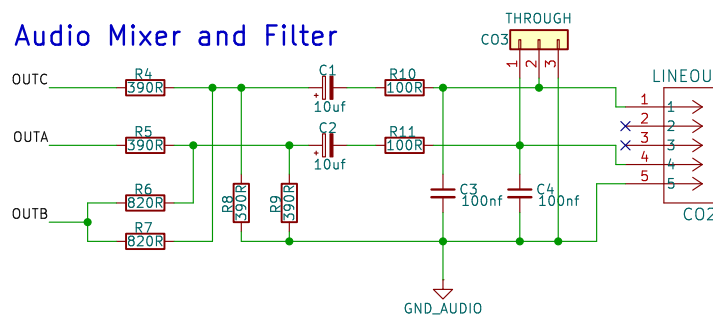
## Clock Divider



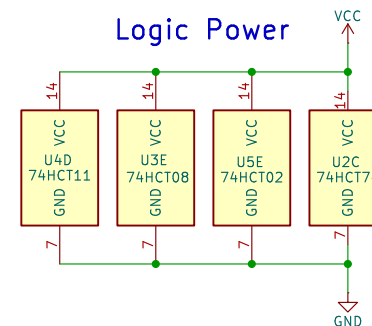
## PSG



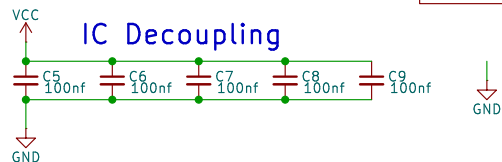
## Audio Mixer and Filter



## Logic Power



## IC Decoupling



Channel A is Left  
Channel C is right  
Channel B (normally noise on ZON-X) is mixed

BDIR	BC1	IOREQ	XXQFH	INACTIVE
0	0	/IORW	XXQFH	READ PSG
1	0	/IORW	XXQFH	WRITE PSG
1	1	/IOWR	XXQFH	LATCH ADDRESS

XXQFH = 0FH = 00001111  
XXQFH = DFH = 10001111

Sheet: /  
File: JONZON-X-81.kicad\_sch

Title: JONZON-X-81

Size: A4 Date: 2025-08-01

KiCad E.D.A. 9.0.3

Rev: 1.2

Id: 1/1