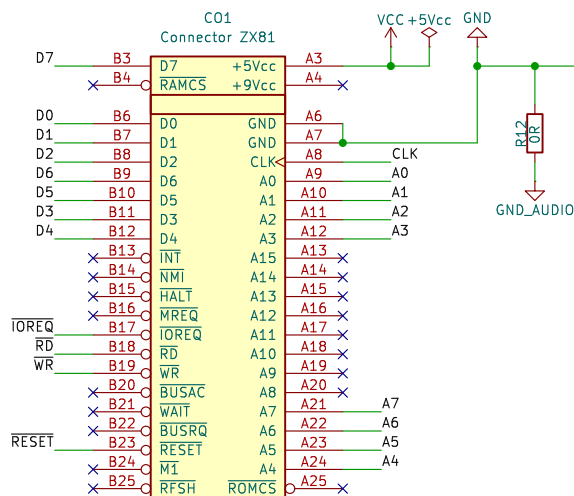
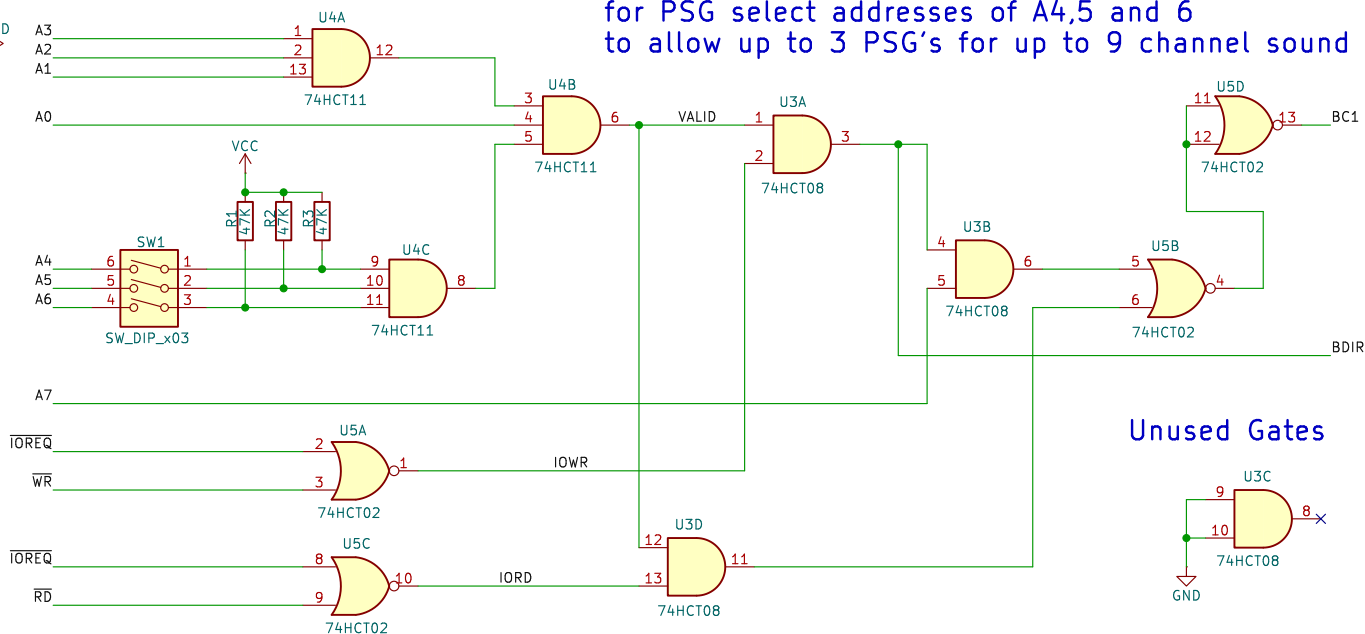


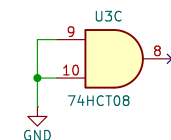
## ZX81 Edge Connector



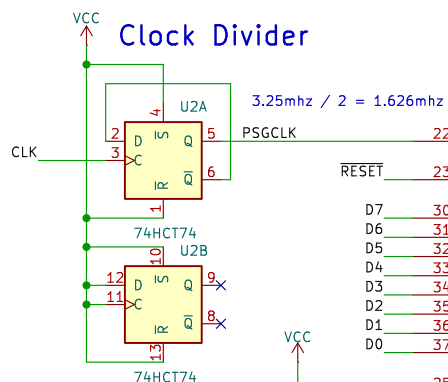
Address Decoding with DIP Switch Options for PSG select addresses of A4,5 and 6 to allow up to 3 PSG's for up to 9 channel sound



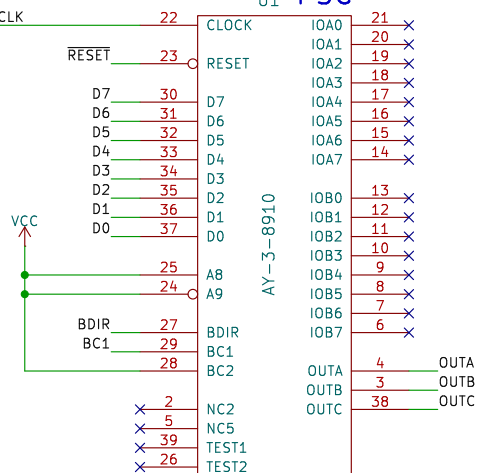
Unused Gates



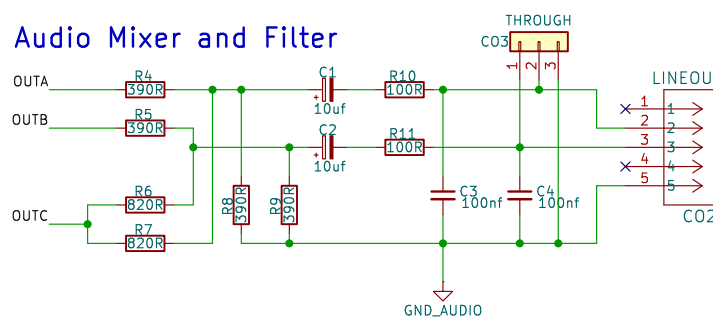
## Clock Divider



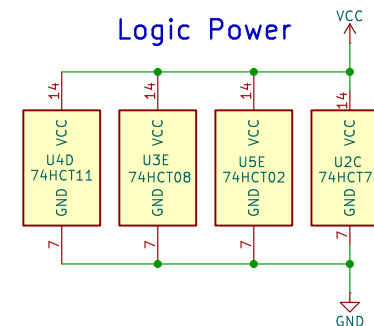
## PSG



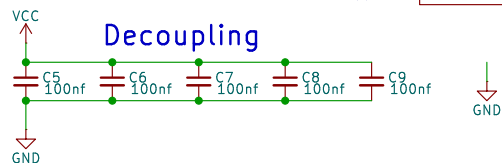
## Audio Mixer and Filter



## Logic Power



## Decoupling



Channel A is Left  
Channel B is right  
Channel C (normally noise) is mixed

| BDIR | BC1 | IOA0 | IOA1 | IOA2 | IOA3 | IOA4 | IOA5 | IOA6 | IOA7 | IOB0 | IOB1 | IOB2 | IOB3 | IOB4 | IOB5 | IOB6 | IOB7 | OUTA | OUTB | OUTC | OUTD |
|------|-----|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| 0    | 0   | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    | 0    |
| 1    | 1   | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    | 1    |      |

XX0FH = 0FH = 00001111  
XXDFH = DFH = 10001111

Sheet: /  
File: MyZON-X.kicad\_sch

Title: JonZON-X

Size: A4 Date: 2025-08-01

KiCad E.D.A. 9.0.3

Rev: 1.0

Id: 1/1