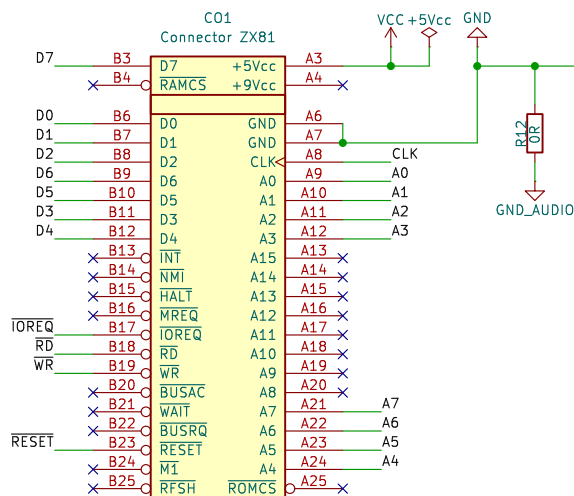
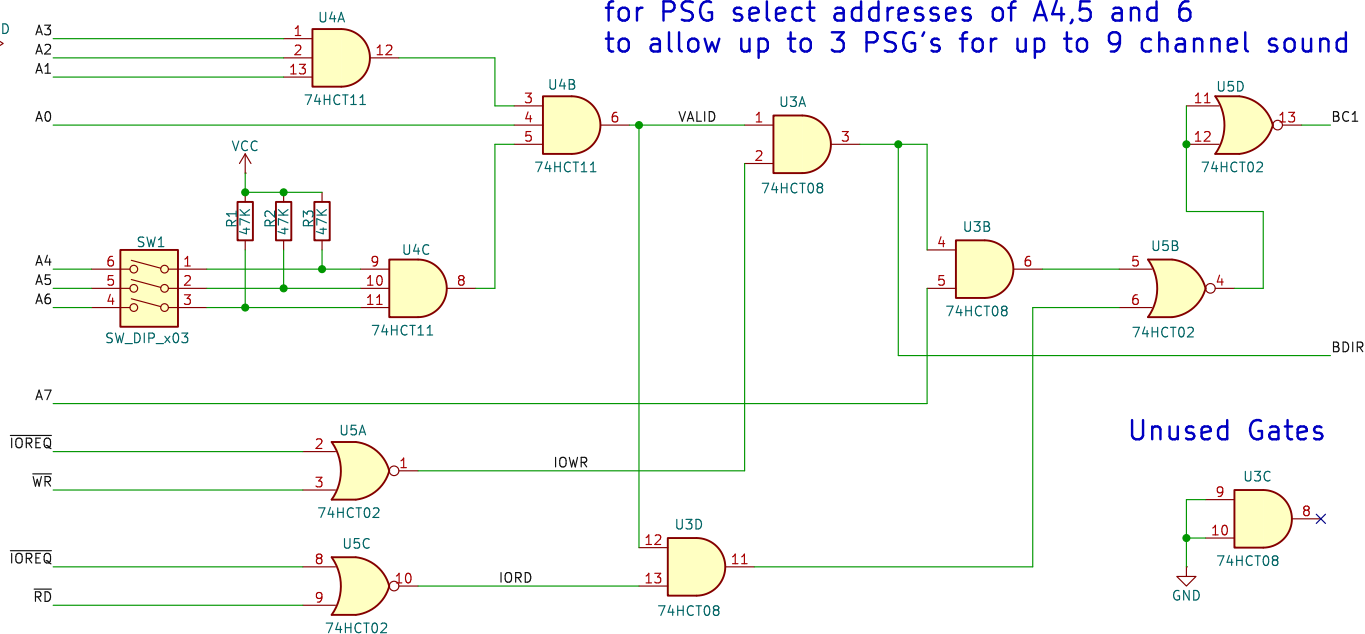


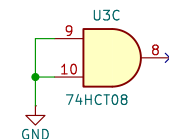
## ZX81 Edge Connector



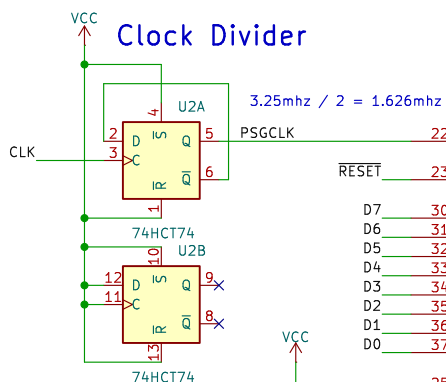
Address Decoding with DIP Switch Options for PSG select addresses of A4,5 and 6 to allow up to 3 PSG's for up to 9 channel sound



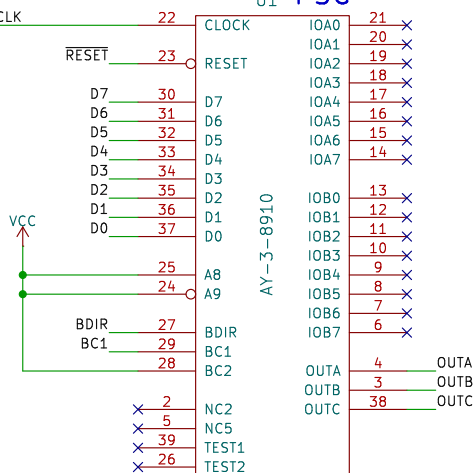
Unused Gates



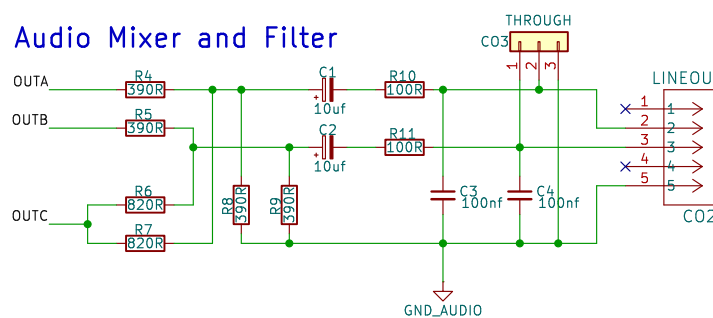
## Clock Divider



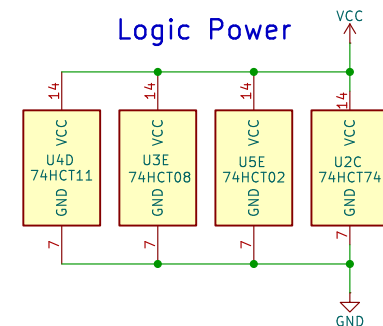
## PSG



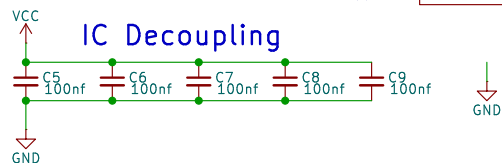
## Audio Mixer and Filter



## Logic Power



## IC Decoupling



Channel A is Left  
Channel B is right  
Channel C (normally noise) is mixed

BDIR	BC1	IO/RD	XX0FH	INACTIVE
0	0	0	0	READ PSG
0	1	0	0	WRITE PSG
1	1	0	0	LATCH ADDRESS
			XX0FH	
			XXDFH	
			XXDFH	

XX0FH = DFH = 00001111  
XXDFH = DFH = 10001111

Sheet: /  
File: JONZON-X-81.kicad\_sch

Title: JONZON-X-81

Size: A4 Date: 2025-08-01

KiCad E.D.A. 9.0.3

Rev: 1.0

Id: 1/1