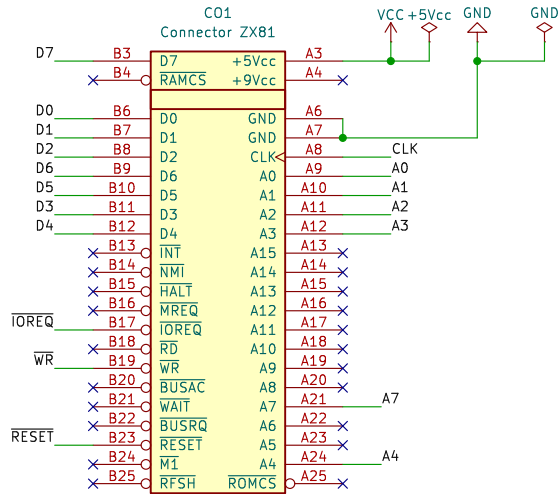
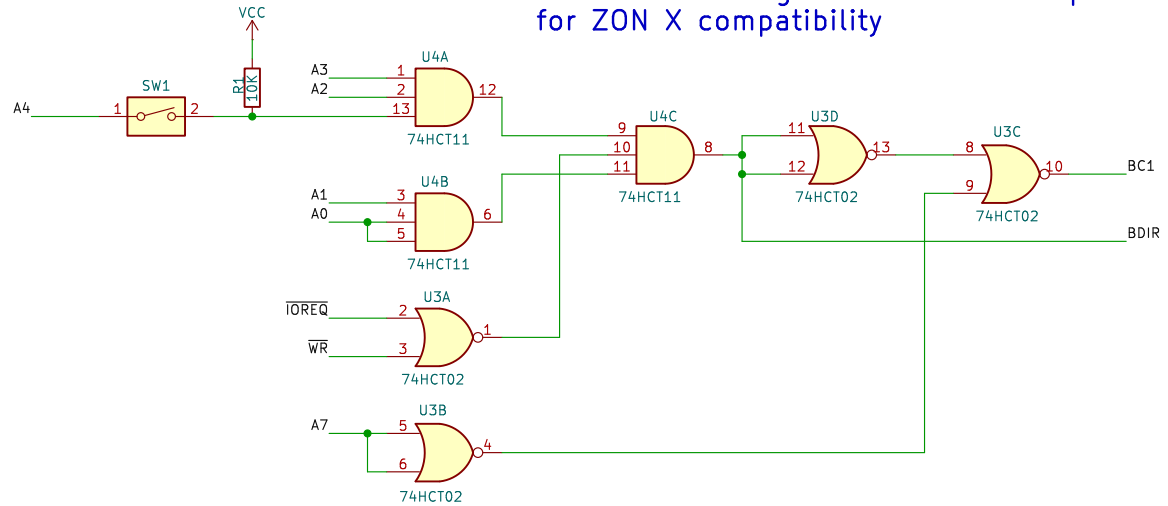


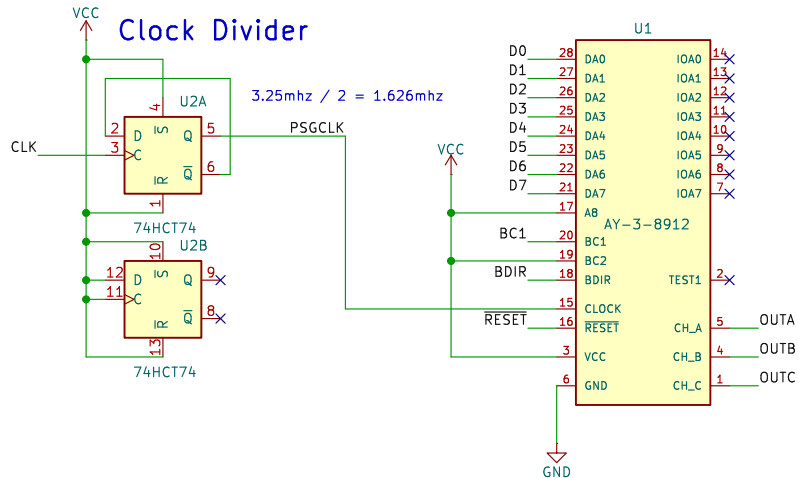
ZX81 Edge Connector



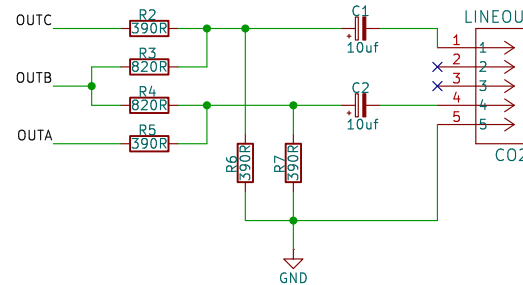
Address Decoding with DIP Switch Option for ZON X compatibility



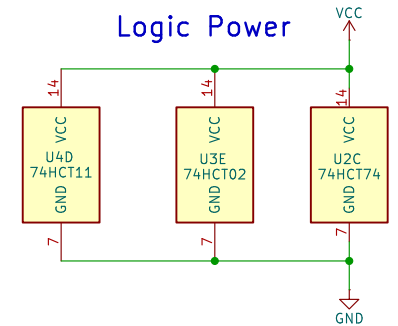
PSG



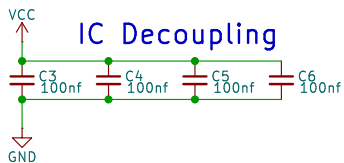
Audio Mixer and Filter



Logic Power



IC Decoupling



Channel A is Left
Channel C is right
Channel B (normally noise on ZON-X) is mixed

BDIR	BC1	
0	0	INACTIVE
0	1	/IORW XXDFH READ PSG
1	0	/IORW XXDFH WRITE PSG
1	1	/IOWR XXDFH LATCH ADDRESS

XXDFH = DFH = 00001111
XXDFH = DFH = 10001111

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File: JONZON-X-81.kicad_sch

Title: JONZON-X-81

Size: A4 Date: 2025-09-05

KiCad E.D.A. 9.0.4

Rev: 2.0

Id: 1/1