1. in figs/ folder
2. It’s hard predict from the data. One hypothesis could be the loop counter register, since it needs to be written only once but could be read anywhere inside the loop. We can add more registers and use register renaming in order to remove a few long term dependencies which can be exploited to speed up out of order execution.
3. Architecture A have lower percentage of small dependence distance. Which hints at the fact that there is less register reuse going on, i.e. it has more number of architectural registers.