

Glen Muthoka Mutinda

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• [Bananz0](#)

Professional Summary

Final-year Electrical & Electronics Engineering student (Expected First Class Honours) with extensive hands-on experience in embedded C/C++ programming, bare metal firmware development, and low-level hardware interfaces. Proven track record developing space-grade software for lunar CubeSat missions and real-time FPGA-based DSP systems. Strong electronics foundation spanning IC design through PCB-level implementation. Seeking graduate embedded software engineer position in Bristol.

Education

University of Southampton <i>BEng Electrical & Electronics Engineering (Expected First Class Honours)</i>	Southampton, UK 2023–2026
Key Modules: Embedded Systems, Digital Systems Design, IC Design, Signal Processing (DSP), Control Systems, Real-Time Systems	
Final Year Project: TSMC 65nm IC Design & Fabrication - Complete RTL to silicon flow (Team Lead)	
ONCAMPUS Global <i>Undergraduate Foundation Programme, Engineering Pathway (Distinction)</i>	Southampton, UK 2022–2023

Professional Experience

ARTEMIS Small Sat-1 Lunar CubeSat Project <i>Junior Software Engineer - Embedded Systems</i>	University of Southampton Sep 2023–Jun 2025
<ul style="list-style-type: none">○ Developed bare metal flight software in C/C++ for ARM Cortex-M microcontroller, directly managing interrupts, DMA, and peripheral registers without OS abstraction○ Reduced system latency by 6% through low-level optimization: eliminated context switches, minimized interrupt overhead, and optimized critical path timing○ Implemented custom device drivers for SPI-based IMU sensors, I2C temperature monitors, and UART telemetry, achieving <10s jitter○ Enhanced system reliability by 20% through comprehensive unit testing and hardware-in-the-loop validation○ Conducted code reviews following NASA coding standards and MISRA C guidelines for safety-critical systems	

Technical Projects

16-Stage FIR Notch Filter - Real-Time DSP on FPGA: Nov 2024–Dec 2024

Platform: Altera Cyclone V | **Language:** SystemVerilog

Designed real-time digital filter for stereo audio with -40dB notch depth. Achieved 19-cycle latency (950ns at 50MHz) from ADC to DAC. Created MATLAB scripts for FIR coefficient generation using Parks-McClellan algorithm. Directly interfaced with ADC/DAC via SPI, managing chip select timing and 48kHz sample synchronization.

github.com/Bananz0/16-Stage-FIR-Notch-Filter

WattsApp - Bare Metal Smart Energy System: Jan 2025–Mar 2025

MCU: ESP32 | **Protocols:** I2C, SPI, MQTT, Modbus RTU | **Language:** C

Developed bare metal firmware for IoT smart meter monitoring solar, battery, and grid power. Implemented low-level drivers for INA219 (I2C) and ACS712 (ADC) sensors achieving $\pm 0.5\%$ accuracy. Managed dual-core architecture with FreeRTOS task scheduling. Achieved 12% reduction in peak consumption through intelligent load shifting algorithms.

github.com/Bananz0/WattsApp

TSMC 65nm CPU Design - Complete IC Fabrication Flow: Sep 2024–Jun 2025

Role: Team Lead | **Tools:** S-Edit, T-Spice, L-Edit, Calibre DRC/LVS
Led IC design from RTL to GDSII tape-out for TSMC 65nm process. Designed CPU datapath at transistor level, performed SPICE simulations for timing verification, and conducted full physical verification. Successfully submitted for fabrication with zero DRC violations and 15% area reduction.

PiBoard - Real-Time Collaborative Whiteboard: Apr 2024–May 2024

Platform: Raspberry Pi | **Language:** C++/Qt

Developed custom binary serial protocol over GPIO UART for real-time drawing synchronization. Directly configured BCM2835 GPIO registers for hardware UART control. Achieved <100ms update latency with support for 4 concurrent users.

🔗 github.com/Bananz0/PiBoard

ControlCraft - Control Systems Analysis Toolbox: Nov 2024–Dec 2024

Platform: MATLAB | **Focus:** DSP & Control Theory

Built educational tool for control systems analysis with interactive PID tuning, Bode/Nyquist plots, and automated stability analysis. Demonstrates strong DSP fundamentals. Deployed in undergraduate labs serving 60+ students.

🔗 github.com/Bananz0/ControlCraft

MIPSquare - MIPS Processor Simulator: 2024

Language: C++

Built cycle-accurate MIPS simulator implementing instruction decoding, register file management, and pipeline hazard detection. Demonstrates understanding of processor architecture and low-level instruction execution.

🔗 github.com/Bananz0/MIPSquare

Open Source Contributions

Galaxy Book Enabler: PowerShell tool with 8,300+ downloads, 350+ GitHub stars - Windows system automation

eGPU Auto-Enabler: Background service achieving 99.9% reliability with <50ms detection latency

Technical Skills

Programming: C (bare metal firmware), C++, Python, SystemVerilog, Assembly (ARM/MIPS), MATLAB

Hardware Interfaces: SPI, I2C, UART, GPIO, ADC/DAC, PWM, DMA, Interrupt handling

Protocols: Modbus RTU/TCP, MQTT, CAN bus

Microcontrollers: ESP32 (Xtensa LX6), ARM Cortex-M, Arduino (AVR/ARM), STM32, Raspberry Pi

FPGA/Digital: Quartus Prime, Vivado, ModelSim, SystemVerilog, VHDL, FSM design

IC Design: TSMC 65nm, Cadence S-Edit/L-Edit/T-Spice, Calibre (DRC/LVS), GDSII tape-out

PCB Design: KiCad, EAGLE, Altium (basic)

DSP & Control: FIR/IIR filters, FFT, PID tuning, Bode/Nyquist analysis, MATLAB toolboxes

Embedded OS: FreeRTOS, bare metal (no OS), embedded Linux (basic)

Tools: Git, GDB, JTAG/SWD, oscilloscopes, logic analyzers, VS Code, CLion

Standards: MISRA C, NASA coding standards

Professional Memberships & Activities

2024–Present: Member, Institute of Electrical and Electronics Engineers (IEEE)

2024–Present: Member, Information Systems Audit and Control Association (ISACA)

2024–2025: Active open-source contributor and Engineering Society representative

Additional Information

Languages: English (Native), Swahili (Native)

Right to Work: UK Student Visa (valid until 2026), eligible for Graduate Route visa