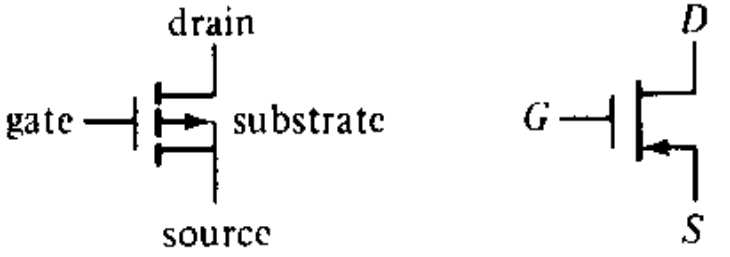
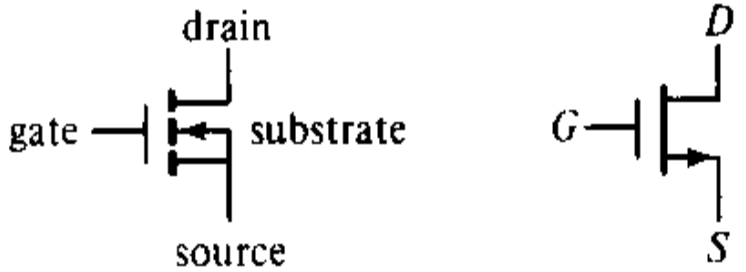
Metal oxide semiconductor



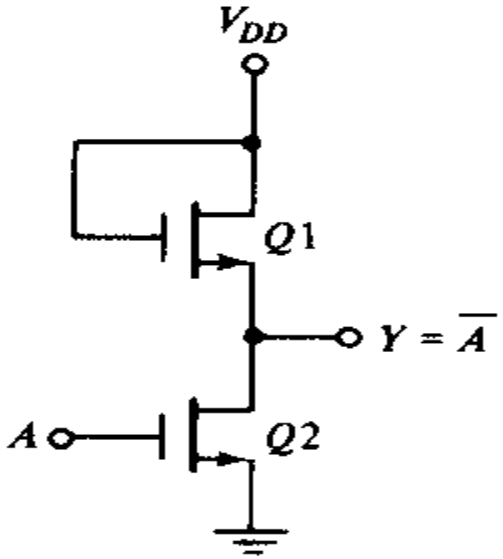
(a) p-channel



(b) n-channel

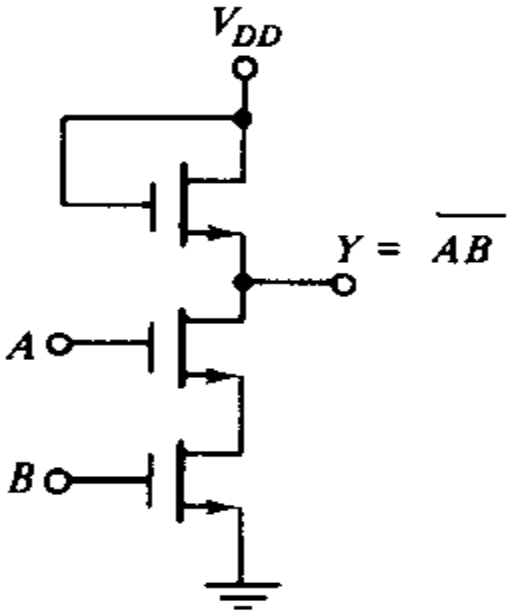
Inverter (NOT gate)

X	Y
0	1
1	0



NAND gate

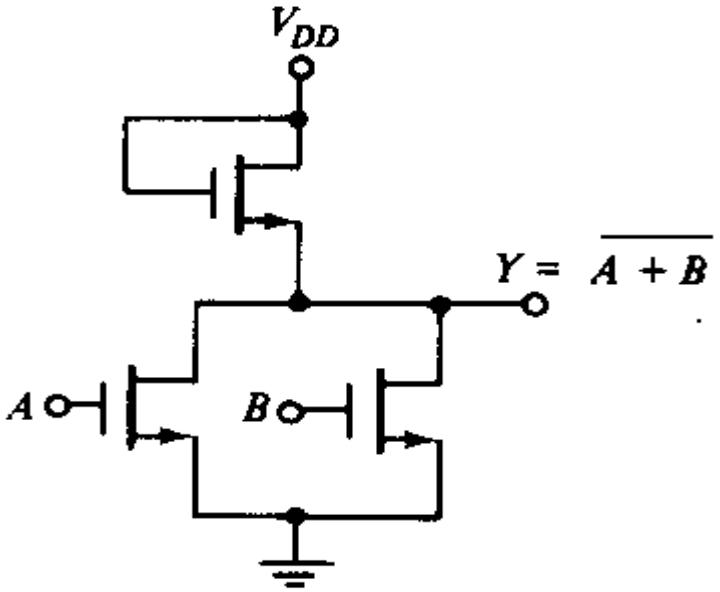
A	B		O/P
0	0		1
0	1		1
1	0		1
1	1		0



(b) NAND gate

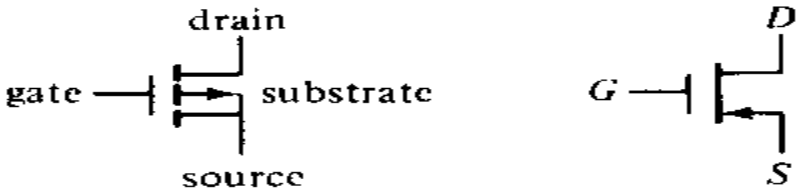
NOR Gate

A	B		O/P
0	0		1
0	1		0
1	0		0
1	1		0

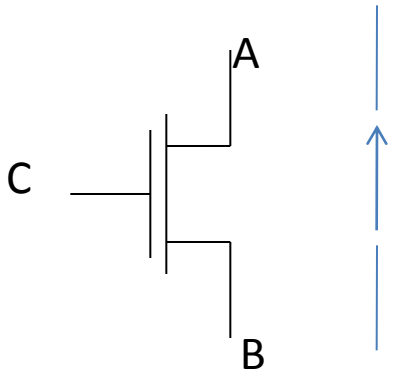


NOR gate

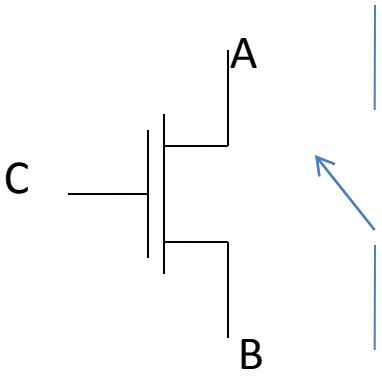
Implementing Gates Using MOSFET Integrated Circuits



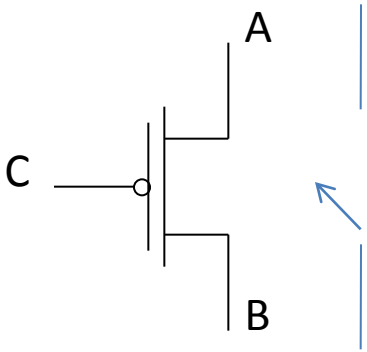
(a) p-channel



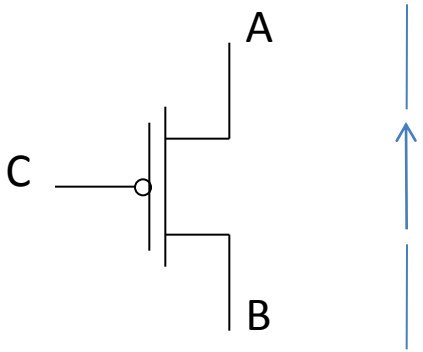
nMOS transistor
C = 1 when
A-B closed



nMOS transistor
C = 0 when
A-B open



pMOS transistor
C = 1 when
A-B open



pMOS transistor
C = 0 when
A-B Closed