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CS475

Project 5 Write Up

1. What machine you ran this on

I ran this on Flip(Linux) which has 24 processors.

1. Show the table and graph
2. What patterns are you seeing in the speedups?
3. Are they consistent across a variety of array sizes?
4. Why or why not, do you think?
5. Knowing that SSE SIMD is 4-floats-at-a-time, why could you get a speed-up of < 4.0 or > 4.0 in the array mutiplication?
6. Knowing that SSE SIMD is 4-floats-at-a-time, why could you get a speed-up of < 4.0 or > 4.0 in the array mutiplication-reduction?