

### Design Parameters:

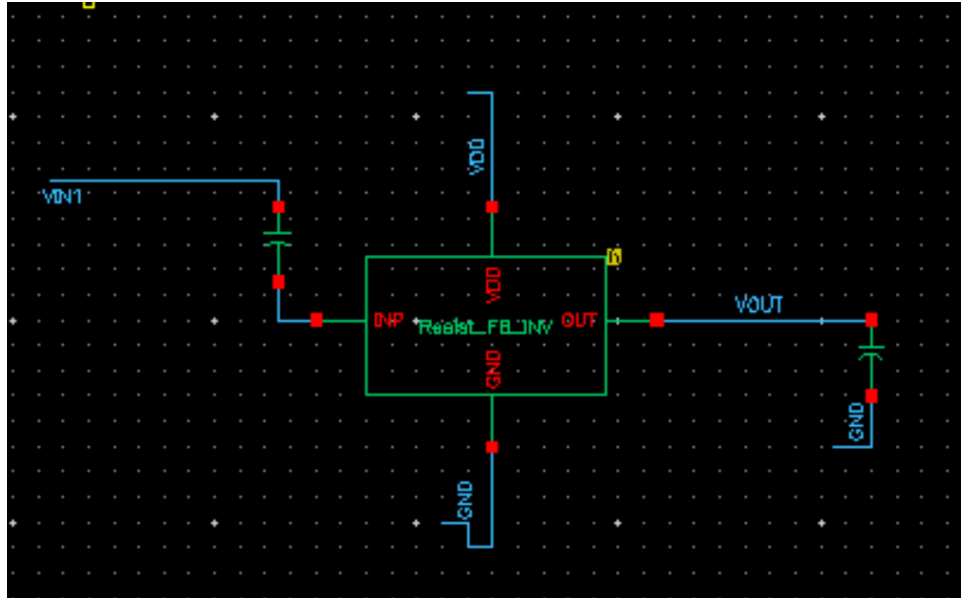
Architecture: **Resistive FB Inverter**

Technology: **Skywater OpenPDK 130nm**

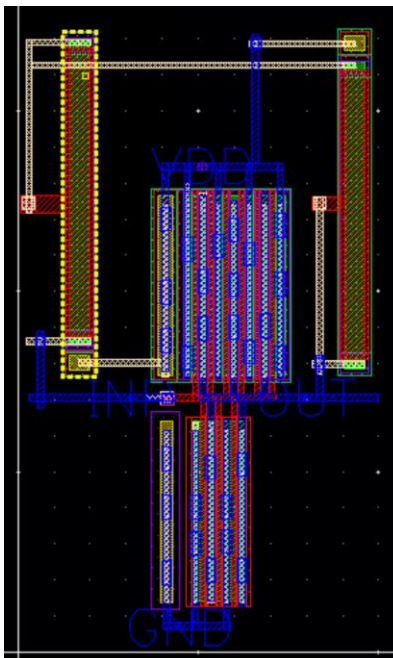
Size: **10um\*17um= 0.00017mm<sup>2</sup>**

Tools Used: **Virtuoso Cadence**

### Symbol



### Generated Layout:



## Uploaded Files:

- Gds file: Res\_FB\_Inv.gds
- Spice Netlist: Res\_FB\_Inv.sp
- PEX Netlist: Res\_FB\_Inv.pex.netlist

## Results:

