**Design Parameters:**

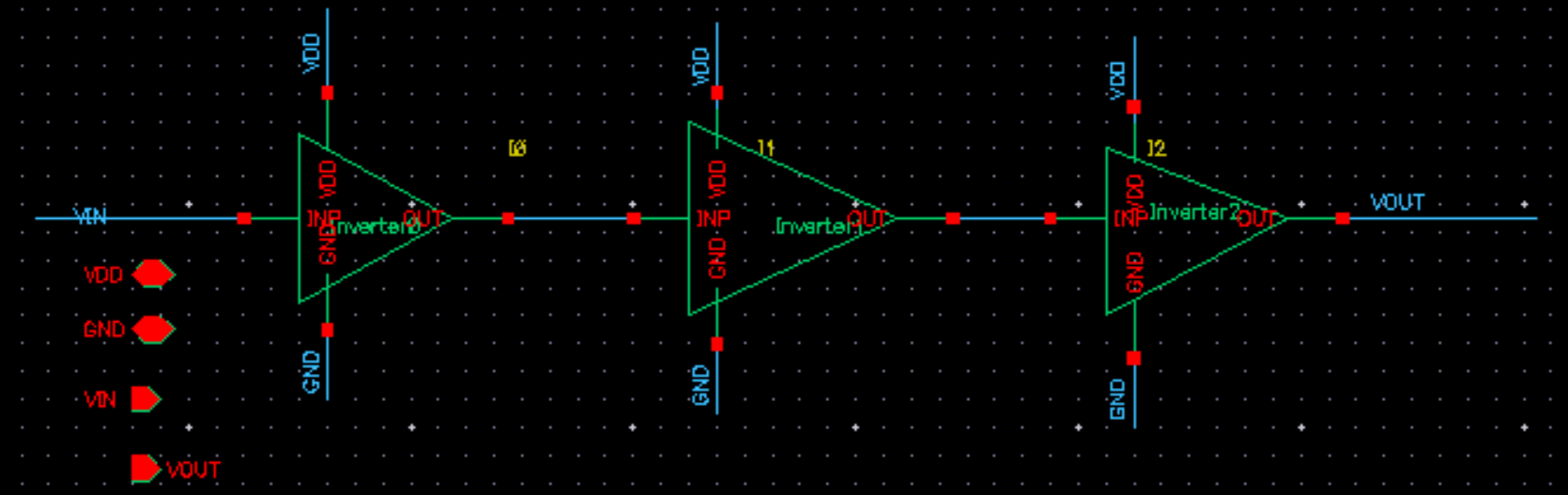
Architecture:  **Inverter based Transmitter (To drive the input cap of Channel)**

Technology: **Skywater OpenPDK 130nm**

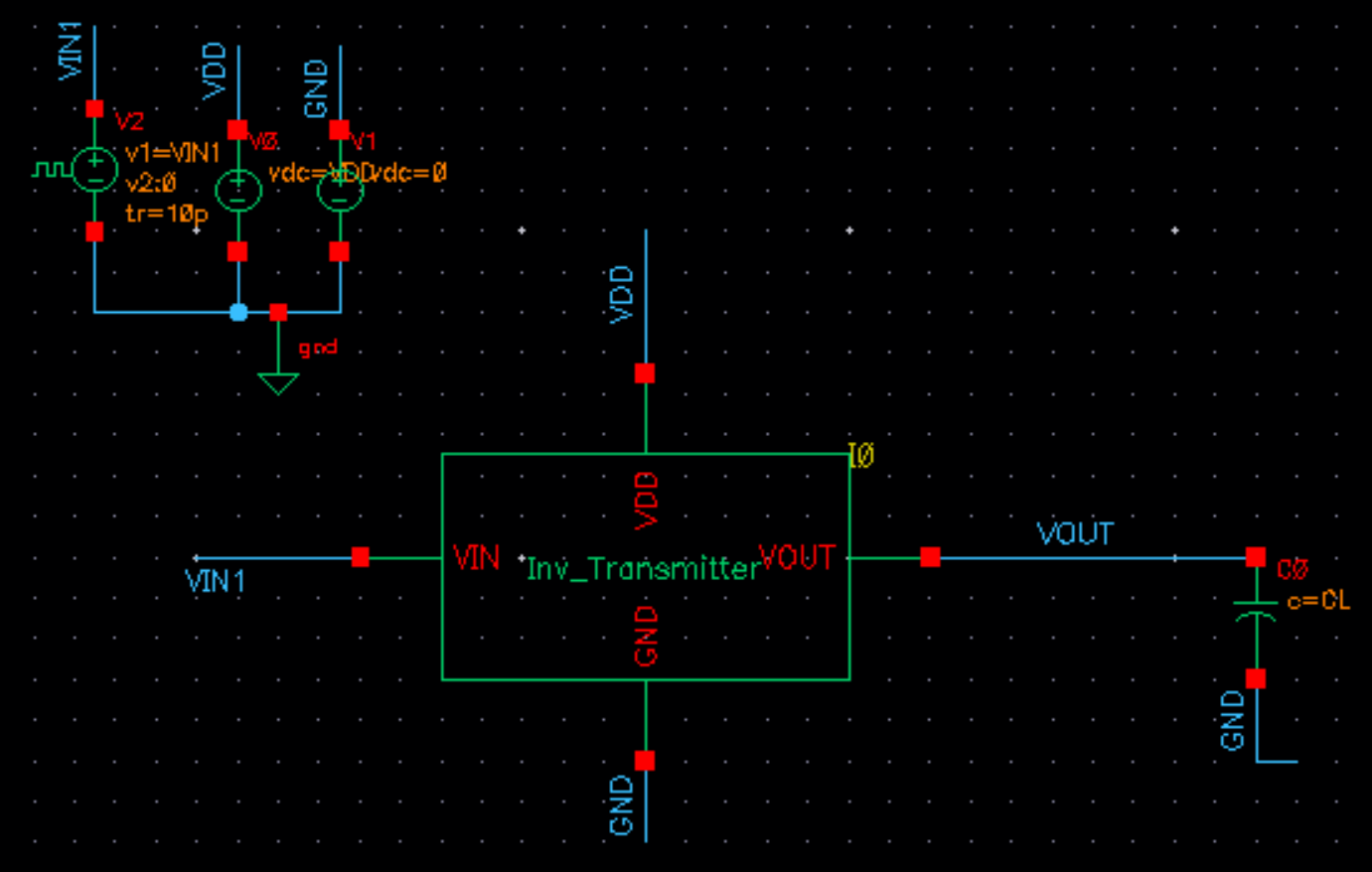
Size: **21um\*13um= 0.0003mm2**

Tools Used: **Virtuoso Cadence**

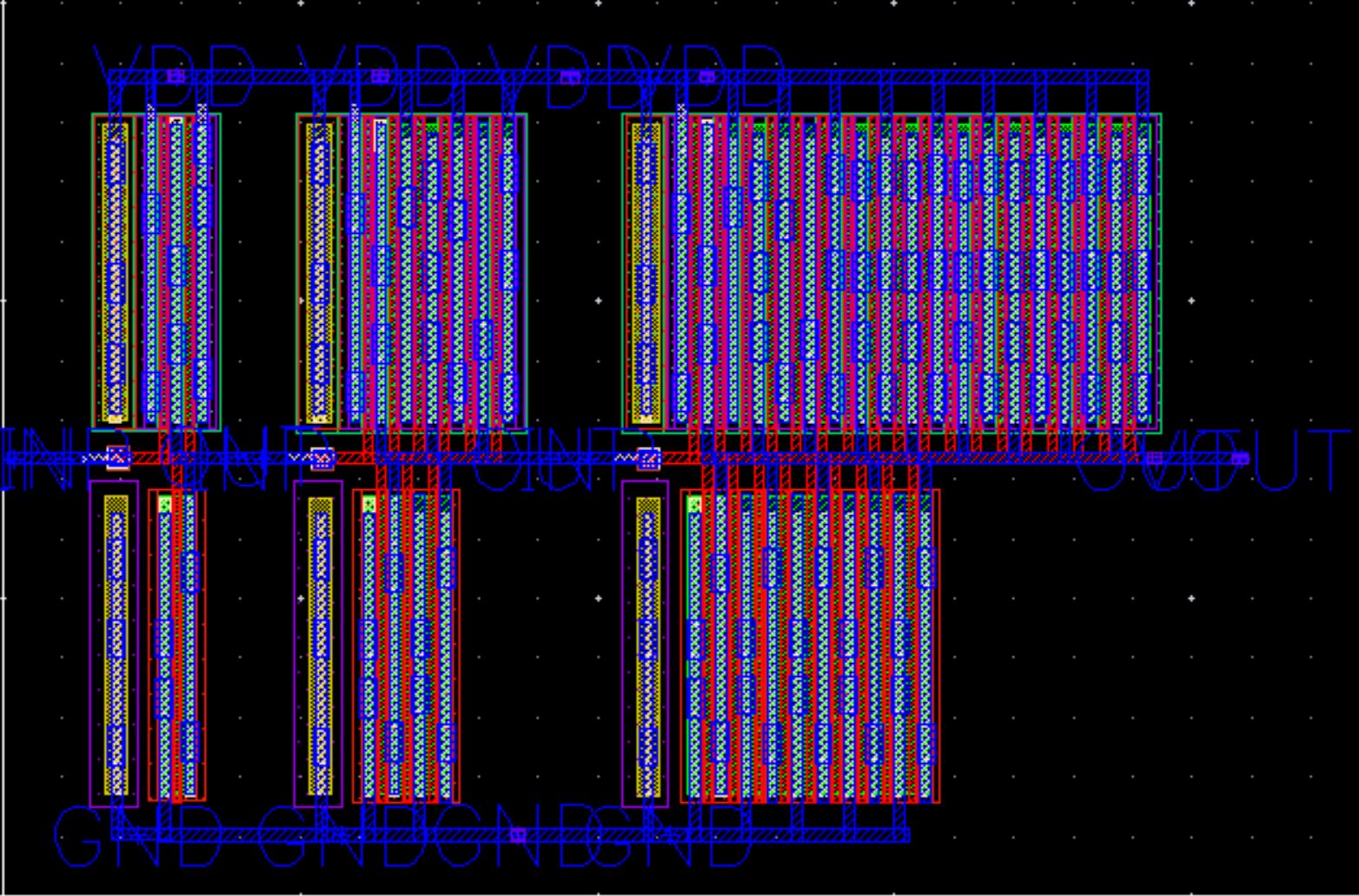
**Schematic**



**Test Circuit**



**Generated Layout:**

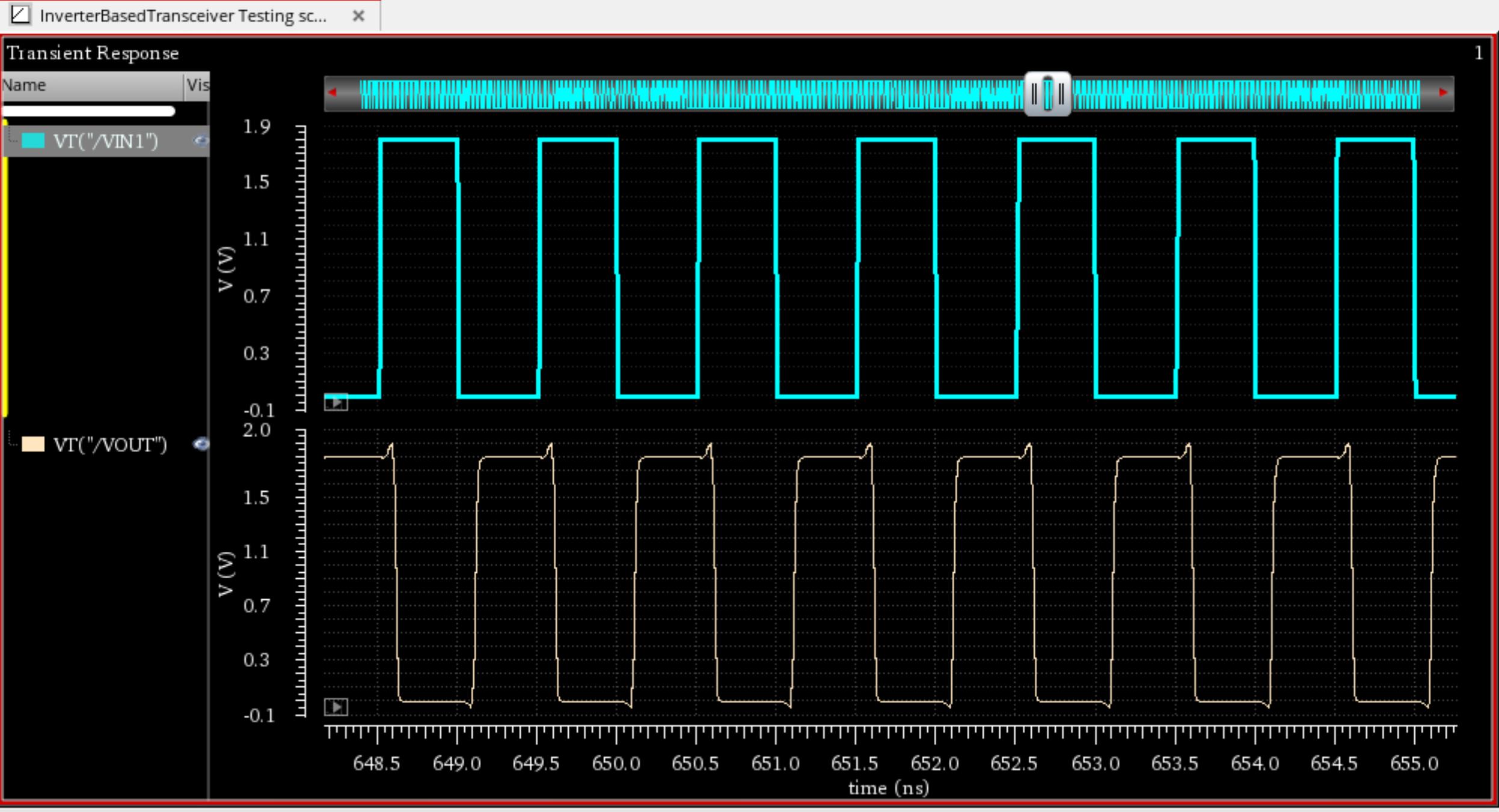


**Uploaded Files:**

* Gds file: Inv\_Transmitter.gds
* Spice Netlist: . /FULL\_TX/ Inv\_Transmitter.sp
* PEX Netlist: . /FULL\_TX/ Inv\_Transmitter.pex.netlist

Also the files required to view the schematic, layout and calibre views in cadence are also present.

**Results:**



**Data\_in**

**Data\_out**