

Review Article





Design of an ECG sensor circuitry for cardiovascular disease diagnosis

Abstract

ECG biosignal conditioning (BC) is critical because it directly affects measurement accuracy, reliability, and repeatability. It also presents a great challenge due to the small amplitude of ECG raw signals and their ease of corruption with noise and other disturbances. This paper describes how an ECG biosignal conditioning circuitry was designed, built, and tested. The circuit consists of an instrument amplifier (AD8220), a 1st-order active high-pass filter (containing a MCP6271 op-amp and a RC filter in Sallen-Key configuration), a 5th-order active Bessel low-pass filter (consisting of a 1st-order LPF and two 2nd-order Sallen-Key filters), and a Twin-T active notch filter (combining two "T" shape RC filters with a MCP6271 op-amp). A right leg drive circuit was added in order to cancel the common-mode signal between the left and right arm electrodes. A power supply circuit provides a ± 5 V DC source for the system using two 9V batteries and two voltage regulators (NTE977 regulates +9V power supply to +5V; NTE1917 regulates -9V supply to -5V). Data acquisition and sampling were performed using a USB6009 module with a built-in A/D converter. Testing of a real electrocardiogram from a human subject was performed on the designed BC circuit, which has indicated that the developed BC circuitry can preserve useful ECG information while removing unwanted noise and interference components.

Keywords: biosignal conditioning, ECG, passive and active filters

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Introduction

Sensors have been playing key roles in all aspects of health care, especially Cardiovascular disease (CVD) -the disease that is the No.1 killer in the United States and the single leading disease that ended 17.3 million people's lives worldwide in 2013.1 Unlike many other diseases, some CVDs can cause sudden death, especially in people who physically appear heathy and fit. Early diagnosis and warning, therefore, are extremely important to uncover hidden or silent heart diseases and prevent sudden death. In addition, the cost for detecting and treating cardiovascular disease is very high, thus early diagnosis and prevention post a significant financial benefits as well. Sensing and monitoring technologies for heart deceases have attracted great attention from health care, engineering, industry and information processing societies.

The sensor technology and development for CVD studies and diagnosis mainly focus on

- i. Detect heart pulse waveforms (e.g., ECG, TCPD);
- ii. Measure blood properties (e.g., pressure, viscosity, contents and their percentage);
- iii. Create heart or vessel images (e.g., CT, MRI, Ultrasonic); and
- iv. Establish wireless sensor network.

The electrocardiogram (EKG or ECG) is a graph that records the electrical activity of the heart. Its amplitude has a range of 0.1mV-10mV; while its frequency falls into 0.01Hz-250Hz. ECG is an important tool used for support of diagnosis and treatment of various cardiac and other related diseases:

Be the first indicator of ischemia and help in proper use of thrombolysis in treating ischemia.

- i. Help in diagnosis of the cause of the chest pain.
- ii. Help in diagnosis of the cause of the dyspnea.
- iii. Help in diagnosis of electrolyte disturbances.
- iv. Help in diagnosis of adverse drug effects.
- v. Help in diagnosis of structural changes of the heart.
- vi. Identify a potentially life-threatening arrhythmia.
- vii. Pinpoint the chronic effects of sustained hypertension or the acute effects of a massive pulmonary embolus.

An ECG is usually obtained by means of metal electrodes placed on the surface of the body. However, the raw ECG signals directly from the electrodes are inherently weak (0.001mV-100mV with a typical value of 1mV. They are easily corrupted with noise and other disturbances such as power line interference, impulse noise, electrostatic potentials, stray capacitance, and nearby electronic devices. Biosignal artifacts can also be introduced into an ECG by subject movement and muscle tension.² All of these facts have posted a great challenge on ECG data. The biosignal conditioning (BC), therefore, has played a key role in ECG and many other biomedical instruments and biosensors. A well designed BC circuit can significantly improve measurement accuracy, reliability, and repeatability.

Several works on ECG sensing have been reported in the literature.3-12 Tenedero et al.3 developed an ECG circuit with a bandwidth of 0.05Hz - 40Hz. An AD620 instrumentation amplifier (IA) was employed due to its low noise, low input bias current, low offset voltage, low power, and high Common Mode Rejection Ratio (CMRR) of 100dB.3 Three filters were used between the IA and the data acquisition unit: an isolation amplifier (isolates 60Hz power line and also protects the patient from cardiac shock), a high-pass filter



with a cutoff frequency of 0.05Hz, and a low-pass filter with a cutoff frequency of nearly 100Hz. The sampling rate in the ADC (analog to digital conversion) for the ECG signal is 500Hz. Fulford-Jones et al.4 designed a portable, low-power ECG system.4 An operational amplifier (op-amp) embedded in a single INA321 chip was used due to its low noise and low power consumption. This op-am has a CMRR of 94dB. A high-pass feedback filter corrects any DC shift occurring over time. Their ADC sampling rate is 120Hz. Matviyenko⁵ used a CY8C27443 microcontroller for ECG signal acquisition and processing. The IA embedded in the controller has a CMRR of 60dB. According to the author, this low CMRR was acceptable due to the unique design which placed a differential low-pass filter before the IA to reduce radio frequency interference (RFI), because RFI error cannot be filtered out after the ECG signal has been rectified by the IA. A 2kHz cutoff frequency high-pass filter was placed at the output of the IA. A buffer amplifier and an inverting amplifier were also used to cancel the RFI interference. The ADC sampling rate is 240Hz. Two industry leaders, Texas Instruments (TI)6 and Analog Devices Inc. (AD),⁷ also developed the ECG conditioning circuits. TI's circuitry features an INA321 IA with several unique features: a power down mode that shuts down the circuit when the supplied current is less than 1 mA (for power saving), embedded op-amps in the microcontroller, a feedback loop that maintains a constant DC level, and a 512Hz sampling rate. Further filtering was digitally implemented to remove power line noise and to provide a pass band of 6 Hz-30Hz. In AD's design, the ECG circuit uses AD's AduC842 (an integrated "system on chip") to perform amplification, digital filtering, and A/D conversion. This paper discusses the design of an electrocardiogram (ECG) sensor circuitry for cardiovascular disease diagnosis. The paper starts with obtaining the real ECG signals (Section II), followed by signal conditioning circuit design and implementation (Section III). ECG Signal testing and results using the designed BC circuit are described and shown in Section IV. Finally Section V draws the conclusions.

Obtaining an real ECG signal

An Electrocardiogram (ECG) is usually recorded by electrodes placed at specific points on the body. The electrode used for recording the ECG signal is silver-silver chloride (Ag-AgCl). It has the following important features: (1) it is non-polarizable, meaning that current flows freely across the electrode junction. No electrons accumulate in the junction as in a polarizable electrode; (2) it generates less noise (<10μV). The Ag-AgCl electrode has an AgCl layer deposited on an Ag plate. The Cl⁻ ions move in the human body (in the electrolyte). In the AgCl layer, these Cl⁻ ions are converted to electron flow in the Ag plate and these electrons are then sent out through a connecting wire. This Ag-AgCl structure reduces the DC offset potential to a very small value. A conductive gel is used to minimize the disturbance of the double charge layer. Three limb leads are commonly used to construct an Einthoven's triangle (Figure 1).13

An ECG wave can be recorded by placing leads at electrically equidistant points on the body from the heart, thus maximizing the potential difference between leads.14 Lead I is configured as the positive electrode on the left arm (L), the negative electrode on the right arm (R); Lead II is configured as the positive electrode on the left leg/foot(F), the negative electrode on the right arm (R); Lead III sets the positive electrode on the left leg/foot (F), the negative electrode on the left arm (L). V_{P} V_{IP} and V_{III} denote the voltage of Lead I, Lead II, and Lead III, respectively. Φ_{L}^{m} , and Φ_{F} are the voltage potentials at the points L, R, and F, respectively. The Cardiac Vector's magnitude $|\overline{P}|$ and direction α can be expressed by. ¹⁴

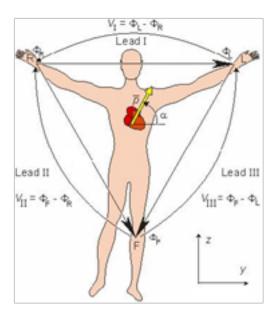


Figure I Einthoven's triangle and limb lead configuration. 13

$$|\overline{P}| = 2\sqrt{(V_I^2 + V_{II}^2 - V_I V_{II})/3}$$
 (1)

or
$$|\overline{P}| = 2\sqrt{(V_I^2 + V_{III}^2 + V_I V_{III})/3}$$
 (2)

$$\alpha = \tan^{-1} \left((V_I - 2V_{II}) / (\sqrt{3}V_I) \right) \tag{3}$$

or
$$\alpha = \tan^{-1} \left((V_{II} + V_{III}) / (-\sqrt{3}V_{I}) \right)$$
 (4)

The typical ECG signal obtained from the Ag-AgCl electrodes is 1mV in amplitude and it is easily corrupted by noise. The main sources of noise include respiration, motion artifacts, muscle contraction, electrode contact noise, power line interference, RFI, and electromagnetic interference (EMI). In certain situations, noise can completely override the ECG waves and make the amplified signal useless.

Signal conditioning circuit design and implementation

To effectively remove unwanted noise and preserve the useful components of ECG signals, the following biosignal conditioning schemes and sequence were developed:

- i. Amplify the raw ECG signal with an instrumentation amplifier to raise the signal voltage level.
- ii. Use a high-pass filter to eliminate DC offset developed between electrodes.
- iii. Apply a low-pass filter to remove high frequency noise.
- iv. Filter out power line interference using a notch filter.
- v. Convert the filtered analog ECG signal to digital for computer display and/or further digital signal processing and analyses.

A block diagram of the BC circuit is shown in Figure 2. The following sections will discuss component functions and features.

AD8220 instrumentation amplifier

An Analog Devices' AD8220 instrumentation amplifier (IA) was

used first to amplify the ECG signal obtained from the potential difference between the right and left arm electrodes (Lead I in Figure 1). The AD8220 has a wide operating range in noisy environments, low input bias current of 10 pA, high CMRR with little effect from RFI, and adjustable gain. ¹⁵ Since the voltage source V_s of the AD8220

is ± 5 V, the gain G was conservatively set at 19 to avoid both output voltage saturation, although a higher gain is achievable with this IA. At this setting, a typical 1.0mV ECG signal is amplified to 19.0mV -well below the V_s level. The gain resistor R_s is calculated by.¹⁶

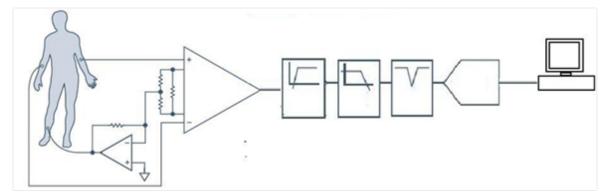
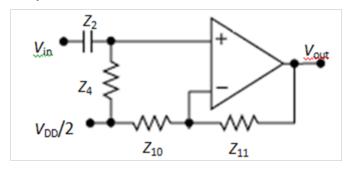


Figure 2 Overall block diagram of the ECG conditioning circuit.

$$R_g = \frac{49.4 \text{ k}\Omega}{G - 1} = \frac{49.4 \text{ k}\Omega}{19 - 1} = 2.74 \text{ k}\Omega$$
 (5)

First-order active High-pass Filter (HPF)

After the amplification, the ECG signal passes through a non-inverting active high-pass filter (HPF) to eliminate DC offset (developed between the electrodes) and to be amplified further. This active HPF consists of a MCP6271 op-amp, a RC filter (capacitor Z_2 and resistor Z_4) and the gain resistors Z_{10} and Z_{11} arranged in a Sallen-Key configuration (Figure 3). The bandwidth of the ECG recording system is very important in order to achieve an accurate recording of cardiac signals. The *American Heart Association* (AHA) recommends a minimum bandwidth of 150Hz for children between the ages of 12 to 16 years; and a minimum bandwidth of 125Hz for adults. ¹⁷



 $\textbf{Figure 3} \ \ I^{\text{st}}\text{-}order \ HPF \ with \ Sallen-Key \ configuration}.$

Based on these recommendations, the ECG signal was set to a

frequency range (bandwidth) of 0.01-100Hz. To ensure the most useful ECG signals pass through the HPF, a cutoff frequency $f_{\rm c}$ of 0.033Hz, instead of 0.01Hz, was chosen in consideration of manufacturing tolerances for electronic components. Z_2 was set to 6.8 μ F for a faster response. Thus, the resistor Z_4 has a value of:

$$Z_4 = \frac{1}{2\pi Z_2 f_c} = \frac{1}{2\pi (6.8 \times 10^{-6})(0.033)} = 710 \text{ k}\Omega$$
 (6)

In addition, Z_{10} and Z_{11} were set to 806Ω and $13k\Omega$ respectively. The resulting HPF gain, $G_{\rm HPF}$ is:

$$G_{HPF} = 1 + \frac{Z_{11}}{Z_{10}} = 1 + \frac{13000}{806} = 17.13$$
 (7)

Fifth-order active bessel low-pass filter

After the HPF, the ECG signal is sent to a low-pass filter (LPF) to remove its high frequency noise components. 160 Hz was chosen as the cutoff frequency based on the normal ECG frequency range as well as the AHA guidelines for minimum ECG frequency bandwidth. The LPF is a cascaded 5th-order active Bessel filter which has excellent transient and linear phase response. In comparison, other filter types, like the Chebychev, have overshoot (ripple) in their passband magnitude response and transient response. An active filter kit from *Microchip Technology* was employed for the filter setup. A total of three active filters were needed to form this 5th-order Bessel filter (Figure 4).

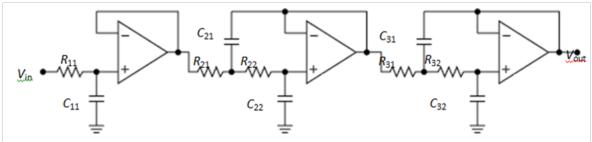


Figure 4 Circuit diagram of the 5th-order active Bessel LPF.

123

The first filter is an active 1st-order LPF which has a real pole in its transfer function; the second and the third filters both have 2nd-order Sallen-Key topologies (each has a pair of complex conjugate poles in their transfer function). All three filters use unity gain (the Sallen-Key topology inherently has excellent gain accuracy as a unity-gain buffer). The unity gain configuration also requires less components-two resistors versus four resistors in non unity-gain designs, thus reducing thermal noise caused by resistors. The resistors and capacitors in the LPF were determined as follows, considering Filter 2 first:

I. Set
$$f_c = 160 \text{ Hz}$$
:

$$f_c = \frac{1}{2\pi\sqrt{R_{21}R_{22}C_{21}C_{22}}} = 160 \text{ Hz}$$
 (8)

II. Let m be the ratio of R_{21} and R_{22} , n the ratio of C_{21} and C_{22} , i.e.:

$$\begin{cases} R_{21} = mR & C_{21} = nC \\ R_{22} = R & C_{22} = C \end{cases}$$
 (9)

III. Let Q-factor = 1 (for better filter performance at cutoff frequency without significant overshoot), i.e.,

$$Q = 1 = \frac{\sqrt{R_{21}R_{22}C_{21}C_{22}}}{C_{22}(R_{21} + R_{22})} = \frac{\sqrt{mn}}{m+1}$$
 (10)

IV. Choose m and determine n based on Q setting,

If m=1, then from (10) n=4.

V. Select C and calculate R based on f:

 $\it C$ should not be too small since low capacitor values can result in significant errors due to parasitic capacitance. Chose $\it C$ =0.068 μF , thus:

$$\begin{cases} C_{21} = 4(0.068) = 0.272 \ \mu\text{F} \\ C_{22} = 0.068 \ \mu\text{F} \end{cases}$$
 (11)

From equation (8),

$$R = \frac{1}{2\pi C f_c \sqrt{mn}} = \dot{\mathbf{U}} \frac{1}{2\pi (0.068 \times 10^{-6})(160)\sqrt{(1)(4)}} = 7.318 \text{ k}$$

$$\begin{cases} R_{21} = mR = (1)(7.318) = 7.318 \text{ k}\Omega \\ R_{22} = R = 7.318 \text{ k}\Omega \end{cases}$$

Similarly, Filter 1 can be designed by letting f_c =160Hz, Q=1, n=4.5, m=2, C=0.068 μ F, and calculating C_{11} =0.068 μ F, R_{11} =14.628 $k\Omega$; Filter 3 designed as C_{31} =0.306 μ F, C_{32} =0.068 μ F, R_{31} =9.752 $k\Omega$, and R_{32} =4.876 $k\Omega$. All op-amps in the LPF are MCP6271. This CMOS chip has a 2MHz *Gain Bandwidth Product* (GBWP) and a 65° phase margin. It also supports rail-to-rail input and output swing. ¹⁹

Notch/band-reject filter

A *notch* or *band-reject* filter is typically used in biomedical instrumentation to suppress a certain frequency or range of frequencies in signals. The 60 Hz power field interference needs to be rejected. This requires a small transition bandwidth or high Q factor to achieve the steeper roll-off. A Twin-T notch filter is one of the few RC networks capable of providing an infinite deep notch at a particular frequency. Two "T" shape RC filters combined with a MCP6271 opamp, as shown in Figure 5A, form an active notch filter. The Q factor can be raised from the usual 0.3 to 2.5 (a Q factor of 50 or more is achievable). Further, the op-amp provides low output impedance and high input impedance, making it possible to use large resistance values in the "T"" so that only small capacitors are required, even at low frequencies.

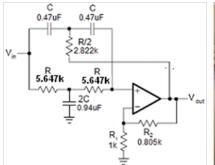






Figure 5 The Twin-T notch filter to remove 60 Hz powerline noise (A) Notch filter circuit board: passive portion (B) and the active portion (C).

With f_n =60Hz and C=0.47 μ F, the resistor value in the filter is:

$$R\dot{\mathbf{L}} = \frac{1}{2\pi f_n C} = \frac{1}{2\pi (60)(0.47 \times 10^{-6})} = 5.647 \text{ k}$$

Another advantage of the Twin-T configuration is that the quality factor, Q, can be altered via the inner gain G without modifying the notch frequency f_n . To achieve a Q factor of approximately 2.5, resistances of R_1 =1.0 k Ω and R_2 =805 Ω were chosen,

$$Q = \frac{R_1}{2(R_1 - R_2)} = \frac{1 \times 10^3}{2(1 \times 10^3 - 805)} = 2.564$$

The resulting filter gain is:

$$G = 1 + \frac{R_2}{R_1} = 1 + \frac{806}{1000} = 1.806$$

The designed notch filter is shown in Figure 5B & Figure 5C shows the passive portion of the Twin-T filter, while Figure 5C shows the active portion of the filter. The resistors placed in series on the passive portion provide a total resistance of $5.647\text{k}\Omega$; while the two resistors, R_1 and R_2 , on the active filter board set the Q factor and gain G of the filter. A +5V power supply is provided to the MCP6271 (similar to $V_{\rm dd}$ in an LPF circuit); all grounds on both the passive and active boards are connected to a 2.5V reference (similar to V dd/2 in an LPF circuit).

Right leg drive circuit

A right leg drive circuit is used to cancel the common-mode signal between the left and right arm electrodes by inverting, amplifying, and then feeding the signal back to the body through the right leg electrode. As shown in Figure 6, there are three leads connected to the body: a left arm lead, a right arm lead, and a right leg lead. Two AD708 dual op-amps are used: the first one acts as a buffer with a unity gain, and the second one acts as an inverter and amplifier (i.e., inverting amplifier). A gain of 68.19 was achieved with the $866k\Omega$ and $12.7k\Omega$ gain resistors (i.e., G=866/12.7=68.19). A 0.068μ F capacitor in the feedback loop was used to maintain the stability of the right leg drive circuit. The $499k\Omega$ resistor at the output end limits the current driven to the body for patient protection. This right leg drive circuit was built on a 3.81 cm x 3.81 cm prototyping board. The AD8220 requires a $\pm 5V$ DC power supply.

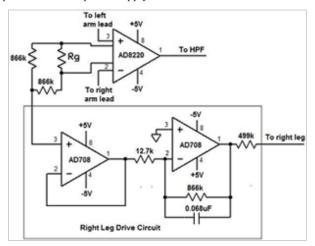


Figure 6 The right leg drive circuit using two AD708 op-amps and one AD8220.

The ±5V DC source in the BC circuit comes from a ±5V power supply circuit. It consists of two 9V batteries and two voltage regulators. The NTE977 regulates the +9V power supply to +5V, while the NTE1917 regulates the -9V supply to -5V. The overall ECG signal conditioning circuit is shown in Figure 7. The high-pass, the low-pass, and the notch filters were connected in cascading series. The ±5V power supply circuit is located between the right leg drive board and the USB6009. All circuits and evaluation boards, along with the USB 6009 data acquisition device, were mounted on a 40.64cmx20.32cm base board made of static-dissipative clear cast acrylic sheet. Each circuit or module, as well as their associated components can be easily changed or modified to adapt to different needs for different biosignals.

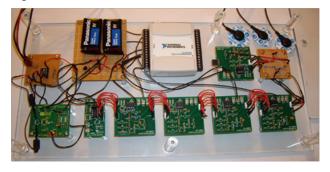


Figure 7 The signal conditioning circuit assembly.

ECG signal testing using the designed BC circuit

The designed BC circuitry was tested on a real ECG signal from a human subject. The ECG was recorded by the three Ag-AgCl electrodes attached to the body and sent to the right leg drive circuit. It then passed through the IA, the HPF, the LPF, and finally the notch filter for a series of conditioning. The ECG output at each stage was recorded, displayed, and examined. In order to examine and display the filtering result at each stage, a USB6009 unit and LabView program²⁰ were used for data acquisition, analog to digital conversion, and display. The output of each filter was captured at the V_{OUT} pin located at the top edge of each filter board and was connected to one of the terminals on the USB6009 unit. Figure 8 shows the ECG outputs at each filtering stage. The instrumentation amplifier first amplified the original ECG signal to raise its voltage level (with a gain of 19). At this stage, no visible ECG peaks were observed. After the high-pass filter and further amplification, the "bump" features of the ECG signal started to appear, although there was high frequency noise riding on the ECG output. The subsequent low-pass filter attenuated the high frequency component. However, 60 Hz power line interference was still visible. At this stage, the ECG peaks -P, R, S, T – became more distinct. The notch filter was able to remove the power line noise, resulting in a very clear and smooth ECG signal with P, Q, R, S, T peaks even more distinguishable. This final output of the ECG waveform suggested that the designed ECG conditioning circuit functioned properly and the results are comparable with commercial products.

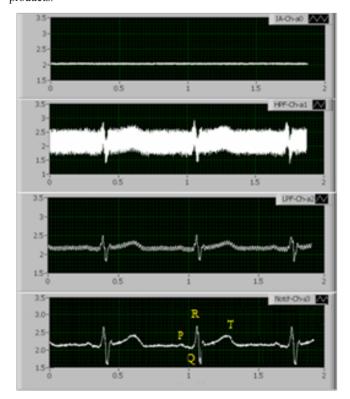


Figure 8 ECG outputs at each stage of signal conditioning- IA, HPF, LPF, and notch filter.

Conclusion

The sequential usage of the IA, the HPF, the LPF, and the notch filters have resulted a successfully conditioning a raw ECG wave from

125

a human subject. The merits of this circuity not only lie its excellent functionality, but also its flexible modularity. Each filter unit, as well as their associated components, can be easily changed or modified to adapt to other biosignals with different amplitudes and frequencies. The system could also be used to condition other non-biosignals. The device provides a useful educational platform for students to learn, step-by-step, signal conditioning principles, function and construction of active and passive filters, electronic components, interface, and LabVIEW programming tools.

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None.

Conflict of interest

The author declares no conflict of interest

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