ECE 274a Fall 2018

## Pre-lab Task 1: ALSO SUBMIT YOUR MODIFIED DATAPATH

Add your own control signals used with muxes that you use to support the shift left/right operation

Type	Opcode	funct	ALUSrc	RegDst	RegWrite	ALUOp	MemRead	MemWrite	MemtoReg	PCSrc	RegA	ReB
add	000000	100000	0	1	1	0000	0	0	0	0	0	D
sub	000000	100010	Ö		l	0001	0	0	D	0	0	D
and	000000	100100	Û	1	(	ווטס	0	0	D	0	0	0
or	000000	100101	Û	l		0100	0	0	D	0	0	O
slt	000000	101010	0	)	(	0101	0	0	D	0	O	0
sll	000000	000000	X	(		1000	0	0	D	0	U	1
srl	000000	000010	\( \	1	l	1001	0	0	D	0	l	1
clo	011100	100001	0	1		1011	0	0	O	0	C	0
clz	011100	100000	0	1		11 00	0	0	O	0	C	0
mul	011100	000010	O	1		UOID	0	0	D	0	0	Ō
addi	001000	100000		U		טטטט	0	0	D	0	0	0
ori	001101	100101		Ō	J	0100	0	0	D	0	0	J

