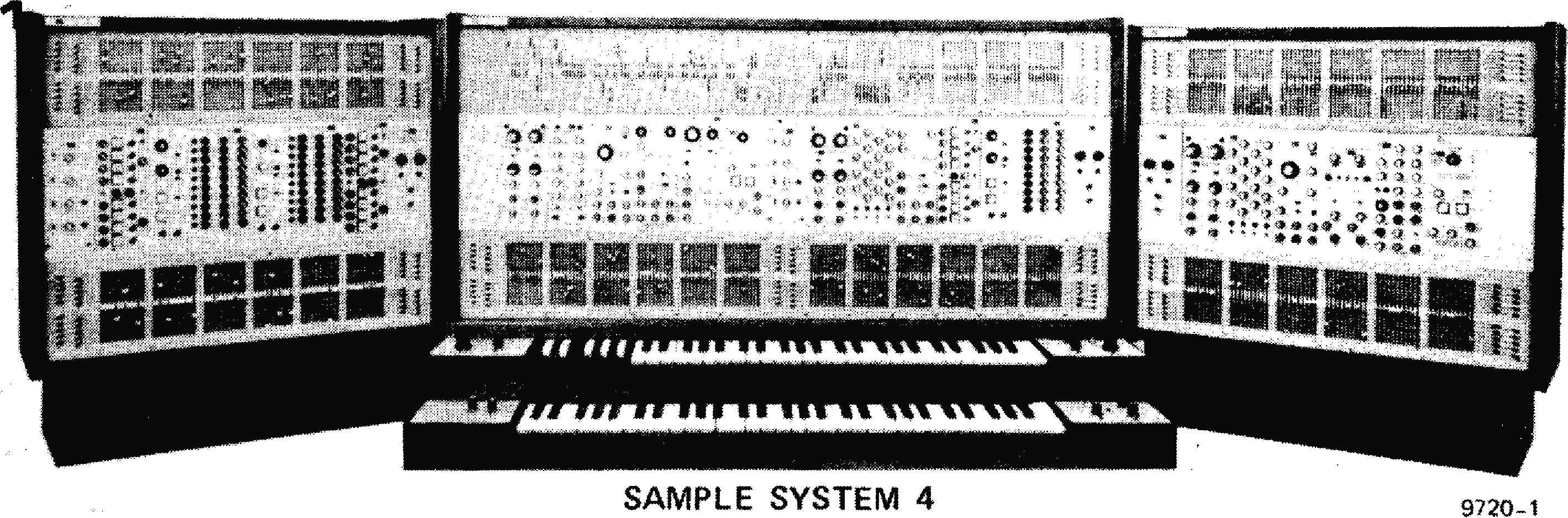
**FORMANT 007**

**formant**

**the elektor**

**music synthesiser**

**chapter 1**



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**introduction**

**Formant — the Elektor music synthesiser — is an instrument of advanced specification that bears comparison with many commercially available synthesisers, but at a fraction of the cost.**

**As synthesisers are something of a mystery to many people this first chapter provides an introduction to the basic principles of synthesisers in general.**

**Figure 1. A comprehensive commercial music synthesiser, the Modell 'Sample System 4'.**

A synthesiser may be **defined as an** electronic musical **instrument whose** tonal characteristics can be **varied at will** by the musician. This immediately makes the synthesiser different from conventional musical instruments, whose tonal characters are fixed by their physical construction. It also makes the synthesiser **different from an** electronic organ, since the latter **has a fixed set of voices, generally imitative of conventional organ voices, whereas the synthesiser has no fixed tonal charac­teristics. The synthesiser may be used to imitate conventional instruments, but on the other hand it may also produce sounds that cannot be produced by any conventional acoustic instrument, and which can be generated only by electronic means.**

**The synthesiser then, is an extremely versatile instrument, and it is a great pity that it is often used to provide monotonous** background accompani­ment to beat music, or as a 'band in a box' to produce television advertising jingles. Fortunately the capabilities of synthesisers have been fully exploited by musicians such as W. Carlos, K. Emerson, **P. Moraz et al.**

**Principles of the Voltage Controlled Synthesiser.**

The concept of the voltage-controlled synthesiser and related circuits was originated by Robert A. Moog. Any sound can be characterised by just three

**time dependent** parameters, **namely pitch, tone colour and volume, or to put it in electronic terms, fundamental frequency, harmonic content and amplitude. If these three parameters can be precisely controlled for the duration of a particular sound then that sound can accurately be synthesised. In practice this is obviously limited to fairly simple 'musical' and related sounds.**

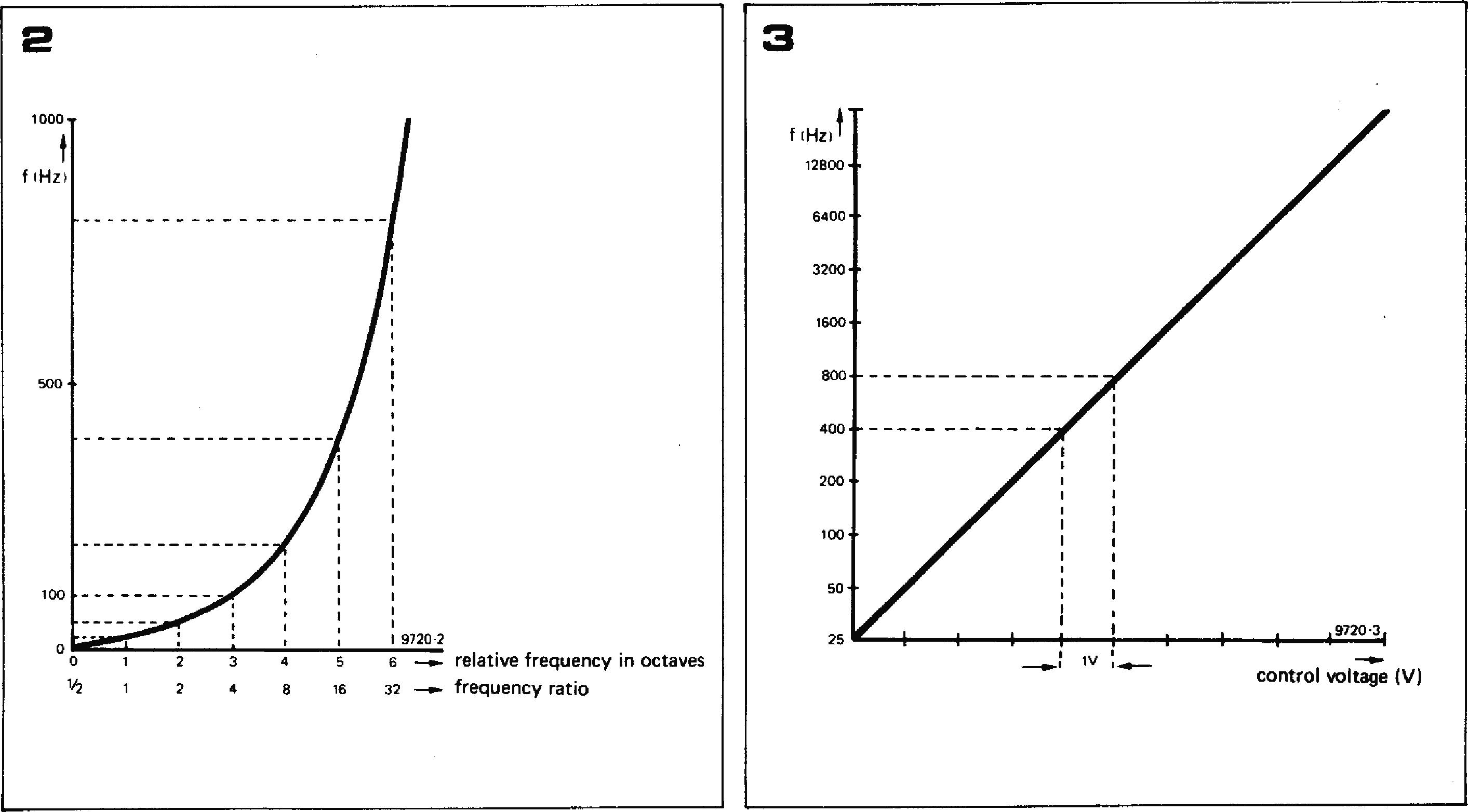
**A synthesiser thus requires three basic circuit blocks: oscillators to generate sounds of the required pitch, filters to produce the required harmonic content, and amplifiers to obtain the required amplitude. Since the three parameters may vary during the existence of a particular sound there must be some means of rapidly controlling the charac­teristics of these circuit blocks, which is where the concept of voltage control comes in. The** *pitch* **of a voltage-controlled oscillator (VCO) may be varied by changing the control voltage applied to it. The** *cutoff-frequency* of a voltage-controlled filter (VCF) may similarly be varied, as may the *gain* **of a voltage-controlled amplifier (VCA).**

**Exponential Voltage Control**

**The fundamental design parameter of a synthesiser is the control voltage versus frequency characteristic of VCO's and VCF's. In many applications a linear characteristic is required, i.e. n volts per** Hz. However, musicians are concerned

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**not with linear frequency relationships but with musical intervals, the basic one of these being the octave. For each octave increase in pitch the frequency of a note doubles. This means that if the absolute frequency in Hertz is plotted against the relative frequency in octaves an exponential curve results, as shown in figure 2. It therefore seems more logical from a musical point of view to have a linear control voltage versus octaves characteristic. Figure 3 shows a control characteristic of 1 octave/volt (which is the standard generally adopted). This exponential control has several other advantages which will be discussed later.**



**—..- relative frequency in octaves**

32 rt **frequency ratio**

**An exponential (octave linear) control characteristic may be achieved by preceding a frequency linear VCO or current-controlled oscillator by an exponential generator whose output voltage or current doubles for each one volt increase in input voltage (figure 4). The exponential generator can be preceded by a summing amplifier into which is fed the main control voltage along with other voltages such as a D.C. offset voltage to transpose up and down the scale, and/or A.C. modulating voltages to produce vibrato effects.**

**Keyboard Voltage Control**

**In order to play the synthesiser there must be some method of feeding varying control voltages into the instrument. Since most (Western) musical instruments are tuned and played in the tempered tonic scale it seems logical that a synthesiser should conform to this scale, and the most obvious choice of 'input terminal' is a normal organ keyboard with electrical contacts.**

**The keyboard circuit (figure 5) consists
  
of a potential divider chain comprising**

**equal value resistors, fed from a current source. Since there are twelve semitone intervals (and hence twelve key contacts) to an octave, each resistor has a potential difference of 1/12 volt across it. Depressing a particular key connects the voltage on that key contact out to the common bus rail, and thence to the voltage-controlled circuits.**

**Transposition**

**Like an electronic organ, a synthesiser keyboard has only a limited compass (three octaves in the case of Formant). In an organ a wider compass is obtained simply by selecting voices with a lower register. In a synthesiser the compass is extended by adding a D.C. offset voltage to the VCO input (or to the keyboard output) to transpose the range of the keyboard. This is shown in figure 6. An offset of +1 volt transposes the range up one octave, while an offset of —1 volt transposes it down one octave.**

**Advantages of exponential control**

**Figure 7 illustrates the principal**

**advantage of exponential control, which is chording. This shows three VCO's each with three summing inputs. The first input of each VCO is commoned and connected to the keyboard. The second input of each VCO is connected to an independently variable D.C. offset voltage, while the third inputs are all commoned to another variable D.C. offset voltage. Suppose that the independent offsets are adjusted so that the adjacent VCO frequencies are one octave apart, e.g. 1 kHz, 2 kHz and 4 kHz. If the keyboard input increases by one volt then the frequencies will increase to 2 kHz, 4 kHz and 8 kHz respectively, which are still one octave**

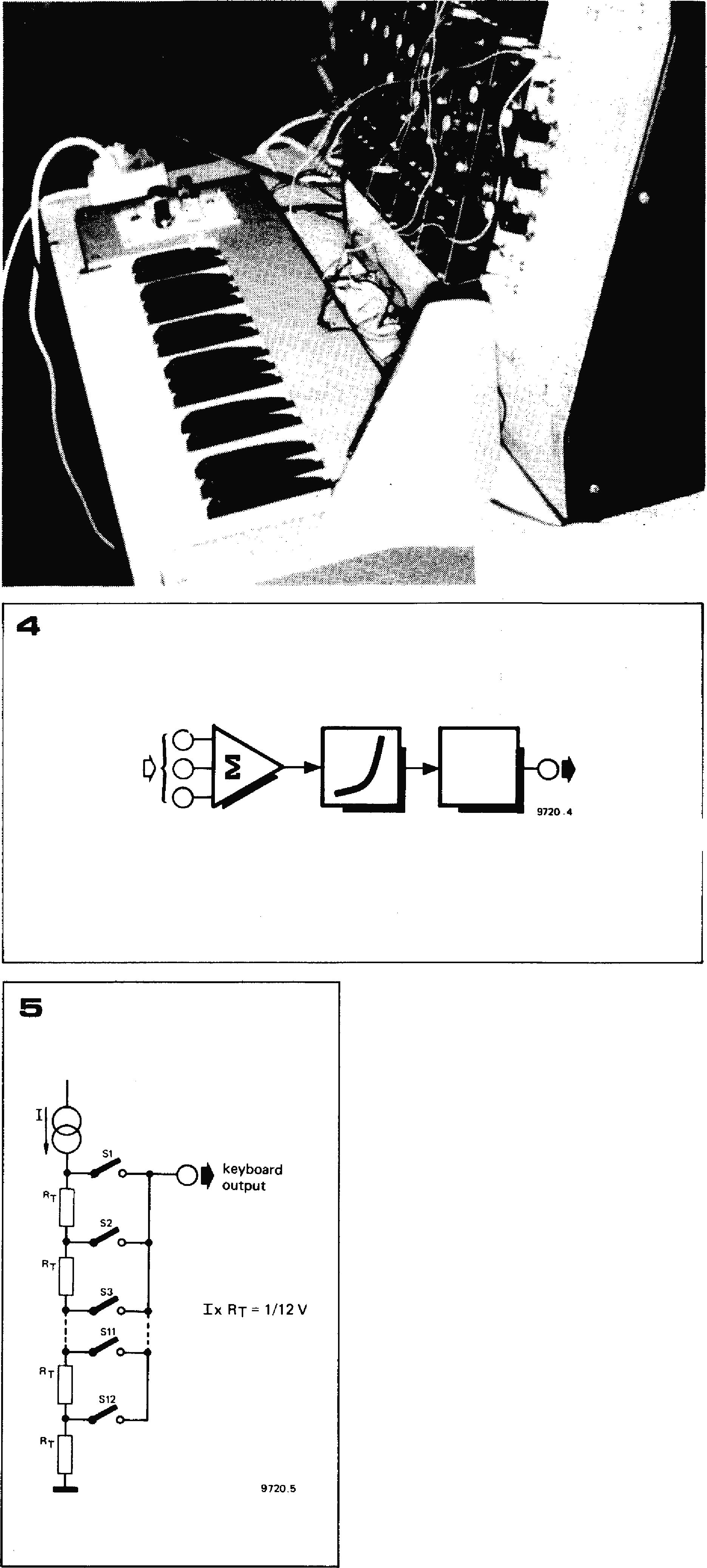
**apart. This would not be the case with a linear VCO. As an example, suppose the first frequency increases by 1 kHz to 2 kHz; the second will also increase by 1 kHz (to 3 kHz), and the third will increase to 5 kHz. This 2-3-5 kHz group no longer shows an octave relationship. Of course, with exponential control one is not confined simply to octave chords. By adjusting the independent offset controls the VCO's may be set up in any musical interval relationship. Addition­ally any number of VCO's may be employed. The commoned third inputs of the VCO's permit a common offset voltage to be fed to each VCO to transpose the whole chord up or down the scale. A further possibility is to add an offset voltage to the keyboard output to transpose the pitch of the entire synthesiser. This may seem a little confusing at first, but is not so in practice. To summarise:**

1. **VCO's may be arranged in chording groups. The pitch of each VCO may be varied relative to other VCO's within a group to obtain the required chord, by adjusting the independent tuning controls.**
2. **The pitch of a chording group may be varied by adjusting the chord trans­position control.**
3. **The pitch of the entire synthesiser may be transposed by an overall trans­position control that adds a variable offset voltage to the keyboard output.**

**Voltage Controlled Filters**

**Voltage controlled filters employed in synthesisers are commonly of the lowpass type. A block diagram of their operation is given in figure 8. A D.C. voltage sets the cutoff frequency relative to the pitch of the VCO's, while a control voltage derived from the keyboard shifts this cutoff point up or**

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**down according to which note is played, so that all notes played, whether high or low, have the same harmonic content'. Natural sounds are characterised by dynamic changes of tone colour. A note may start by having a `bright' character with a large proportion of the higher harmonics, but these then decay rapidly leaving only the fundamental and lower harmonics. Provision must therefore be made to vary the cutoff point during the note. e.g. the cutoff point might initially start off at a fairly high frequency, which would decrease with time to cause the decay of the higher harmonics. This is achieved by means of an envelope shaper which generates a varying voltage having the required characteristics. The envelope shaper is controlled by a gate pulse which is derived from a second set of contacts on the keyboard. The voltage versus cutoff frequency characteristic of the VCF's is again made exponential by preceding the control input of the VCF with an exponential generator.**

Summing amplifier Exponential

generator

**Figure 2. Showing the exponential relation­ship between relative frequency in octaves and absolute frequency in Hertz.**

**Figure 3. In common with other synthesisers, Formant has an exponential or octave-linear control characteristic of 1 octave/volt, i.e. if the control voltage is increased by 1 volt the frequency doubles.**

**Figure 4. Principle of a voltage-controlled synthesiser module according to Moog. This forms the basis of the voltage-controlled modules in Formant.**

**Figure 5. Showing how the control voltage is derived from the keyboard using a resistive potential divider.**

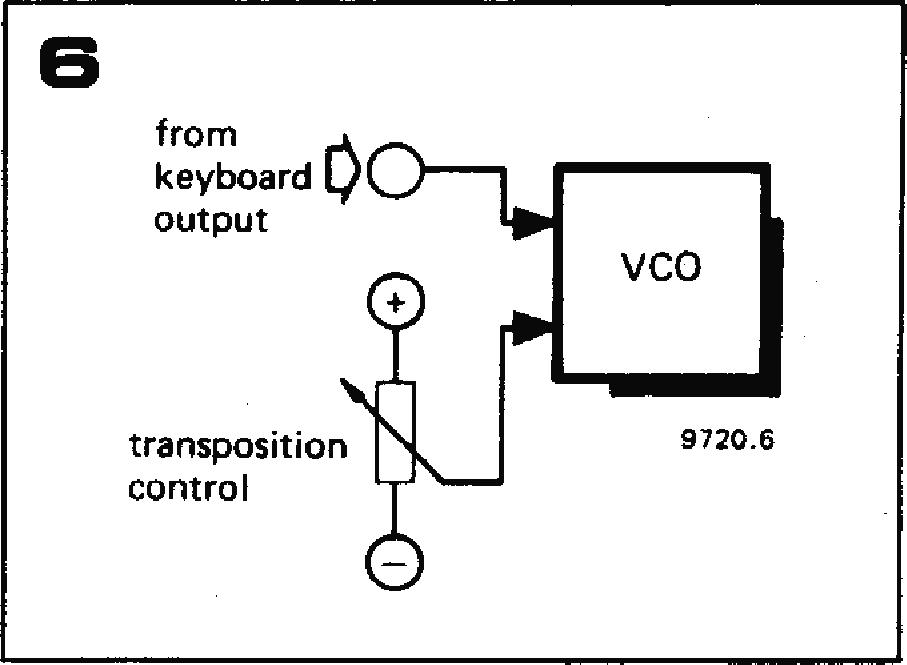
**Figure 6. By adding a DC offset voltage to the summing input of the VCO along with the keyboard voltage, the frequency range can be transposed. For example, an offset of +1 V makes the note one octave higher than that actually played on the keyboard. An offset of —1 V would make it one octave lower.**



**Voltage controlled amplifiers**

**The VCA's are simply amplifiers whose gain may be varied by a control voltage. Their function is to control the duration of a sound, and also its dynamic amplitude characteristics, i.e. its attack, sustain and decay. The VCA is again controlled by an envelope shaper whose output voltage has a form corresponding to the amplitude envelope of the required sound. The VCA of course has no control voltage input from the keyboard, since the amplitude of all notes must remain the same and does not depend upon the frequency of the note being played.**

**The envelope shaper which controls the
  
VCA is itself controlled by a gate pulse**



**derived from the second contact set on the keyboard, and this determines the duration of the note played.**

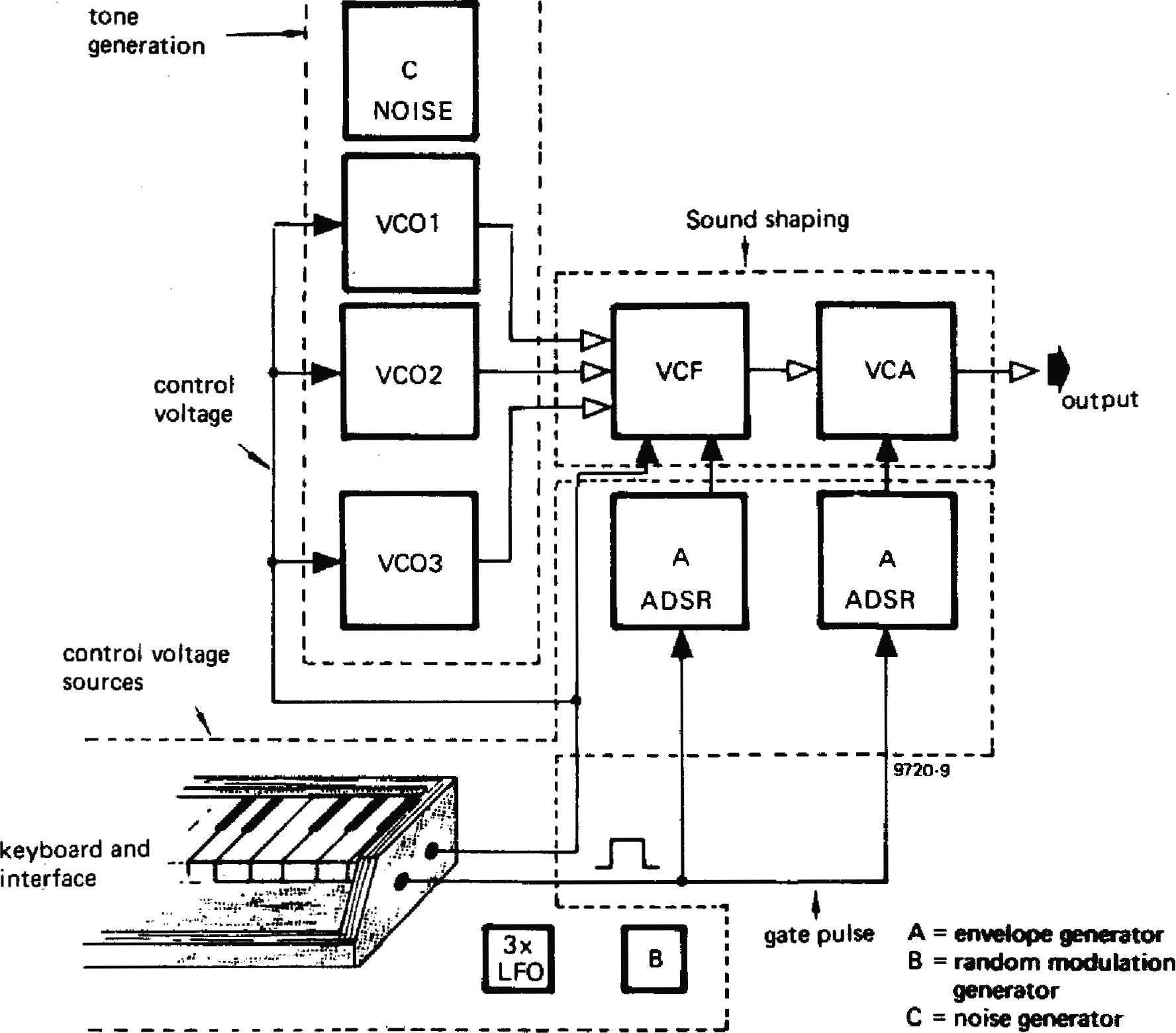
**In the case of both the VCF's and VCA's the output voltage characteristic of the controlling envelope shaper (i.e. the manner in which the envelope voltage varies with time) can be adjusted by the musician. This is extremely important since the dynamic characteristics of a sound largely determine the character of the sound. Returning to conventional musical instruments as an example, if the attack transient at the beginning of a note is removed and only the steady**

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| **Figure 7. To play a chord, the offset of several VCO's may be adjusted to give the required musical intervals. They can then be controlled simultaneously by the keyboard, and may also be transposed together by a common D.C. voltage. This is known as 'chord transposition.**  **Figure 8. The voltage-controlled filter (VCF) is controlled in exactly the same manner as the VCO. The keyboard controls its cutoff frequency, which can also be 'transposed' by a D.C. offset voltage. The third input allows dynamic changes of cutoff frequency during the playing of a note by means of an envelope shaper.**  **Figure 9. Layout of a basic synthesiser. Several additional blocks are shown such as low-frequency oscillators and noise generator to modulate the voltage-controlled modules, and a noise generator to produce effects such as wind, rain etc.**  **note is played then it becomes extremely difficult to distinguish between many orchestral instruments. Indeed, it becomes difficult even to determine whether sounds so treated belong to string, brass or woodwind families.**  **Synthesiser Block Diagram and Additional Circuits**  **Figure 9 shows the block diagram of a basic synthesiser, which contains all the circuits previously described plus a few extras.**  **At the bottom of the diagram is the keyboard and its interface circuits. These consist basically of the control voltage potential divider, the gate pulse generator and the envelope shapers. In addition there are low-frequency oscillators that provide a signal for periodic modulation of the voltage controlled circuits (e.g. for effects such as vibrato) plus a noise voltage generator to provide random modulation.**  **The block containing the VCO's needs little explanation, except for the addition of a noise generator. Since this generates a stochastic signal of no fixed pitch its frequency is not controlled by the keyboard, but the noise signal can be processed by passing through the VCF's and VCA's to produce effects such as wind, water, rain, thunder, steam trains, applause etc., as well as totally unnatural sounds.**  **The VCO and noise signals are passed through the VCF and then through the VCA, both of which are controlled by the envelope shapers.**  **Tonal Character of Synthesisers**  **The foregoing description of the basics  of synthesisers can hardly give any** |
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**impression of the range of tonal possi­bilities available with a synthesiser. To begin with, it should not be imagined that the sound produced by a synthesiser is like that produced by an**

**9**



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**tone generation**

**NOISE**

**Sound shaping**

**\_\_44>**

**output**

J

**control
  
voltage**

**control voltage sources**

**keyboard and interface**

**A = envelope generator**

**B = random modulation generator**

**C = noise generator**

**electronic organ. A synthesiser has much more in common with conven­tional musical instruments. Whereas the sound of an electronic organ has a fairly static character, a synthesiser is much**

**more lively and dynamic. The reasons for this are twofold. Firstly, a synthesiser permits precise control of the dynamic characteristics of the sounds produced, whereas an electronic organ (unless it is an expensive one) has only fixed attack and decay characteristics that must suffice for every voice.** Secondly, **whereas most electronic organs are fixed phase, the synthesiser, with its phase-independent VCO's, can much better produce more 'natural' sounds, which have varying phase patterns.**

***Literature***

***Moog R.A. 'Voltage-controlled
  
electronic music modules'.***

***JAES 13.3 July 1965.***

***`A voltage-controlled low-pass, high-pass filter for audio signal processing'. AES Preprint 413, 1965.***

***Burhans R. 'Simplified educational music synthesiser: JAES 19.2, February 1971.***

***Orr. T. and Thomas D. W., 'Electronic Sound Synthesiser'. Wireless World, August-October 1973.***

***Simonton J. 'Build a modular electronic music synthesiser. Radio-Electronics, May-October 1973.***

***Shaw G. `P.E. Sound Synthesiser'. Practical Electronics, February 1973-February 1974.***

***Tiinker H. 'Electronic-Pianos and Synthesiser. Franzis-Verlag, 1975. Nr. 302 (RPB Serie).***

**chapter 2**

**keyboard and keyboard interface**

**Having discussed the basic principles of synthesisers, we can now proceed to a description of the Formant itself, starting with the keyboard and associated electronics.**

**Before going any further it must be stressed that 'Formant' is not a suit­able project for the beginner. The com­plexity of the synthesiser demands a high degree of competence in soldering p.c. boards and interwiring** if **an unac­ceptably large number of faults are not to arise. Some knowledge of operational amplifier basics is also almost essential. Nor should the project be undertaken by anyone who does not have access to an oscilloscope, a good multimeter, and preferably a digital voltmeter.**

**Top grade components are also a must. Where specified, 1% metal film or metal oxide resistors must be used. All other resistors should be good quality 5% car­bon film types, while capacitors (except where the capacitance demands an elec­trolytic) should be low loss, low leakage types such as polycarbonate, polyester or polystyrene. Ceramic capacitors should not be used. Semiconductors too should be first-grade devices from a reputable source, not 'unmarked, un­tested' manufacturer's rejects. The temptation to save money by buying dubious components should be avoided,**

**as unsatisfactory performance will almost certainly be the result.**

**The synthesiser comprises two separate units, the module unit containing VCO's, filters, power supplies etc, and the manual unit containing the key­board. These two units are interconnec­ted by cables with plug and socket terminations and can be separated for transportation or storage.**

**The keyboard is a 37-note C to C key­board fitted with Kimber-Allen two-pole normally open contact blocks. The keyboard used in the prototype was an SKA type. This keyboard is rec­ommended for the project and the descriptions given will relate to it, though other types may also be suitable. The keyboard consists of an aluminium chassis with dimensions similar to those given in figure 1, to which the keys and return springs are assembled. The key contacts are depressed by a plastic actuator on the underside of each key, which protrudes through a hole in the chassis (see figure 1 c). Contact blocks are supplied separate from the key­board, and the first task is to assemble**

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**Figures la, lb and lc. Mechanical details of the SKA keyboard. (A) Keys. (B) Contact actuator. (C) Chassis.**

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**1 2 3 4 5**

**3 7 KEYS**

**34 35 36 37**

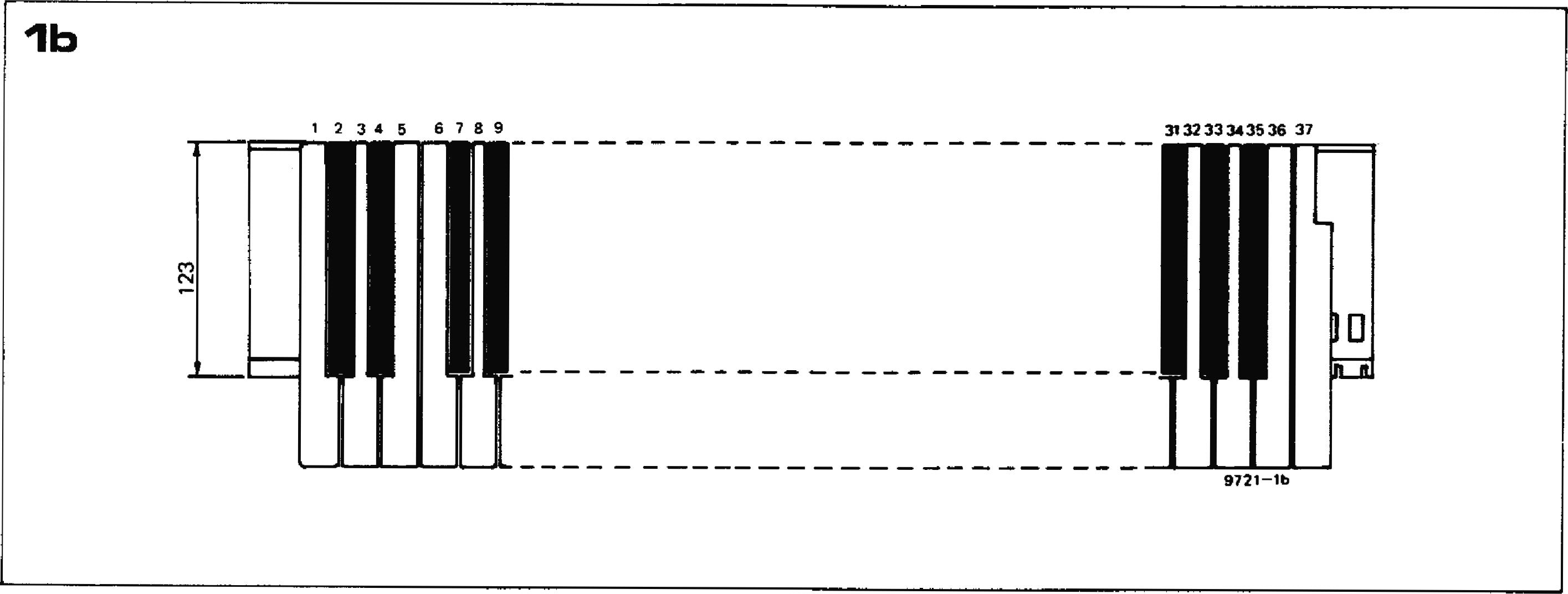
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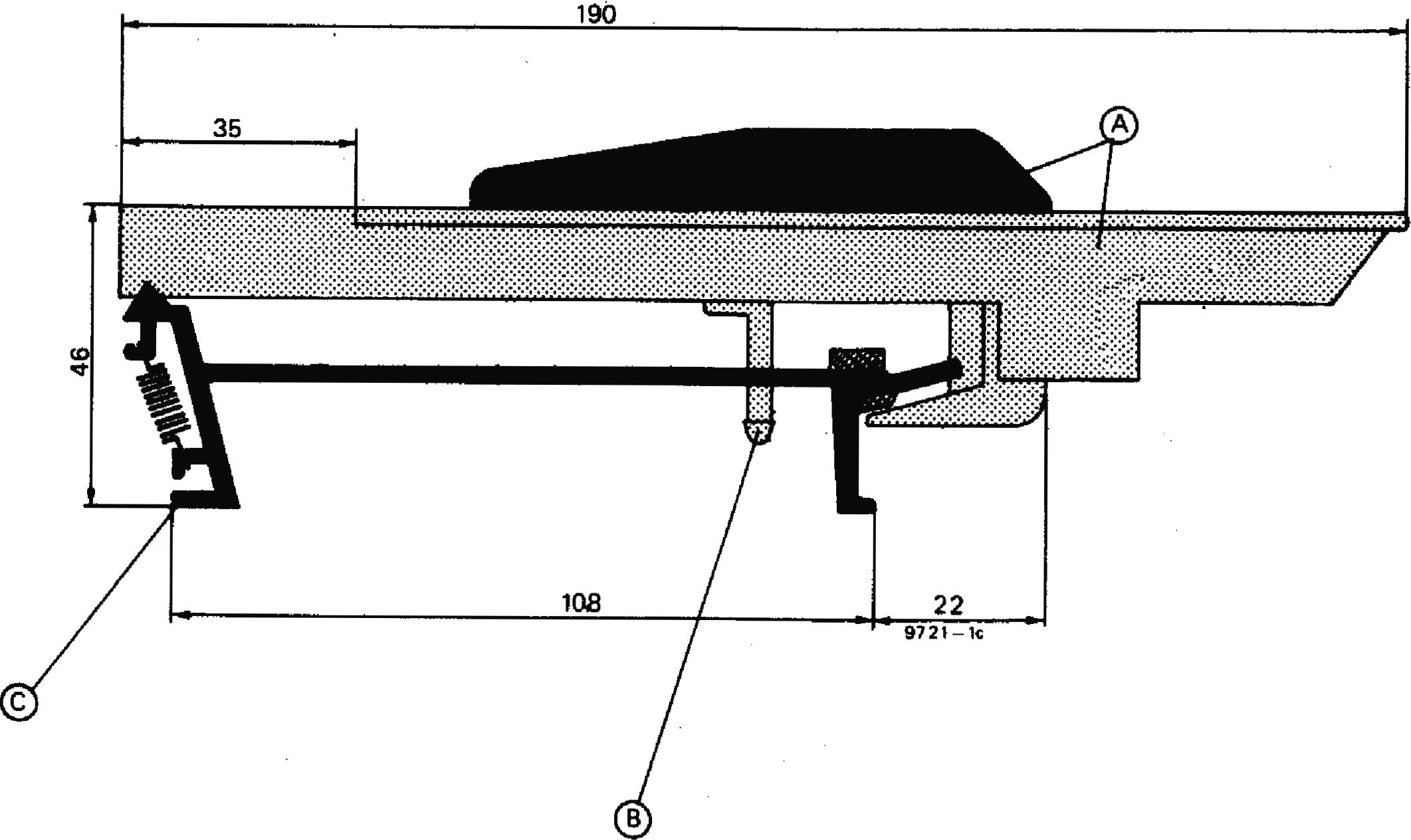
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**Figures 2a and 2b. Showing the mounting of the key contact blocks. (A) Key. (B) Actuator. (C) Chassis. (D) Contact block. (E) Divider board. (F) Spacer.**

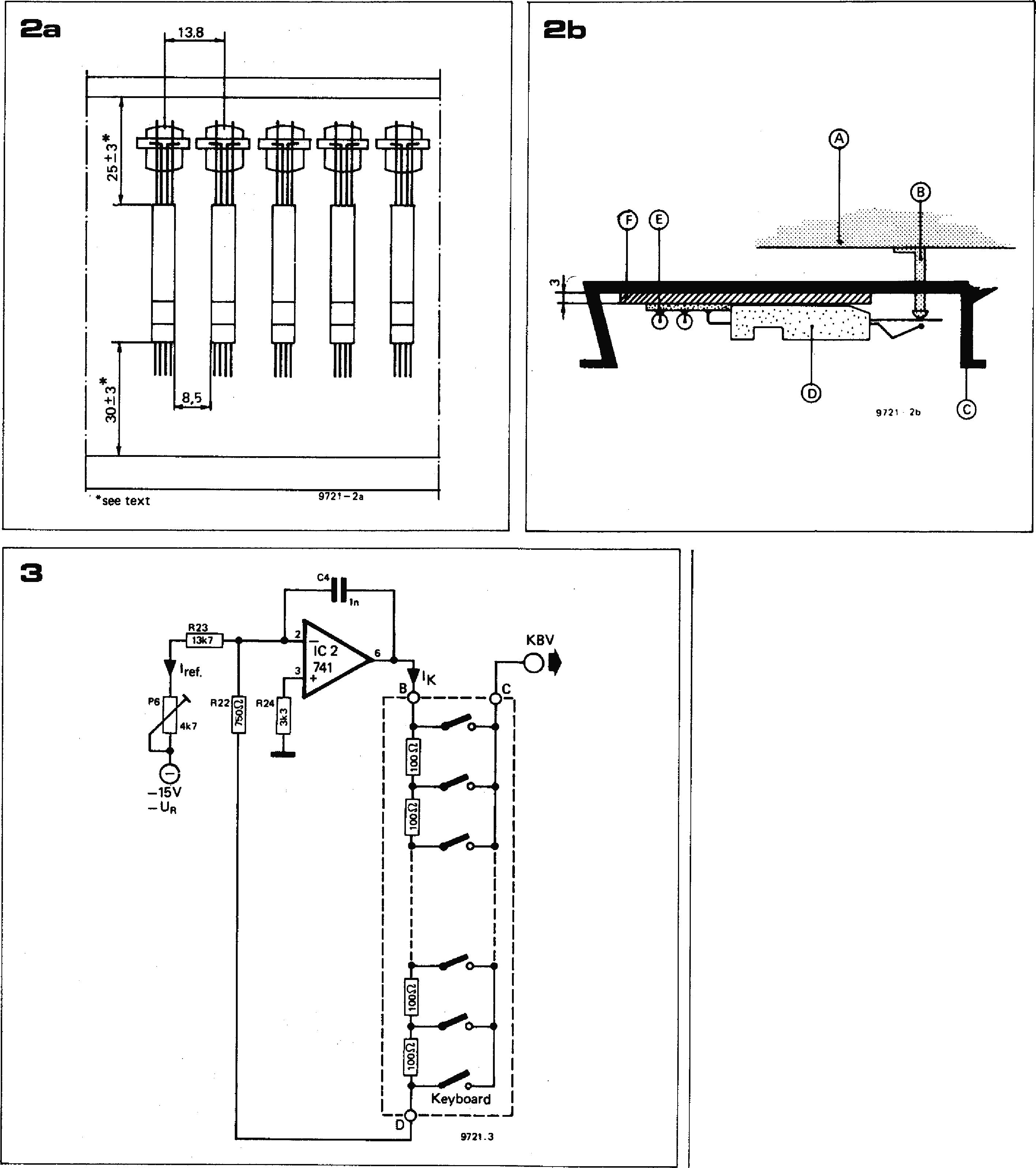
**Figure 3. Circuit of the keyboard divider chain and current source.**





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**FORMANT - 013**

the contacts to the underside of the chassis as shown in figures 2a and 2b. The contact blocks must be spaced away from the chassis so that the actuator (A) just touches the movable contact with the key in the rest pos­ition. A strip of 3 mm thick perspex or other plastic makes a suitable spacer (F). The simplest method of fixing the con­tact blocks in position is to glue them to the plastic spacer using quick setting epoxy adhesive, taking care not to get any adhesive into the 'works' of the contact block. For those preferring a more easily serviceable assembly the slot in the underside of the contact will accept a rectangular section metal strip

which will clamp the contacts to the chassis using nuts and bolts.

**Keyboard interface**

The principle of the synthesiser key­board, which was briefly explained in chapter 1, is shown in figure 3. The function of one contact-set on the key­board is to provide a control voltage to the voltage-controlled modules of the synthesiser. Each key can be used to switch a voltage from a particular point in a potential divider comprising equal value close tolerance resistors fed from a constant current source. The con­trol characteristic of the Formant is **1** octave/volt so each resistor in the

chain **must drop 1/12 V giving a step of 1/12 V per semitone.**

**In figure 3 the constant current source is built around IC2, a 741 op amp. From the —15 V stabilised supply a constant current flows through P6 and R23. Since only a negligible bias current can** flow into the inverting input of the 741, this same current must also flow out of **the op amp output and through the potential divider chain back to the inverting input. Since R24 holds the non-inverting input** at ground potential, the voltage at the junction of R22 and R23 **is also zero — a 'virtual earth' point. P6 can be adjusted to give a current of 833µA or a voltage drop of 1/12 V across each 100** a **resistor, i.e. 83.3 mV. In practice the voltage will not be exactly 83.3 mV but will be somewhat higher to compensate for voltage losses in other parts of the circuit.**

**It may seem rather strange to use a stabilized reference voltage to produce a constant current which in turn is used to produce a constant voltage. Why not simply feed the potential divider from a constant voltage in the first place? The answer is quite simple. Since the syn­thesiser is a monophonic instrument only one note can be played at once. If the divider chain were fed from a volt­age source and several keys were depressed simultaneously, either by accident or intentionally, then part of the divider chain would be shorted out, increasing the voltage drops across the remaining (unshorted) resistors and giving a discordant note. Feeding from a constant current source means that, even if part of the chain is shorted out the voltage drops across the remaining resistors will stay correct and the note sounded will actually be the lowest note of those played.**

**Sample and hold circuit**

**It is not possible directly to use the**

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**output voltage of the keyboard to con­trol the synthesiser, since immediately a key is released that voltage disappears abruptly, and so would any tone that was controlled by it, making effects such as sustain impossible. For this reason the output voltage of the key­board is stored in a sample and hold circuit. This consists basically of a switch and a capacitor connected to the input of an op amp in voltage follower configuration. When the switch is closed the capacitor charges rapidly to the same level as the input voltage. The op amp output also assumes this level.** If the switch is now opened, then assuming the op amp has a high input resistance, the capacitor can discharge only ex­tremely slowly, so the op amp maintains its output voltage for a long time.

There are a number of difficulties inherent in this simple approach. Firstly, since the switch in figure 4 corresponds to a key contact of the key­board, the leakage resistance of the switch when open is the leakage resist­ance of 37 key contacts connected in parallel, which can be quite low, especially in a humid environment. This could be overcome by increasing the value of the capacitor so that it dis­charges more slowly, but it would then take much longer to charge from the keyboard divider chain, which would result in unwanted 'glissando' effects. The solution is to use a double sample­and-hold circuit, as shown in figure 5. The pre S and **H** circuit stores the out­put of the keyboard on a small capaci­tor C1, the output being buffered by a source follower FET TI. Before the voltage on Cl can decay due to the key contact leakage the voltage at the source of Ti is transferred to a larger capacitor C2 by an electronic switch T2. The 'off' resistance of this switch is much higher than that of the keyboard, and T3 has a high input resistance, so C2 can hold its charge for quite a long time. T2 is switched by a gating pulse controlled by the second contact set of the keyboard. The cathode of DI is normally at —14 V and T2 is thus cut off. When a key is depressed the gate pulse takes the cathode of D1 up to +14 V and T2 turns on.

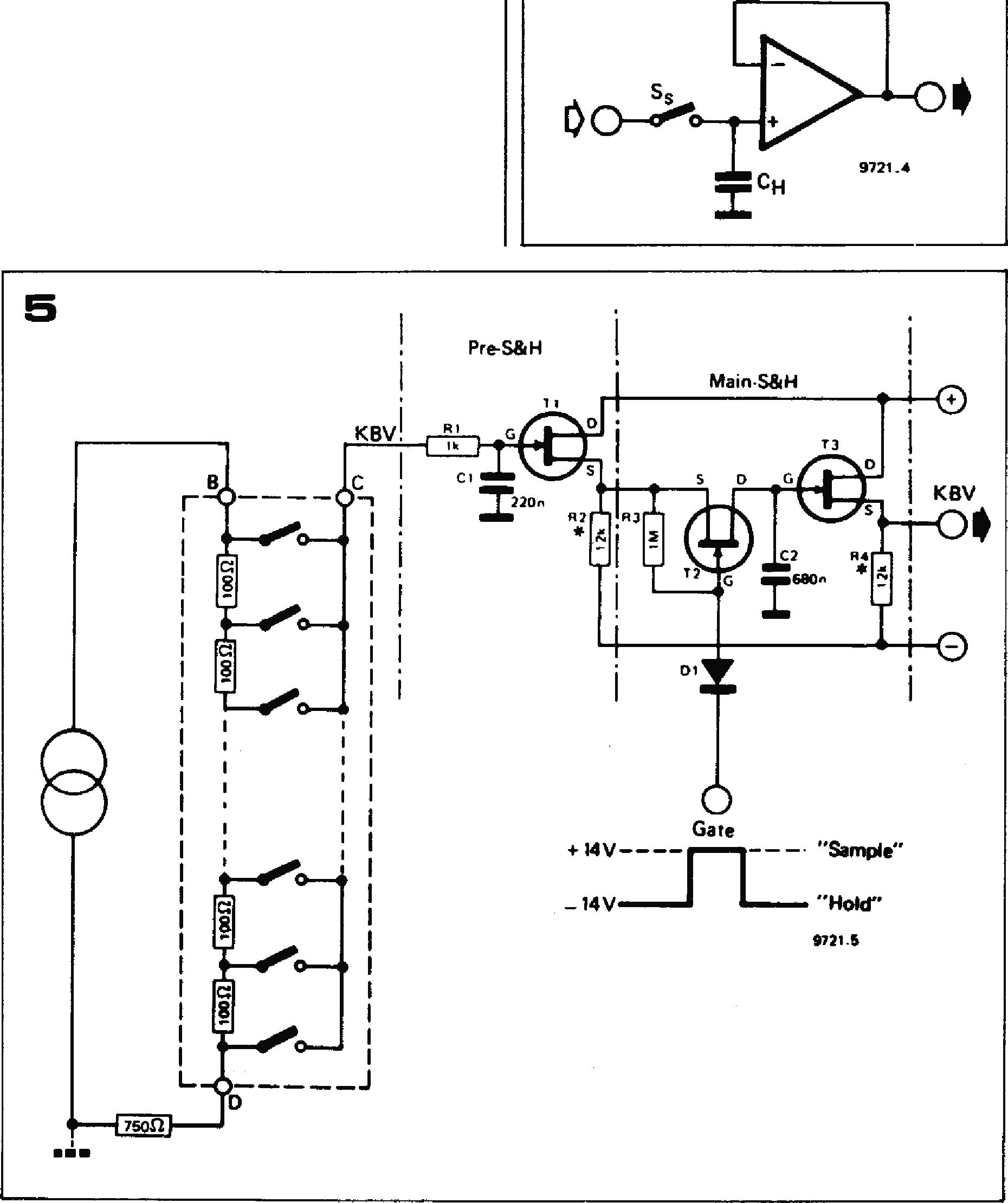
**RC network that integrates the stepwise output of T3 to give a much smoother change as shown in figure 6b. P1 con­trols the `smoothness' of the change. Note that, due to the FET** tolerances, the source **resistors R2, R4 and R6** must be selected on test, and this will be described in chapter 3.

**Overall tuning, frequency modulation and offset balance**

FETs connected as **source followers differ in two essential respects from ideal voltage followers. Firstly, between gate and source there is always the gate-source voltage of the FET, which means that the source is always at a higher** voltage than the gate. This **appears as an** undesirable positive **offset voltage at the** source of the FET, **and since, in this cir­cuit, three FETs are connected in cas­cade these offset voltages are additive. Secondly, the gain of a source follower is slightly less than unity, which means that a 1 V change on the gate does not produce a 1 V change at the source.** The offset **voltage is compensated for** in the circuit of **figure 8. This comprises two IC** op **amps. 1C3 is connected as an** inverting **summing amplifier, while 1C4 is connected as a unity gain inverter to restore the correct sense of the control voltage. A negative voltage controlled by P4 may be summed with the control voltage input (KBV") to cancel out the positive offset voltage.**

**The gain losses in the sample and hold and portamento stages are compensated by increasing the current through the keyboard divider chain by means of P6 in figure 3 until a control characteristic of 1 octave/volt is obtained at the out­put of IC4.**

**Figure 4. Showing the principle of a ample­and-hold circuit.**

**Figure 5. The dual sample-and-hold circuit used in the Formant.**

**Figure 8. Showing the output waveforms of the keyboard interface when playing a scale la) normally and (b) portamento.**

**Figure 7. Circuit of the portamento stage. P1 controls the 'smoothness' of the portamento.**

**Figure 8. Circuit of the offset compensation, octave tuning and frequency modulation stage.**

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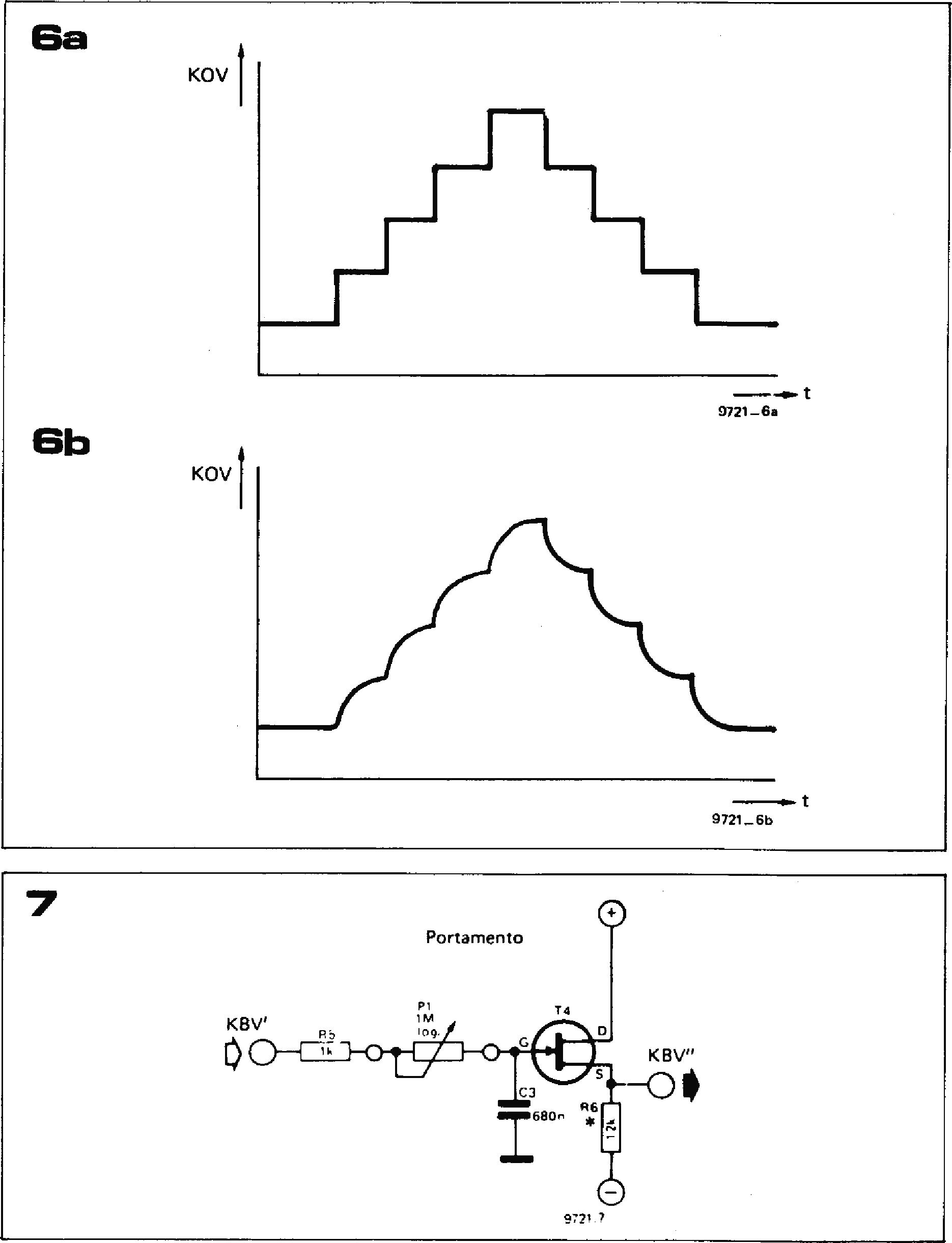
**Portamento control**

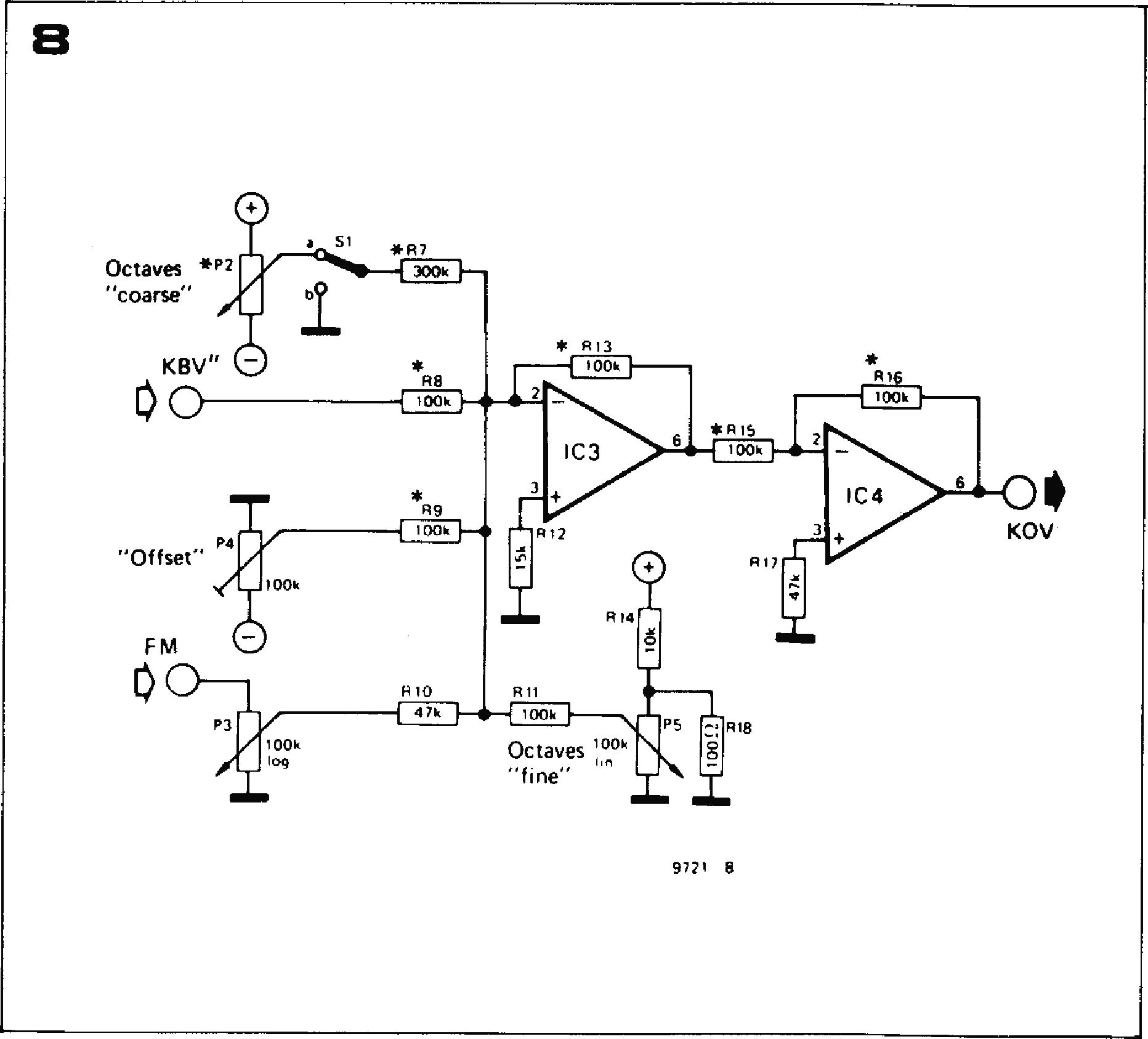
When playing up and down a scale the control voltage from the source of T3 would normally consist of a series of discrete steps, as shown in figure 6a. This would give rise to equally discrete changes of pitch, the minimum change in pitch being one semitone interval of the tempered scale, as with any other keyboard instrument. However, many instruments are characterised by the ability to make continuous (smooth) changes of pitch, one example being the trombone with its slide. This style of playing **is known as** `portamentoi.

The circuit of a portamento stage is
  
given in figure 7. It consists simply of a
  
source follower FET, preceded by an

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**The circuit of figure 8 performs two additional functions. By adding a vari­able DC voltage to the control voltage the entire tuning range of the syn­thesiser may be shifted. P2 provides an adjustment of about 5 octaves, while P5 provides a fine adjustment of about ± one semitone, so that the synthesiser can easily be tuned to match other instruments.**





**100k**

log

**Octaves 17" "fine"**

0

**Octaves \*P2**

**"Coarse"**

**KBV"**

**00**

**"Offset"**

**F**M

**00**

**9721 8**

**8**

**016
  
lOok**

**IC 4**

**3**

**\*R7**

**300k**

**Re
  
100k**

**R9**

**{ 100k**

\* **R13**

100k I--,

**2**

**IC3**

**3**

**917**

0

**R14**

\*R IS

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O

**0\***

**KOV**

**R11**

**ECM**

**P**S 018

**A**

0

**00k**

**R10**

**CUM**

**A further input is provided for fre­quency modulation, for example to provide vibrato. The modulation level can be adjusted by means of P3, and with P3 fully clockwise the 'sensitivity' of this input is about 1 octave per 500 mV.**

**Gate circuit**

**Because of the action of the sample and hold circuit, once a key has been depressed the control voltage remains at the KOV output until another key is depressed. This would cause a note, once pressed, to sound indefinitely were it not for the envelope circuits that control the attack, sustain and decay of the notes.**

**Gate pulses to control the sample-and­hold circuit and to control the envelope shapers are derived from the second set of keyboard contacts. As shown in fig­ure 9, these are all connected in parallel and fed with 4.7 V DC from IC1. When a key contact closes, the output of IC5 immediately goes to +4.7 V. C6 charges via P7 until its voltage exceeds the volt­age on the inverting input of IC6 (IC6 functions as a comparator) when the output of IC6 will swing positive. When the key is released the output of 105 will become zero and C6 will discharge rapidly through D2 so that the output of IC6 will swing negative.**

**The RC network P7/C6 provides an adjustable delay that compensates for a difference in closing time between the two sets of contacts. For example, should the gating contact close before the control voltage contact then the synthesiser would first sound a note determined by the residual voltage on CI in figure 5. Then when the control voltage contact closed the correct note would sound. The delay network ensures that the gate pulse is delayed until after the new control voltage has been applied to Cl. However, since C6 discharges rapidly through D2 the gate pulse ends immediately the key is released. C5 and R25 at the input to 105 help to suppress noise due to con­tact bounce.**

**Construction**

**Figure 10 shows the complete circuit of the keyboard interface, while the printed circuit board and component layout are given in figure 11. The p.c. board for the divider chain is given in the following chapter.**



**Figure 9. Circuit of the gate pulse generator, which is activated by the second contact set of the keyboard.**

**Figure 10. Complete circuit of the keyboard interface circuit.**

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10

**P2 a**

**S1**

100k

**7**

\* 513

**R11**

**R28**

**3**

R21

**525**

**T 3**

IC 5

**T3**

D **1M**

**% 1**

**%Jr R5 log. irk**

**Ma • • 0MI**

**Noir**

**aml=C3**

**880n**

**11**

**R1**

Cl

**T4**

**KBV**

**1220**

**R2**

**R3**

**2**

**C2 R4**

**-LT.L0 .680n**

**R**

3

**DI**

**512**

**\*R9100k**

**KOV**

**P4 00k**

**T1 ... T4 =. BF245 A, BF244 A**

**IC1 ... IC6 = mA741 C, MC1741 CP1
  
(MINI DIP)**

**D1,D2 = 1N4148**

**CIL**

**MOM**

**220n**

**5**

**R10**

**518**

**R27**

**P5 100k**

**22n**

**P3 100k los.**

3

**P7 100k**

**02**

**IC6**

**2V3
  
528**

**2**

**514**

**15V 20mA**

**-15V®**

**20mA**

**FM**

**9721\_10**

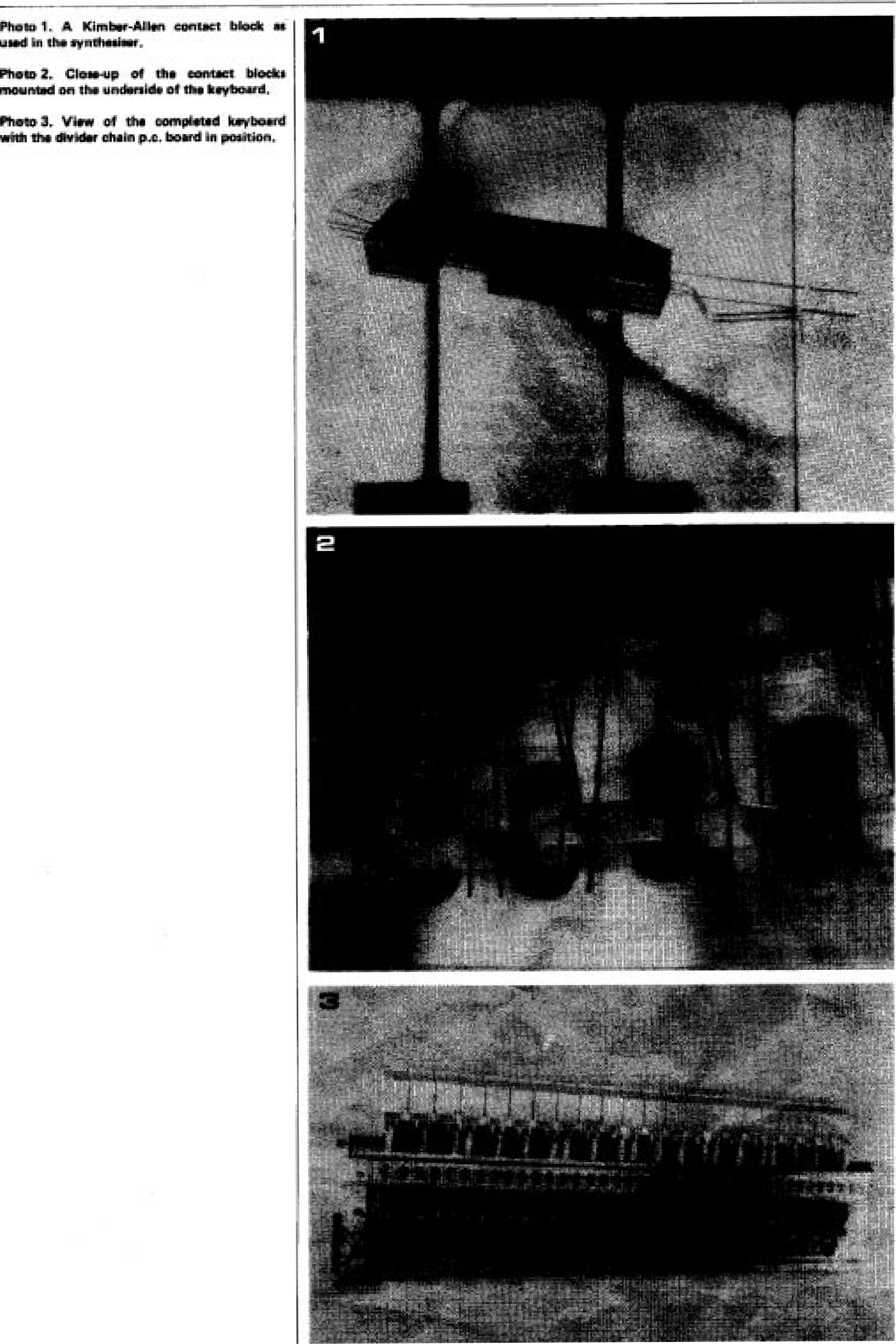
**GATE**

0

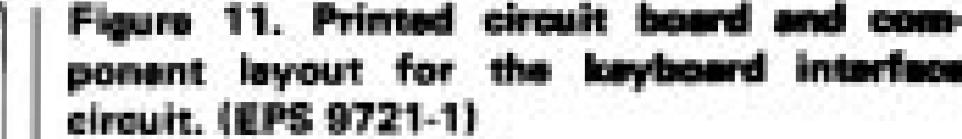
520

**-L**

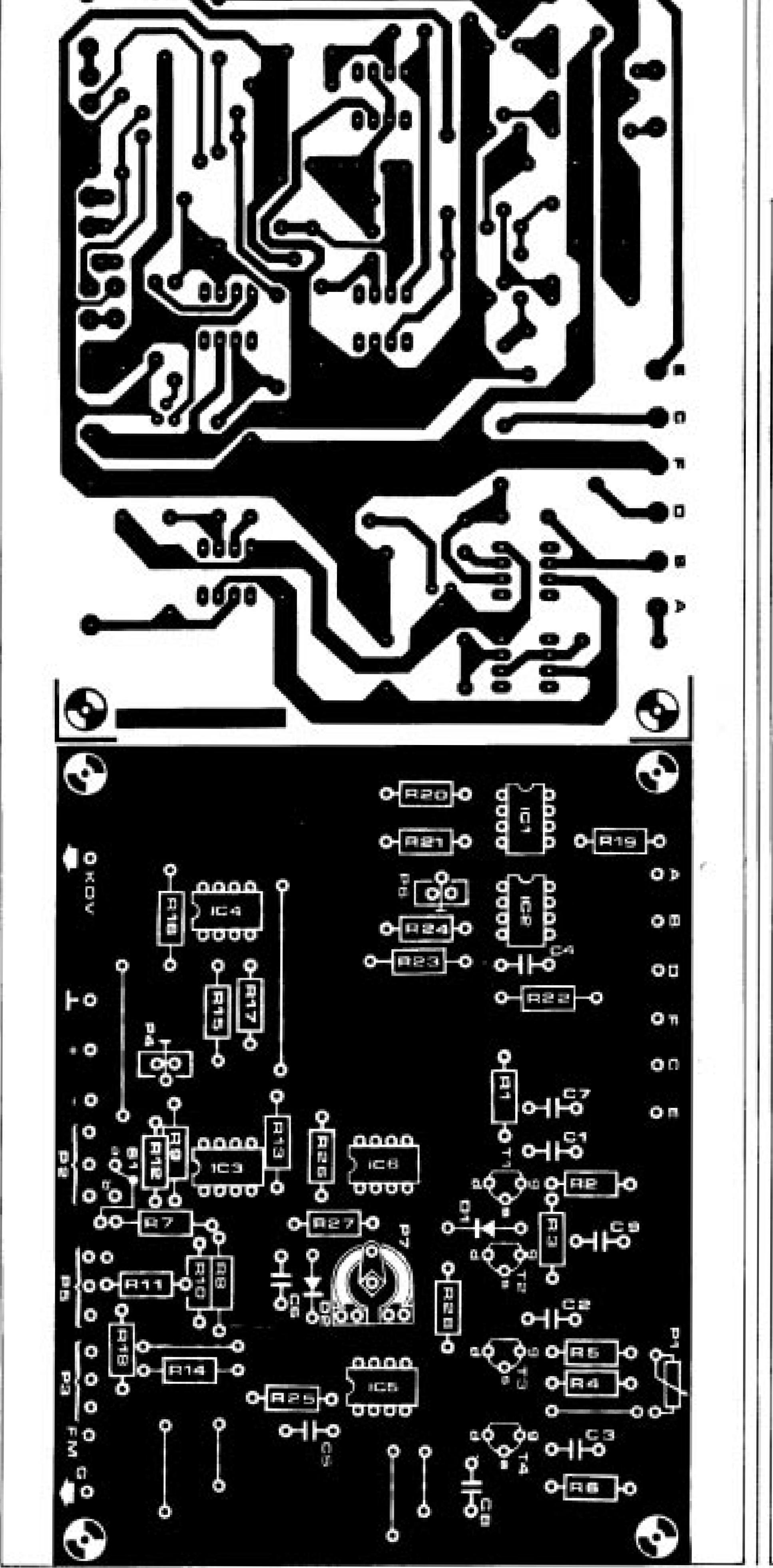
**FOFIMANT - 017**



**eTa - FORMA/17**



**Pam Lin la PI iii 11\_**



14

I )

**RkFlii0rE H1,1111.1t111,R210.- 1 k**

**A2,114.F65 - 12' k Ilrunaviirmi %%lug.
  
idia pen 30**

**p1-1 H**

**fIrl - 3D3 k CM rikPal MIME**

**1111.1111.013.,H11;**

**R 16 .• 1[14) ic [1 1,.. onatol oxlci■l. F110 7 = 47 II**

**lall I ,R25 0 *102* k**

**1113. 11511 A14111:201- 113 k**

**MS +. 160 Et 11121 - 4kY**

**1122 - ?El 0111% maiNICalarbrk P123 - 131k7 11911 mire \*x14:1•1 1424 - 3k3**

**1121E1 - 1 2 k R27. 21E2**

**?marls-**

**H - 11:110 k CICatirrrr) P5 A 4111 IS k., Orant) 177 .• 190 k**

**arrntiornalam.**

**1:11 o 1 M Ilya.**

**PP- 1(11)11 lilt. I Cermet 1:13 . 1043 k In\_**

**P3 . Ida k lin.**

**'Mine**

|  |  |
| --- | --- |
|  | **Low Ni Lb" bpor km ■.g. Slomarillill<M. ienrria f 1<S.** |

**Carle ItIEW**

**tarn linuildtICIDrS'**

**II ..\_ T4 - Br 245A,IF2444**

**Lsa lekn Ed, WO OW 31**

**1C1 . - keg -0.1,A 7.1-1 Q.**

**IX 1 711 CP1 IblinPLHFI**

**Dr1,132 - 1 Pill di**

**rum\* Excepi :whom plhwai. wicillipd ppolnorl Imulltiq Is%**

**crottom film. PM lid Ci.14 i CM trylM 4.1441d Id 4. 17i izir illinf nth**

**I i eh pp" 4 lump elnarl !cams**

**1%1 ppm P C max Prawn I n d pcia 1., ed New spoz p1 fad, s h NJ I El ba­:lapis turn clernat types**

**In Lorna calm \*ail. raw F139F 1% miaiDri era oromlfroS far noir aricrtarrn 0 rid 11019111Mturf cthbilhy. •5 41 fike IhN 4CK BEd wham If thireikue I4 difficult to a•otaip, . Ogle irpcigix Irma an may by 4:1Ntialm, Fr cnoldad a 1% Pleial**

**41N kW *r* caviar is mad**

**FORMANT 019**

**chapter 3**

**Parts list to figure 2**

**Resistors:**

**R1 . . . R12 = 100 W1% Miscellaneous:**

**S1 . S12 = KA keyboard contacts**

**Note that one board is required for each octave. For a three-octave synthesiser, for instance, three boards and 36 resistors are required. For further details see text, figures 1 and 3 and photos 1 and 2**

**keyboard construction and power supply**

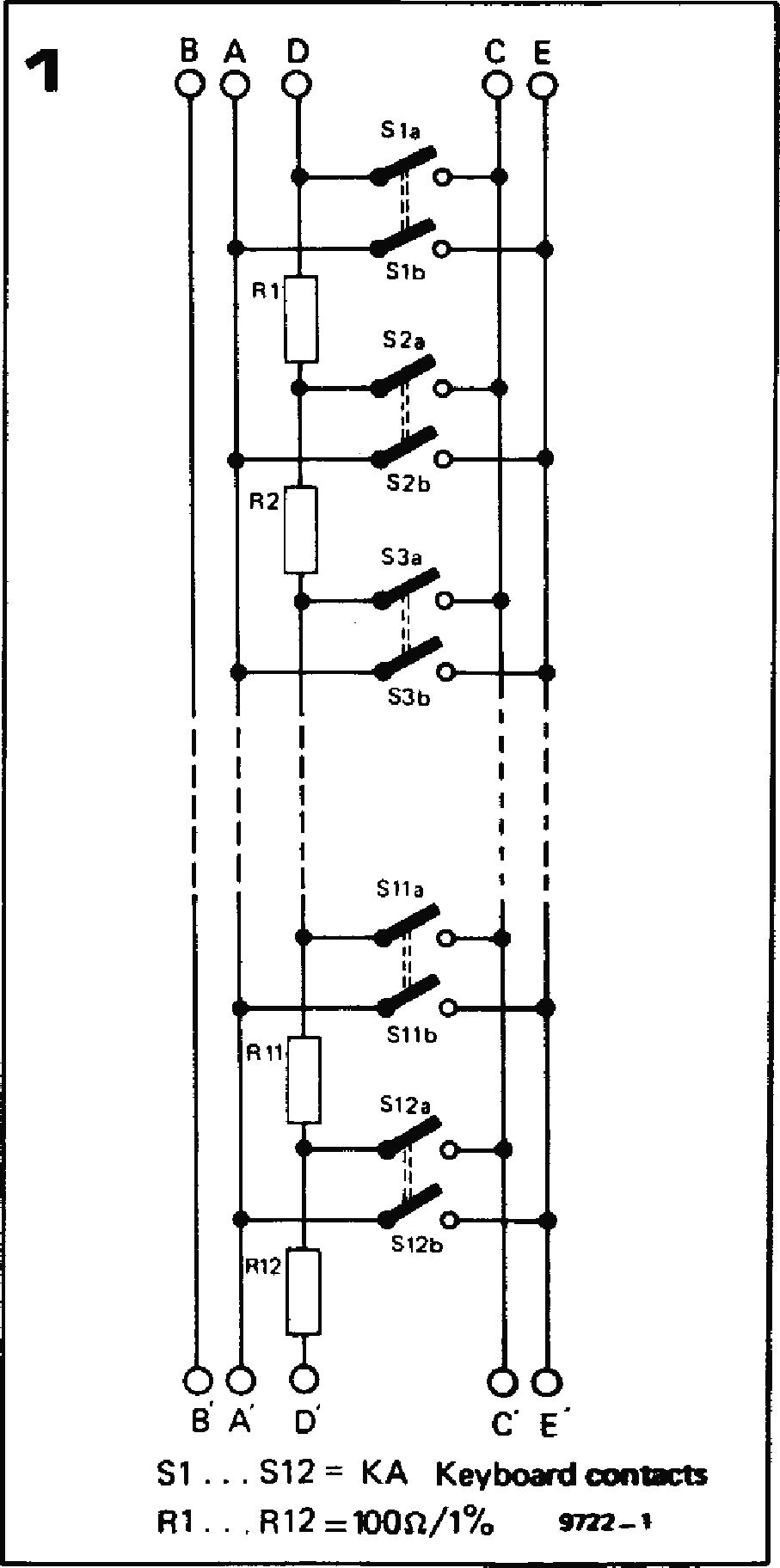
**In this chapter the p.c. board layout for the keyboard resistance divider is given, along with constructional details of the keyboard case and the test procedure for the keyboard interface assembly. The description of the voltage-controlled module unit is then commenced, starting with the power supply and details of the module case.**

Wiring to the keyboard contacts is largely eliminated by mounting the keyboard divider chain on p.c. boards directly behind the keyboard contacts, so that the 'tails' of the contacts can be soldered direct to the p.c. board. The wiring diagram of the keyboard divider boards is given in figure **1.**

The p.c. board and component layout are given in figure 2. Each p.c. board covers one octave of the keyboard, so three p.c. boards are required. They are linked by butting together the ends and wiring across from one board to the next, terminal A to terminal A', **B** to B' and so on.

At the left-hand end of the keyboard points A to E are joined to the corre­sponding points on the interface p.c. board by short wire links. Since each keyboard divider p.c.b. has connec­tions for only twelve sets of key contacts the extreme right-hand set of contacts (note 37) must be wired to the end of the p.c.b. as shown in figure 3. Note also the wire link between points B' and D'.

**In** order that the p.c. boards may be **mounted** directly behind the key contacts by glueing, the resistors and connections to the p.c.b.'s are on the



**Figure 1. Circuit diagram of one keyboard divider p.c.b.**

**Figure 2. Printed circuit board and component layout for the keyboard (EPS 9721-4).**

**Table 1. Selection table for FET source resistors.**

**copper side of the p.c.b.'s. This can clearly be seen** in **photo 1. All the resistors are, of course, 100 S2 1% metal oxide types.**

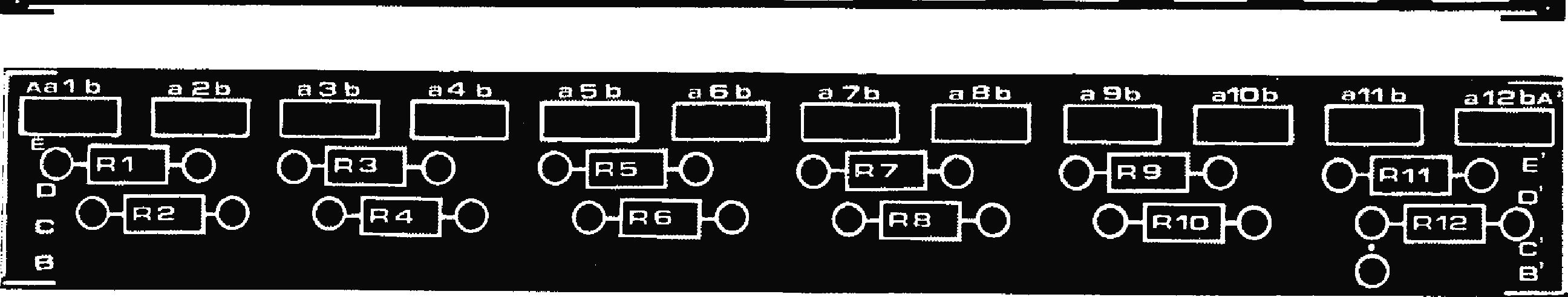
**Selection of FET source resistors**

As **mentioned in** the last article, the source resistors for FETs Ti, T3 and T4 must be selected before the keyboard interface p.c.b. can be completed and tested. This is accomplished using the test circuit of figure 4a. With the gate grounded the gate-source voltage Us is measured and a corresponding source resistor for each transistor is selected from table 1.

|  |  |  |
| --- | --- | --- |
| **Table 1 Us (V)**  **0.2**  **0.25** | | **Rs (k 22**  **18** |
| **0.3** | **. . . 0.4** | **15** |
| **0.4** | **.. . 0.5** | **12** |
| **0.6** | **. . . 0.8** | **10** |
| **0.9** | **. . . 1.1** | **8.2** |
| **1.2** | **... 1.6** | **6.8** |

**At** the same time the gate leakage of **each** transistor should be checked to ensure that it is within acceptable limits. This is done by removing the grounding **link** across **CG** (330 p). This capacitor will now charge through the gate leakage of the FET, and the source voltage will rise. The rate of change of voltage should be slower than one volt per second. Any FET which cannot meet this criterion should be rejected. This test should also be applied to T2, and when the tests are complete each FET, together with its selected source resistor, can be soldered into the cir­cuit. Due to the possibility of leakage around the sample and hold area of the circuit (T1 to T3) great care should be taken to ensure that the back of the board is scrupulously clean, with no blobs of soldering flux or greasy thumbprints. After testing, the back of the board may be sprayed with insu-

**2**



**0**

**alOb**

**Aa l b**

**a a b a 3 b**

**a 4 b**

**a 5 b**

**a 6 b**

**a 7 b**

**a Bb**

**a 9b**

all **b**

**alalaA\***

**1**

I 1

1

I I

I I

I

I I

I I

J

04R1 RD, (:)\_{.., . OR (:)\_ al **R 9 1-0 Oin 11 10 E**

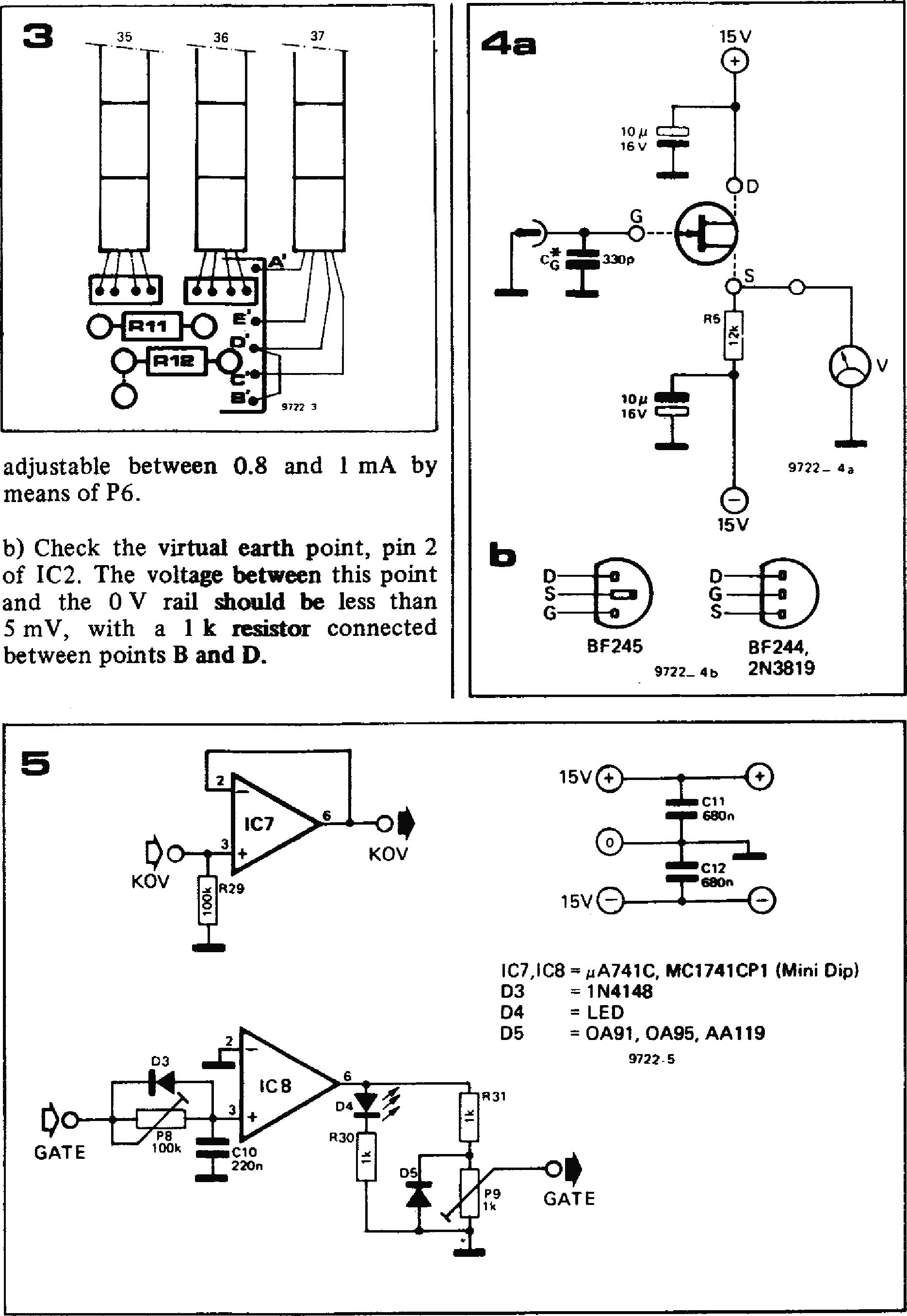
**C**

**CI 0'**

**PO-M-40 FO-F-14:4 F04-76-1-C1 FX7}e3 IF- Od R12** FO

**020 — FORMANT**

lating varnish.



**adjustable between 0.8 and 1 mA by** means of P6.

b) Check the **virtual earth point, pin** 2
  
of 1C2. The **voltage between** this point
  
and the 0 V rail **should be less than**

mV, with a **1 k resistor** connected between points **B and D.**

**IC7,1C8 = i,A741C, MC1741CP1 (Mini Dip)**

**D3 = 1N4148**

**D4 = LED**

**D5 = 0A91, 0A95, AA119**

**9722-5**

**D3**

**GATE**

**0**

**C11**

**680n**

**5**

**15V +®**

0

**15V0**

**C12**

**T**in

Although the BF244 or BF245 is speci­fied for TI to T4, since practically all specimens of this device will function in the circuit, it is possible to use the cheaper and more popular 2N3819 for T2. It should be noted that the board is laid out for the pinning of the BF245. The pinning of the BF244 and most 2N3819's is different, as shown in figure 4b.

**Interface receiver**

In the early design stages the KOV and GATE outputs from the interface board were fed direct into the voltage con­trolled modules. However, it was soon discovered that the input currents taken by these modules caused significant voltage drops along the connecting cable between keyboard and module unit, especially if this was long. In particular, earth return currents along the common earth wire shared by the KOV and GATE outputs caused modulation of the keyboard voltage by the gate pulse. This problem was overcome by provid­ing high impedance buffer stages at the receiving end of the connecting cable. The circuit for this 'interface receiver' is shown in figure 5. It consists simply of a 741 connected as a voltage follower for the KOV input, and a similar voltage follower with an input delay circuit for the GATE input. The output of this circuit also drives an LED to indicate when a gate pulse is present.

A printed circuit board and component layout for the interface receiver are given in figure 6. The two outputs are taken from single screw terminal blocks cut from a 'chocolate block' type of mains connector. This is so that connections to extra voltage controlled modules can be added if and when the system is extended.

**Testing of the keyboard interface assembly**

Once the keyboard interface (see chapter 2, figures 10 and 11) and inter­face receiver boards are complete they can be tested, provided a ± 15 V supply is available, otherwise the testing must wait until the synthesiser power supply has been built. The final adjustment is not carried out until the keyboard assembly is mounted in its case, but these preliminary tests will show up any faults in the circuits and save a lot of frustration at a later stage. The test procedure is as follows:

1. **Current consumption**

Connect positive and negative supplies to the keyboard interface (chapter 2, **figure 10) and measure the current flow in both the positive and negative supply leads. This should be between 18 and 25 mA.**

1. **Keyboard current source**

**a) Connect a multimeter between
  
points B (positive) and D (negative) and**

monitor the current. This should be adjustable between 0.8 and 1 mA by means of P6.

b) Check the virtual earth point, pin 2 of IC2. The voltage between this point and the 0 V rail should be less than 5 mV, with a 1 k resistor connected between points B and D.

1. **Gate circuit**

Connect point E on the interface board to point A via a switch and measure the voltage at point G with respect to 0 V. It should be —12 to —15 V with the switch **open and** +12 to +15 V with **the switch closed.**

1. **Sample and hold**

a) Retain the switch from the previous test. Connect point C to **an SPDT switch so that this point can be switched between point A and ground. With point C grounded, the gate switch closed and P1 set to minimum resistance the source voltage of T4 must be less**

**Figure 3. Showing the wiring to the 37th key contact block.**

**Figure 4. Test circuit for FET's and pinouts of BF244, BF245 and 2N3819.**

**Figure 5. Circuit of the interface receiver.**

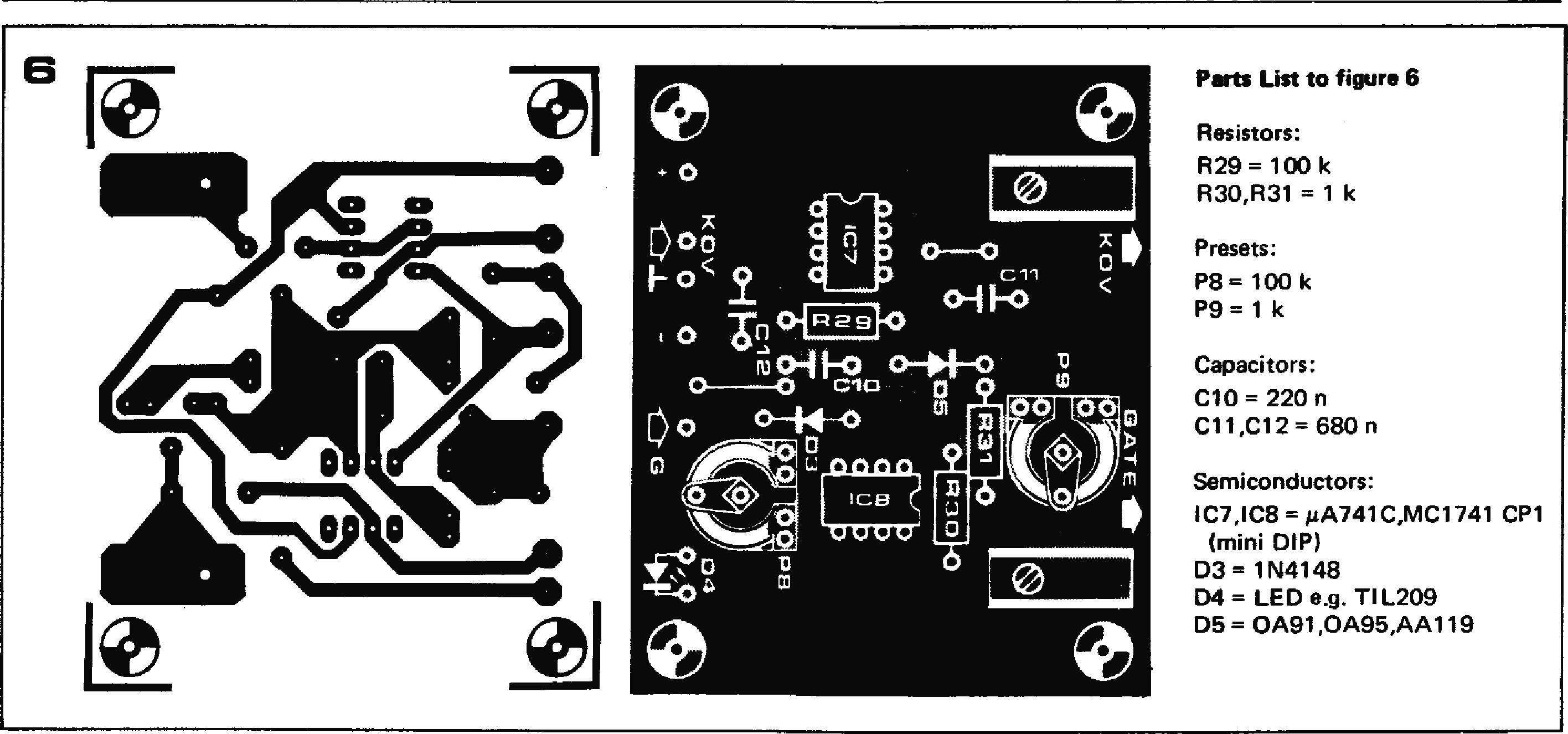
**Figure 6. Printed circuit board and com­ponent layout for the interface receiver (EPS 9721-2).**

**Figure 7. Mounting plate for the interface board.**

than 4 V and should not change **when the gate switch is opened.**

**b) Leave the gate switch open and ground point C using the SPDT switch. The source voltage of T4 must not change. Close the gate switch and the source voltage should now rise by between 3.6 and 4.6 V. Open the gate switch and this new voltage should be maintained.**

**FORMANT — 021**



**Parts List to figure 6**

**Resistors:**

**R29 = 100 k R30,R31 = 1 k**

**Presets:**

**P8 = 100 k**

**P9 = 1 k**

**Capacitors:**

**C10 = 220 n C11,C12 = 680 n**

**Semiconductors:**

**IC7,1C8 = pA741C,MC1741 CP1 (mini DIP)**

**D3 = 1N4148**

**D4 = LED e.g. TI L209**

**D5 = 0A91,0A95,AA119**

**+p**

**;**

**43 CI Vo4.**

**3 0-0**

**F o c a**

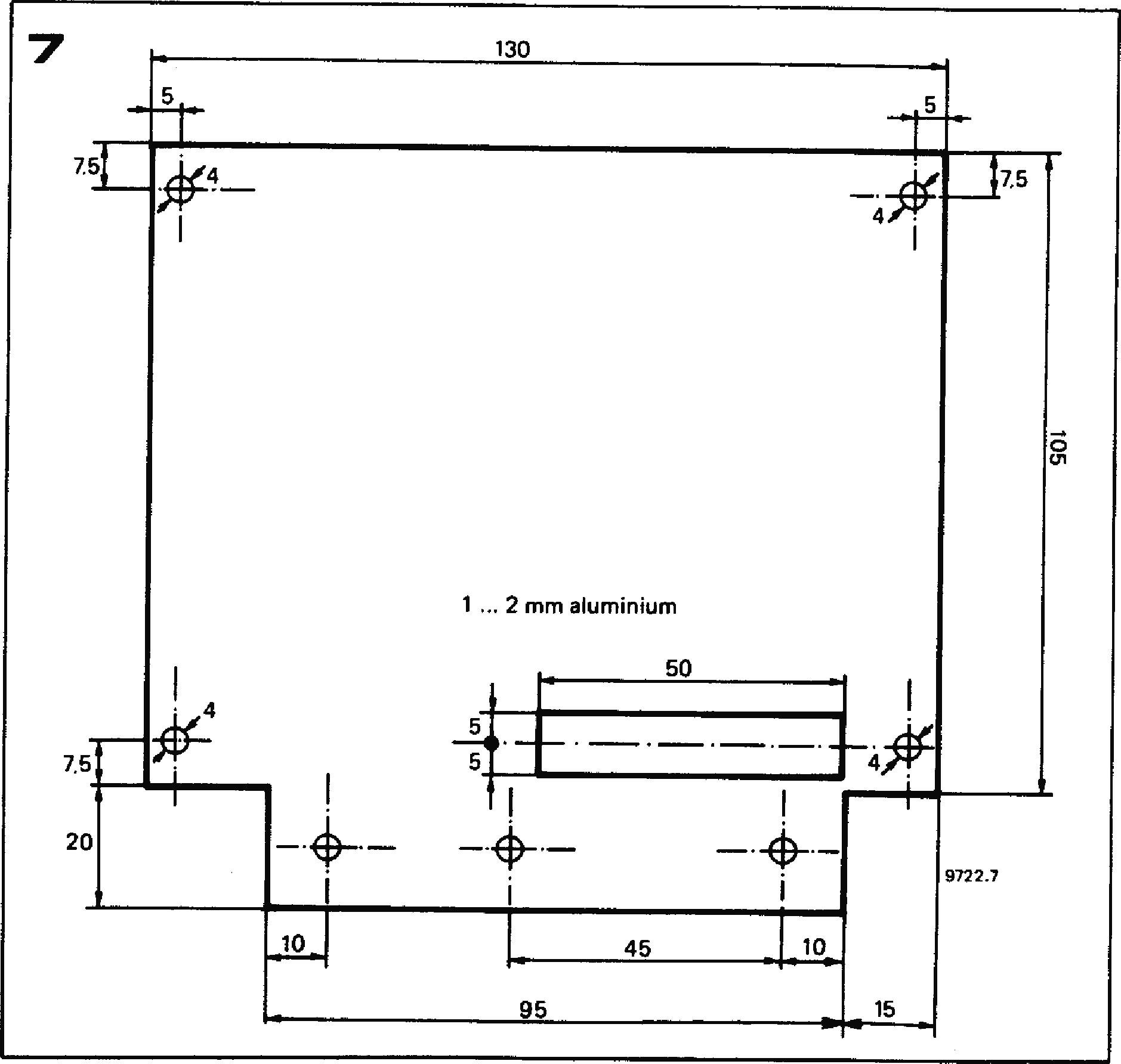
**0 -4"n oin 29N**

**041-0 (>114-40**

**o cio a**

**o 0-W) ul**

**c) Set P1 to maximum resistance, changeover switch to ground point C and close the gate switch. The source voltage of T4 should now drop to** its **original value over two to three seconds.**



**5. Summing amplifier**

1. **Offset adjustment. Maintain the same switch positions as in test 4c. Using S1, switch P2 out of circuit and turn sliders of P3 and P5 to ground. Use P4 to set the KOV output to zero volts.**
2. **Coarse tuning. Switch P2 into circuit using SI and turn P2 fully clockwise and then anticlockwise, when the KOV output should be +5 V and —5 V respectively.**
3. **Fine tuning. Switch P2 out of circuit**

**and turn P5 fully clockwise, when the KOV output should be about 150 mV.**

1. **FM. Turn P5 fully anticlockwise. Link point FM to point A on the board. Using P3 it should be possible to vary the KOV output between zero volts and about 10 V.**

**6. Interface receiver**

**Interconnect the interface and interface receiver boards (connections GATE, KOV, +15 V, —15 V and ground). Repeat tests 3 and 5b, but monitor the KOV and GATE outputs of the inter­face receiver. With the gate** switch **closed the indicator LED should glow. Finally, with the gate switch closed, use P9 to set the gate output voltage of the**

**interface receiver to +5 V. Keyboard unit assembly**

**Once the interface board has been tested, it and the keyboard can be joined to make an integrated keyboard unit.**

**This is accomplished** by **first making an aluminium mounting plate for the interface board, as shown in figure 7. The 'tongue' of this plate fits along the underside of the keyboard chassis (at the left-hand end) and is secured by three 4 mm nuts, bolts and lockwashers. A solder tag beneath one of the nuts provides an earthing point for the keyboard. Note that the larger diameter hole in the tongue is not used yet; it will be required for mounting the keyboard in its case.**

**The next step is to mount the keyboard divider boards. As illustrated in photo 1, these boards should be interlinked in such 'a way that the ends of the boards actually touch at the junction, as other­wise the spacing of the contacts on the board with respect to the switch contact blocks will not be accurate. As** described in chapter 2 (and illustrated in figure 2b), the contact blocks should **be glued or bolted to a 3 mm thick plastic spacer (F). The keyboard divider boards can now also be mounted on this spacer, using either epoxy adhesive or double-sided self-adhesive tape (`Servotape', Tesatape' or similar). Note that the front of the divider boards should touch the contact blocks, as otherwise the wires from the blocks may be too short. The interface board can now be mounted on top of its mounting plate using 4 mm nuts, bolts and spacers, and connections between the keyboard p.c.b.'s and the interface board are made by short wire links which pass through the rectangular slot in the mounting plate. The earthing point for the keyboard chassis is connected to point 'F' on the interface board. The complete assembly can be seen in photos 2 and 3.**