

Board name: PolyVent\_SPI\_Controller

An interface card for the PolyVent to bring out SPI bus for the GPAD.  
Level shift from 5V to 3V on the CIP0.

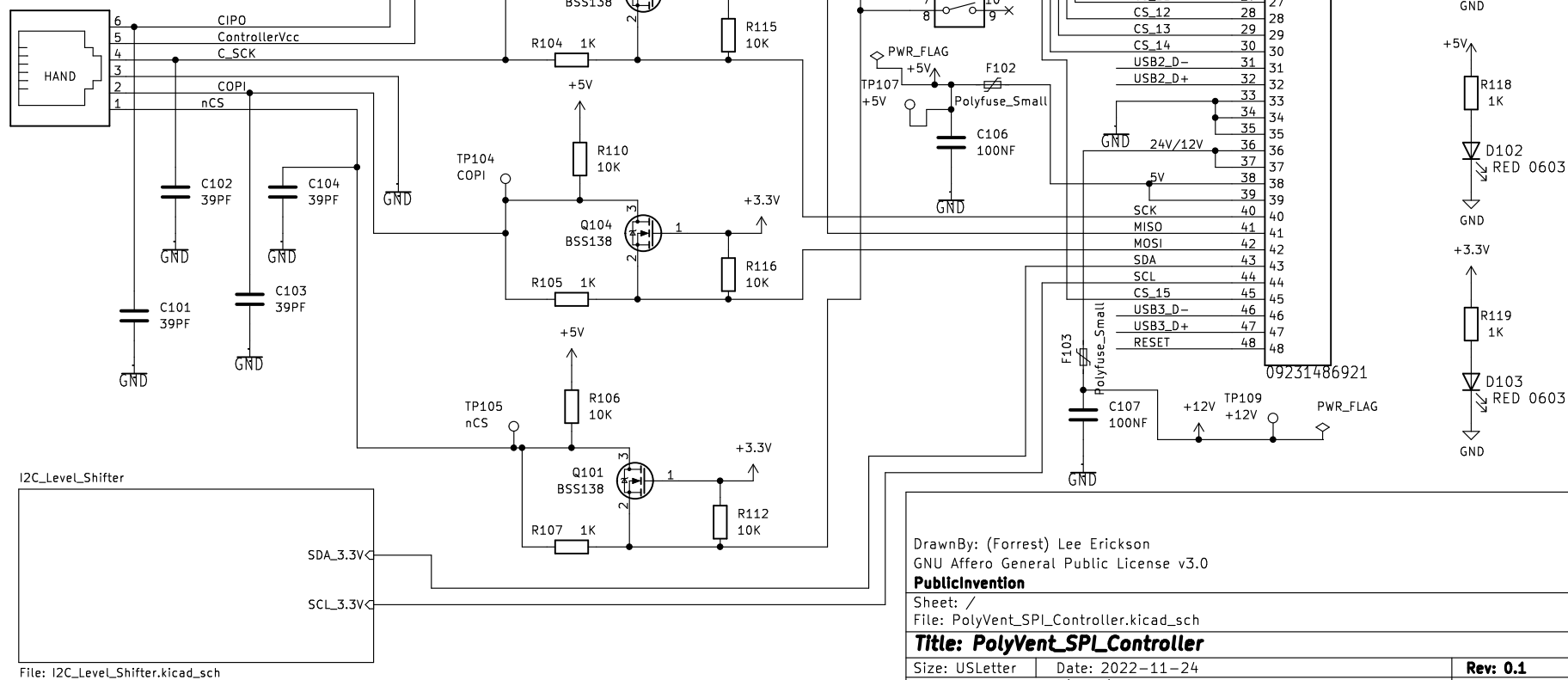
Also to bring out I2C buss through bidirectional level shifters.

J102 is an RJ12 Connector for SPI with 5V logic level.

### SPI Connector Signal Names

CIPO: Controller In, Peripheral Out  
C\_SCK: Controller Serial Clock (output)  
COPI: Controller Out, Peripheral In  
nCS: not Chip Select, ie /CHIP\_SELECT

J102  
RJ12\_6P6C\_H0RZ



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## PublicInvention

Sheet: /  
File: PolyVent\_SPI\_Controller.kicad\_sch

<b>Title:</b> PolyVent_SPI_Controller
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