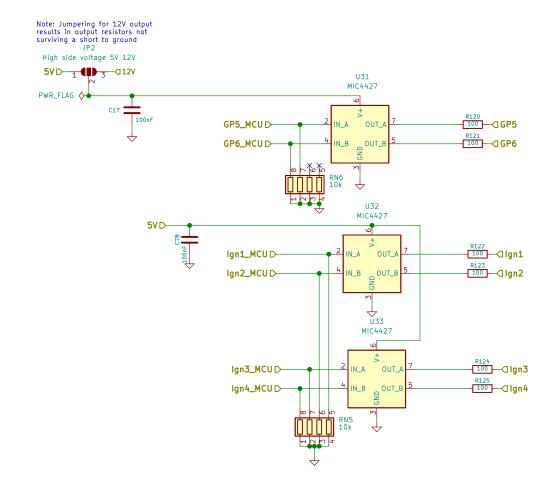


6 channel high / low side driver

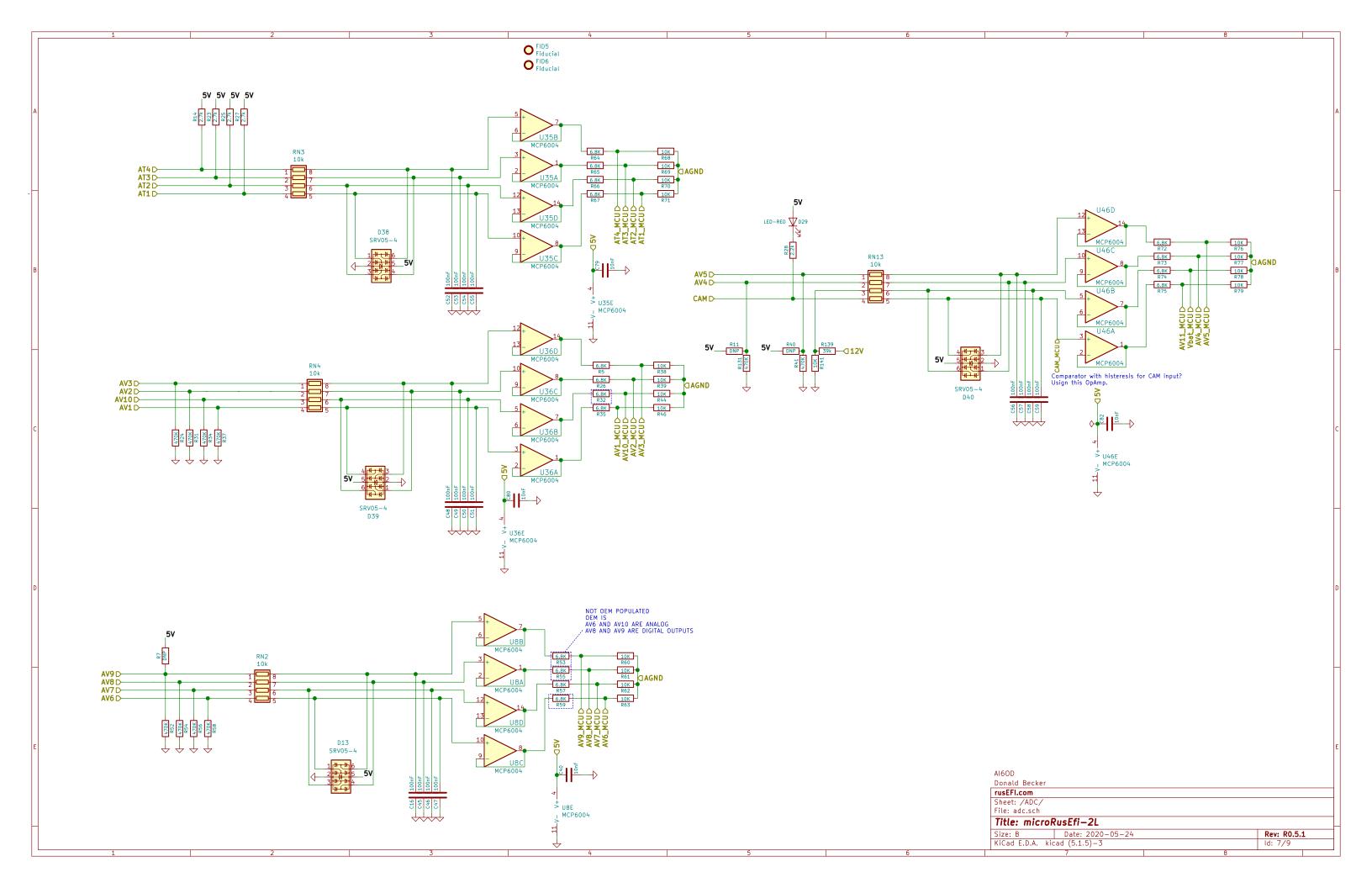


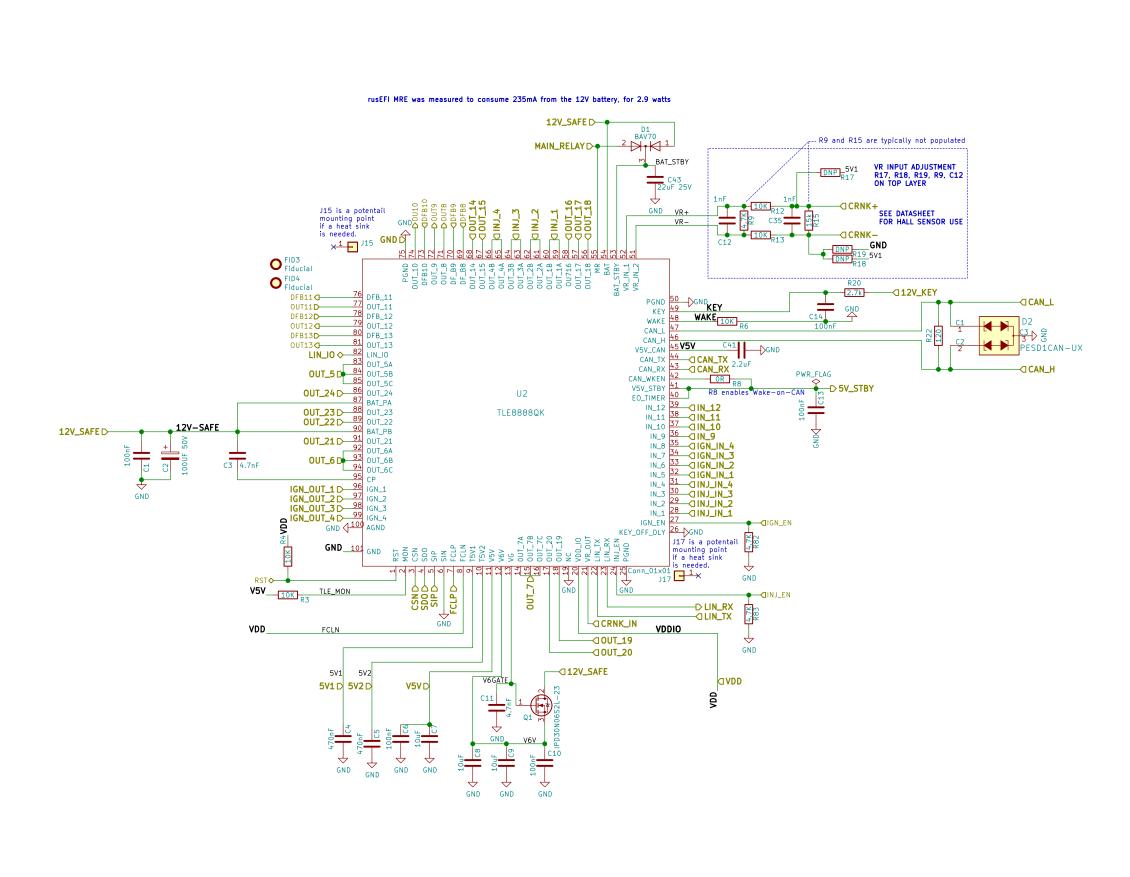
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Sheet: /hi-lo/ File: hi-lo.sch

Title: microRusEfi-2L

Size: B Date: 2020-05-24 KiCad E.D.A. kicad (5.1.5)-3 **Rev: R0.5.1** Id: 6/9





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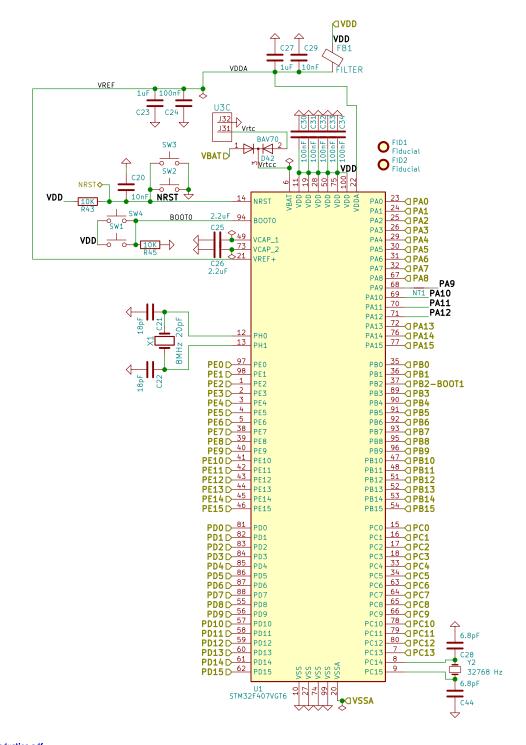
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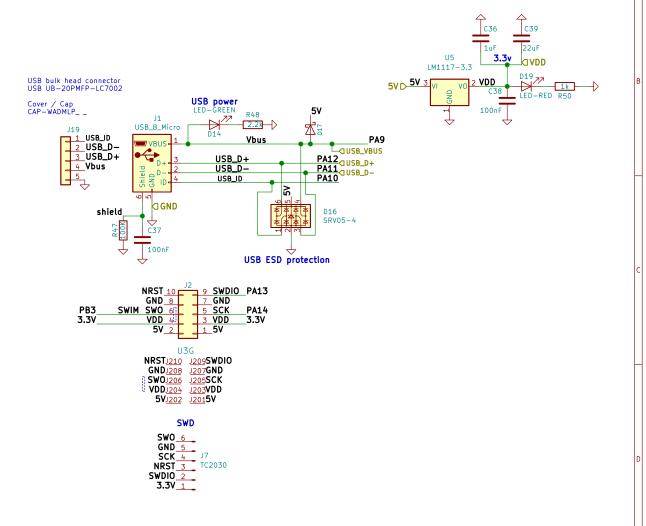
 Title: microRusEfi—2L

 Size: User
 Date: 2020-05-24

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http://www.crystek.com/documents/appnotes/Pierce-GateIntroduction.pdf PCB per predictions with SaturnPCB has less then 3.5pF traces, STM32 pins assumed 5pF ESR = 80ohms max??
Rf = 2meg could be between 1meg and 10meg.
Cload should be 8pf per XTAL datasheet
Cload = ([Cin+C1][C2+Cout])/(Cin+C1+C2_Cout)+PCBstray
Cload = ([Cin+C1][4.7+5])/(5+4.7+4.7+5.5) = 8.35pF
C1=C2=C166=C167 = 4.7pF
Rs = 1/(2piFC2) = 1/(2*pi*8MHz*4.7pF) = 4.2ohms.

Sheet: /MCU/ File: stm32.sch

Title: microRusEfi-2L

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 Size: B
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