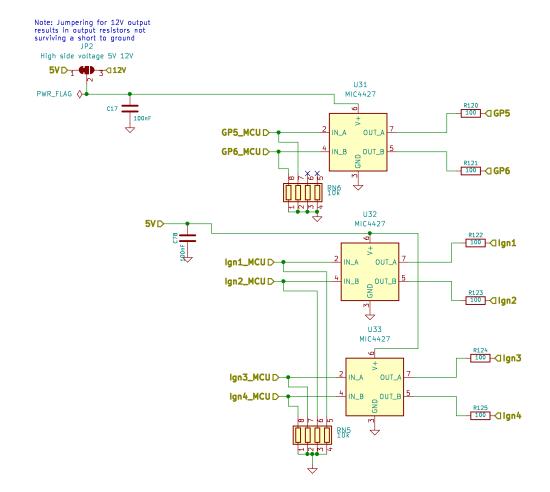
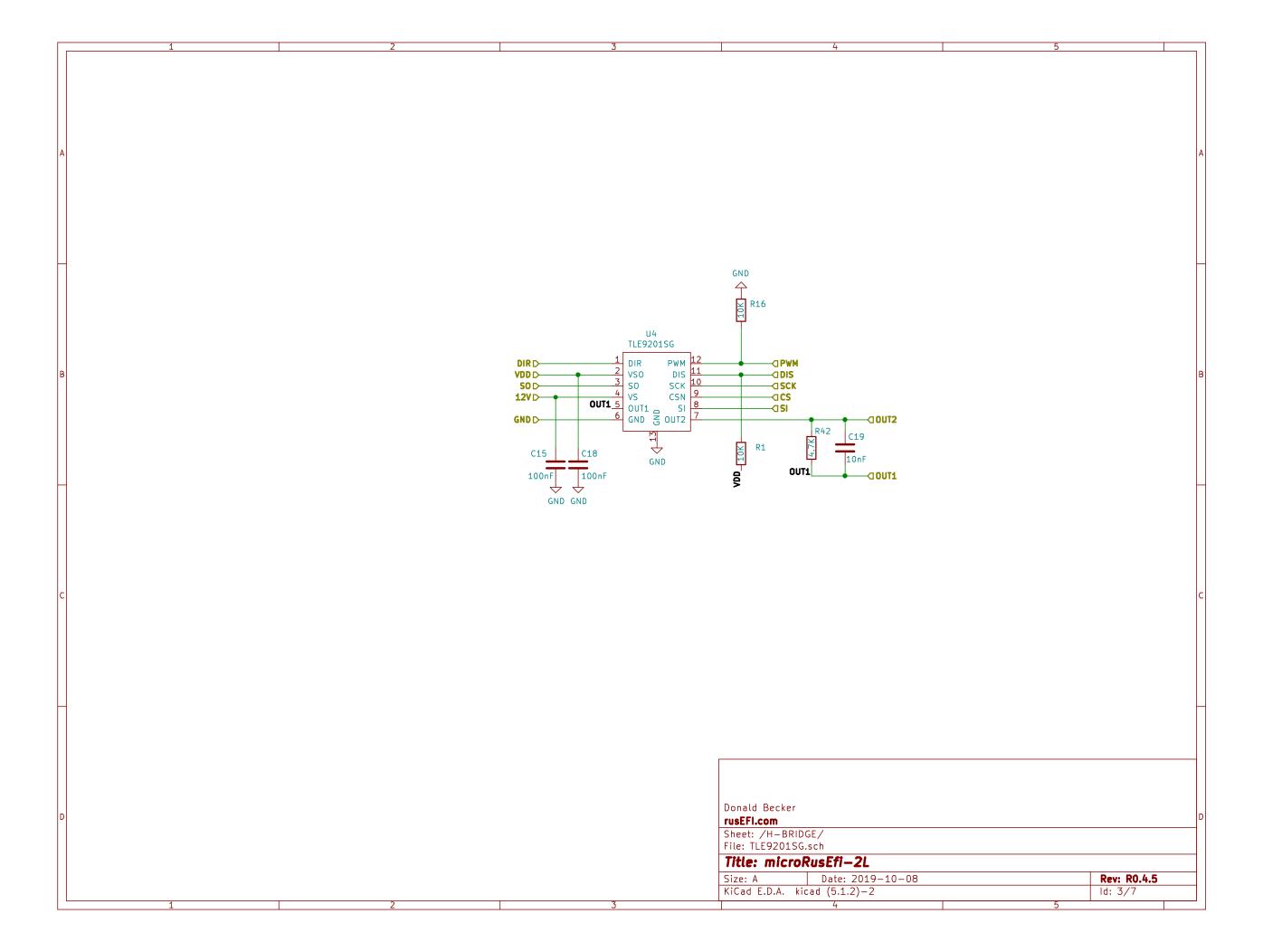


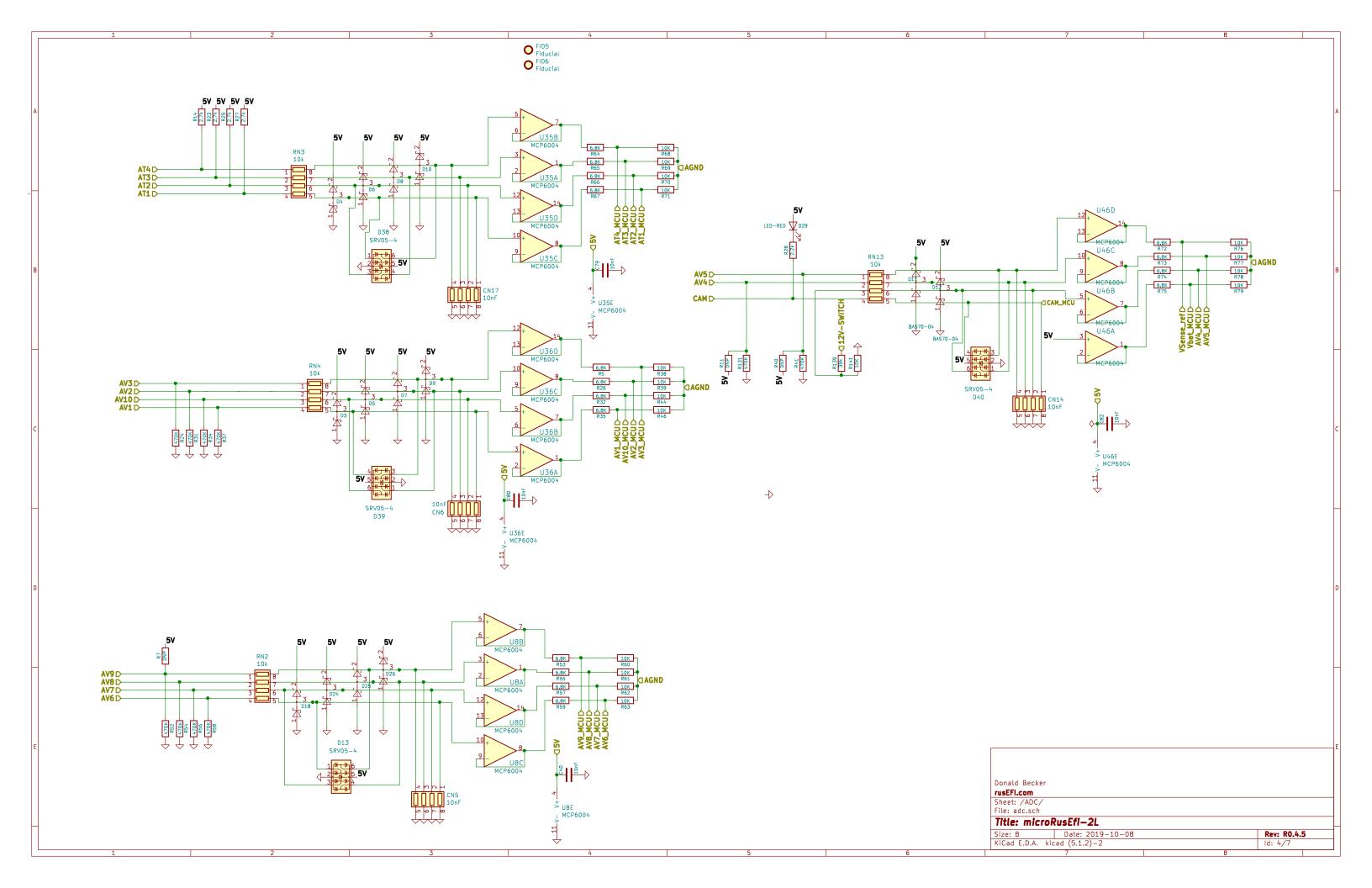
6 channel high / low side driver

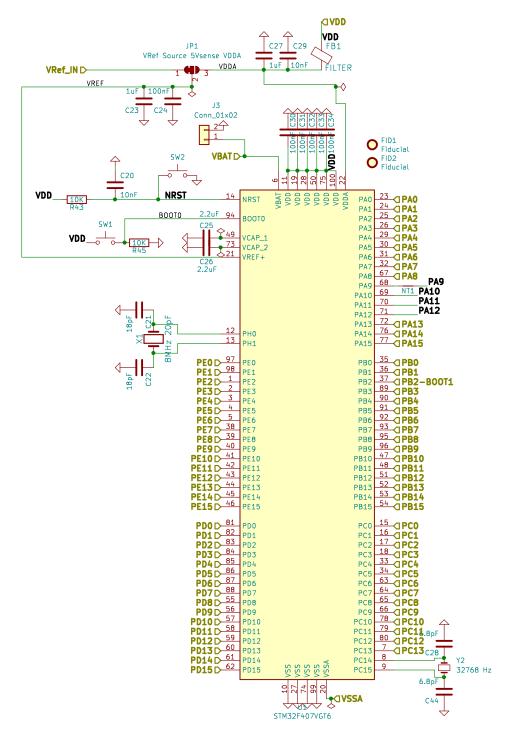


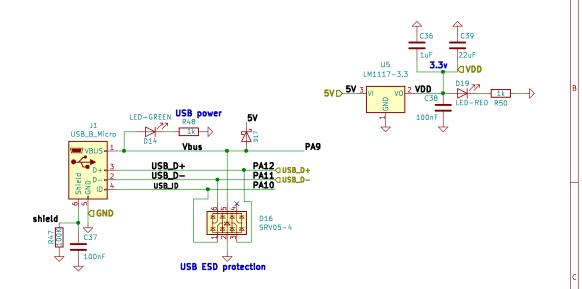
Donald Becker rusEFI.com Sheet: /hi-lo/ File: hi-lo.sch Title: microRusEfi-2L

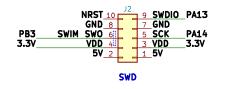
Size: B Date: 2019-10-08 KiCad E.D.A. kicad (5.1.2)-2 **Rev: R0.4.5** Id: 2/7











Donald Becker rusEFI.com Sheet: /MCU/ File: stm32.sch Title: microRusEfi-2L Date: 2019-10-08 Rev: R0.4.5 Size: B KiCad E.D.A. kicad (5.1.2)-2 ld: 5/7

http://www.crystek.com/documents/appnotes/Pierce-GateIntroduction.pdf PCB per predictions with SaturnPCB has less then 3.5pF traces, STM32 pins assumed 5pF ESR = 80ohms max??

Rf = 2meg could be between 1meg and 10meg.
Cload should be 8pF per XTAL datasheet
Cload = ([Cin+C1][C2+Cout])/(Cin+C1+C2-Cout)+PCBstray
Cload = ([Cin+C1][4.7+5])/(5+4.7+4.7+5.)+3.5= 8.35pF
C1=C2=C166=C167 = 4.7pF
Rs = 1/(2piFC2) = 1/(2*pi*8MHz*4.7pF) = 4.2ohms.

