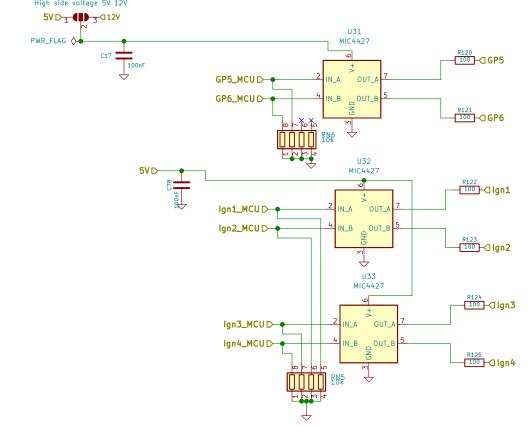


6 channel high / low side driver Note: Jumpering for 3.27 estpat results in output resistors not surviving a short to grown surviving a short to grown grow



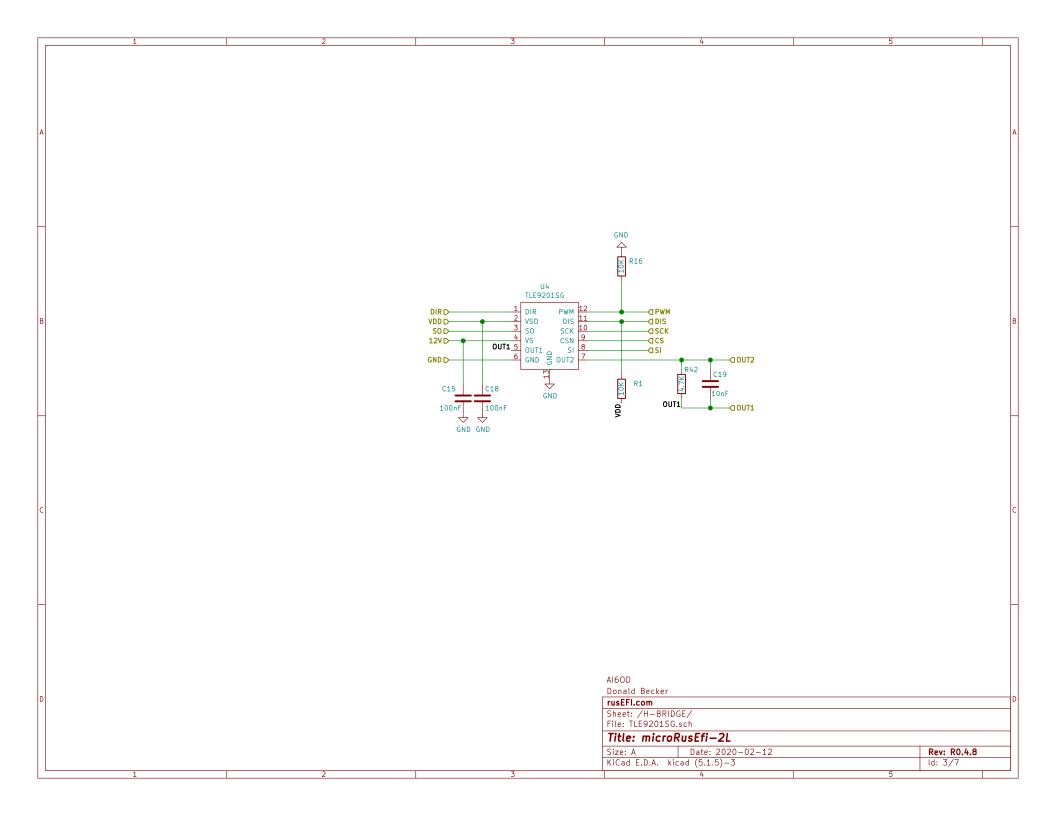
Al60D
Donald Becker

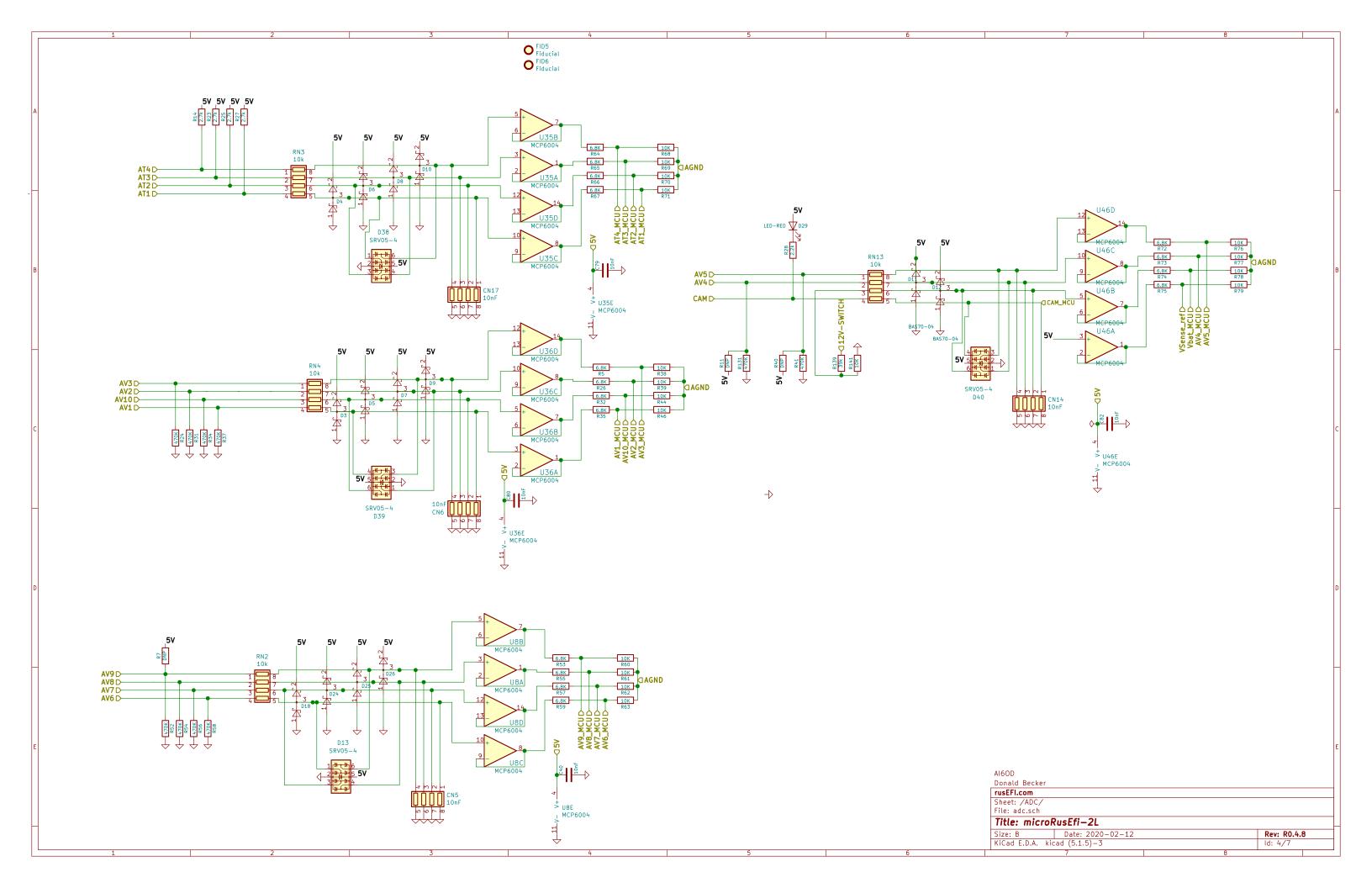
rusEF1.com
Sheet: /hi-lo/
File: hi-lo.sch

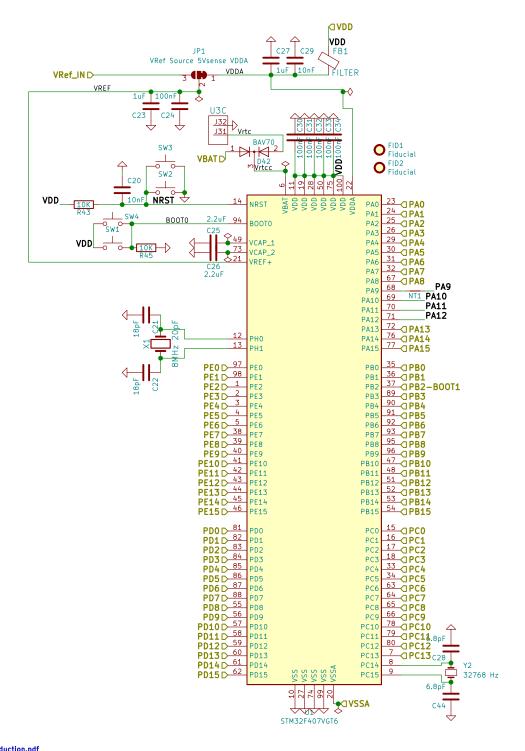
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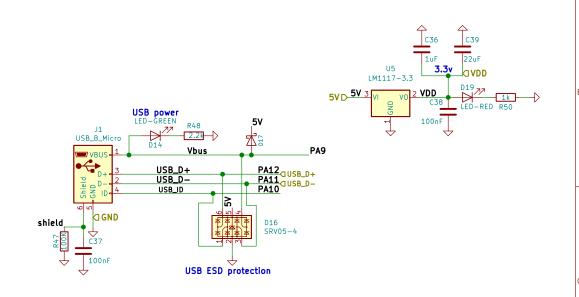
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 Date: 2020-02-12
 Rev: R0.4.8

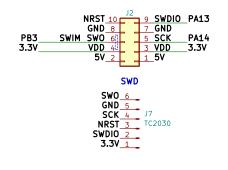
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 Id: 2/7











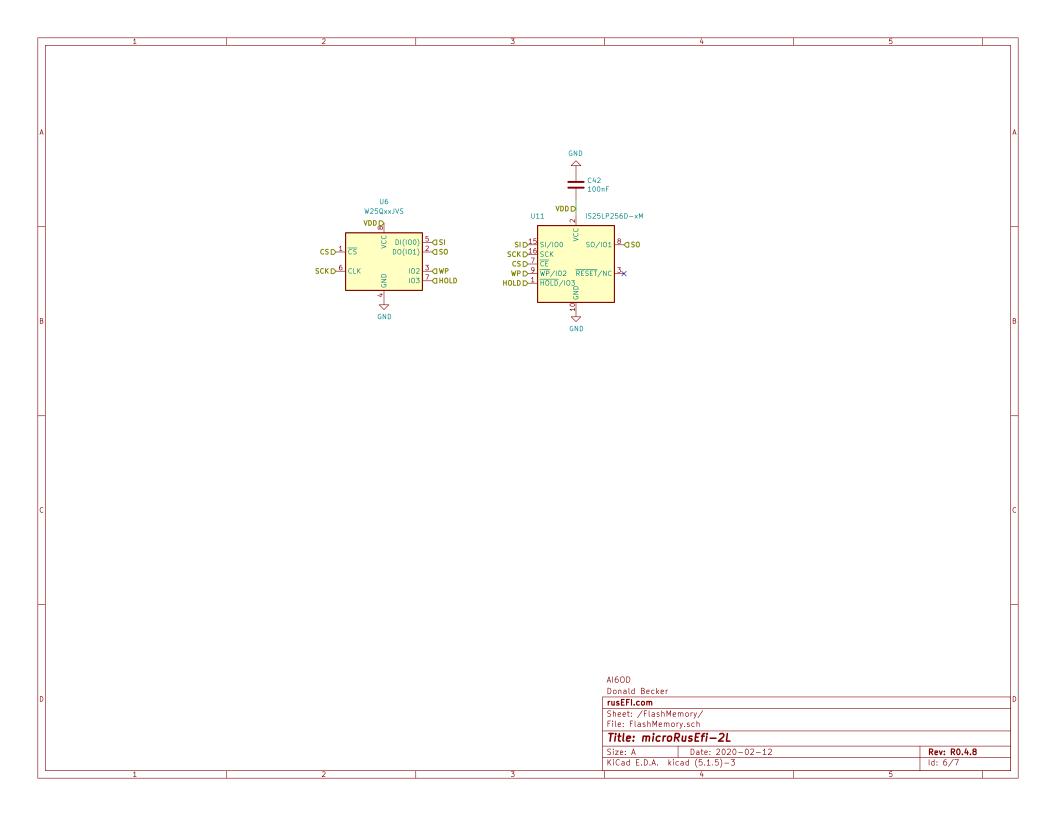
AI60D Donald Becker

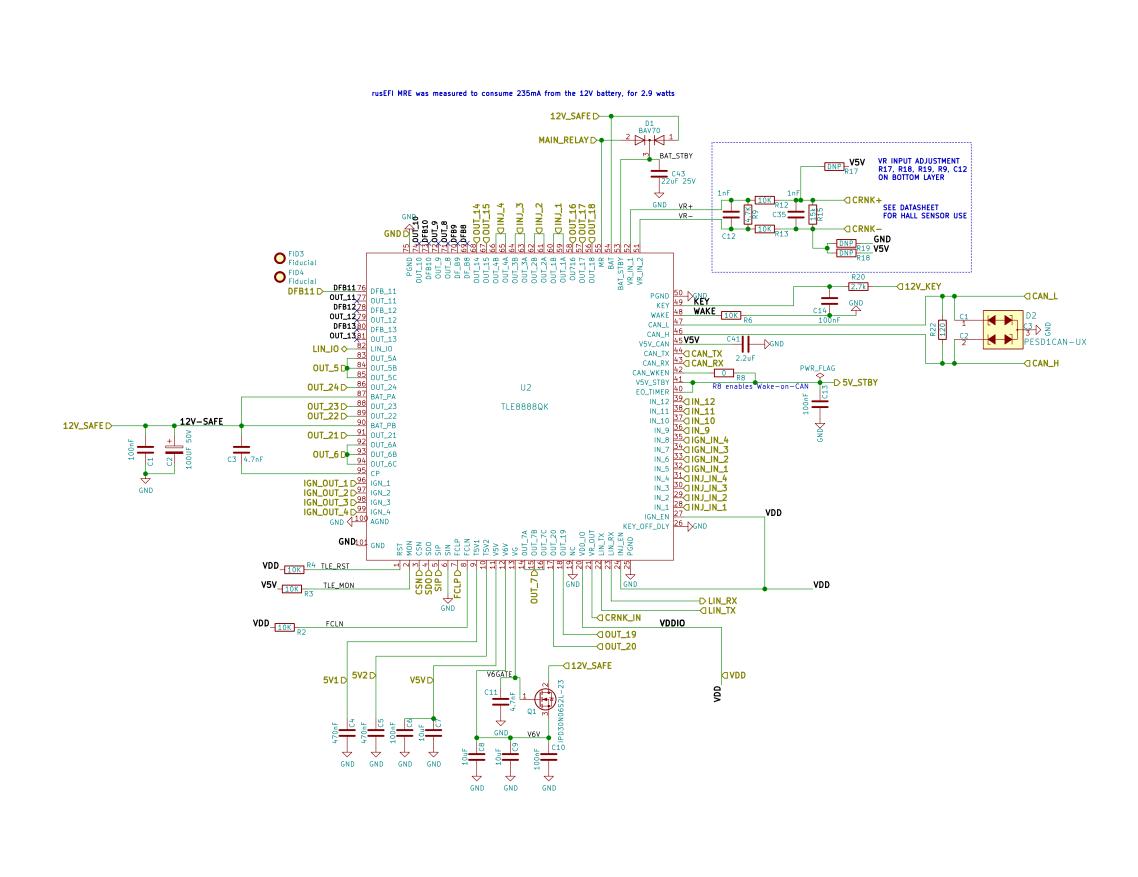
rusEFI.com Sheet: /MCU/

File: stm32.sch Title: microRusEfi-2L

Size: B Date: 2020-02-12 KiCad E.D.A. kicad (5.1.5)-3 Size: B Rev: R0.4.8 ld: 5/7

http://www.crystek.com/documents/appnotes/Pierce-GateIntroduction.pdf PCB per predictions with SaturnPCB has less then 3.5pF traces, STM32 pins assumed 5pF ESR = 80ohms max??
Rf = 2meg could be between 1meg and 10meg.
Cload should be 8pf per XTAL datasheet
Cload = ([Cin+C1][C2+Cout])/(Cin+C1+C2_Cout)+PCBstray
Cload = ([Cin+C1][4.7+5])/(5+4.7+4.7+5.5) = 8.35pF
C1=C2=C166=C167 = 4.7pF
Rs = 1/(2piFC2) = 1/(2*pi*8MHz*4.7pF) = 4.2ohms.





AI60D

Donald Becker

rusEFl.com

Sheet: /TLE8888-1QK/
File: TLE8888-1QK.sch

 Title: microRusEfi—2L

 Size: User
 Date: 2020-02-12

 KiCad E.D.A. kicad (5.1.5)-3
 8

Rev: R0.4.8 Id: 7/7