



1. Description

1.1. Project

| | |
|-----------------|--------------------|
| Project Name | 3D_Scanner |
| Board Name | custom |
| Generated with: | STM32CubeMX 6.11.1 |
| Date | 02/12/2025 |

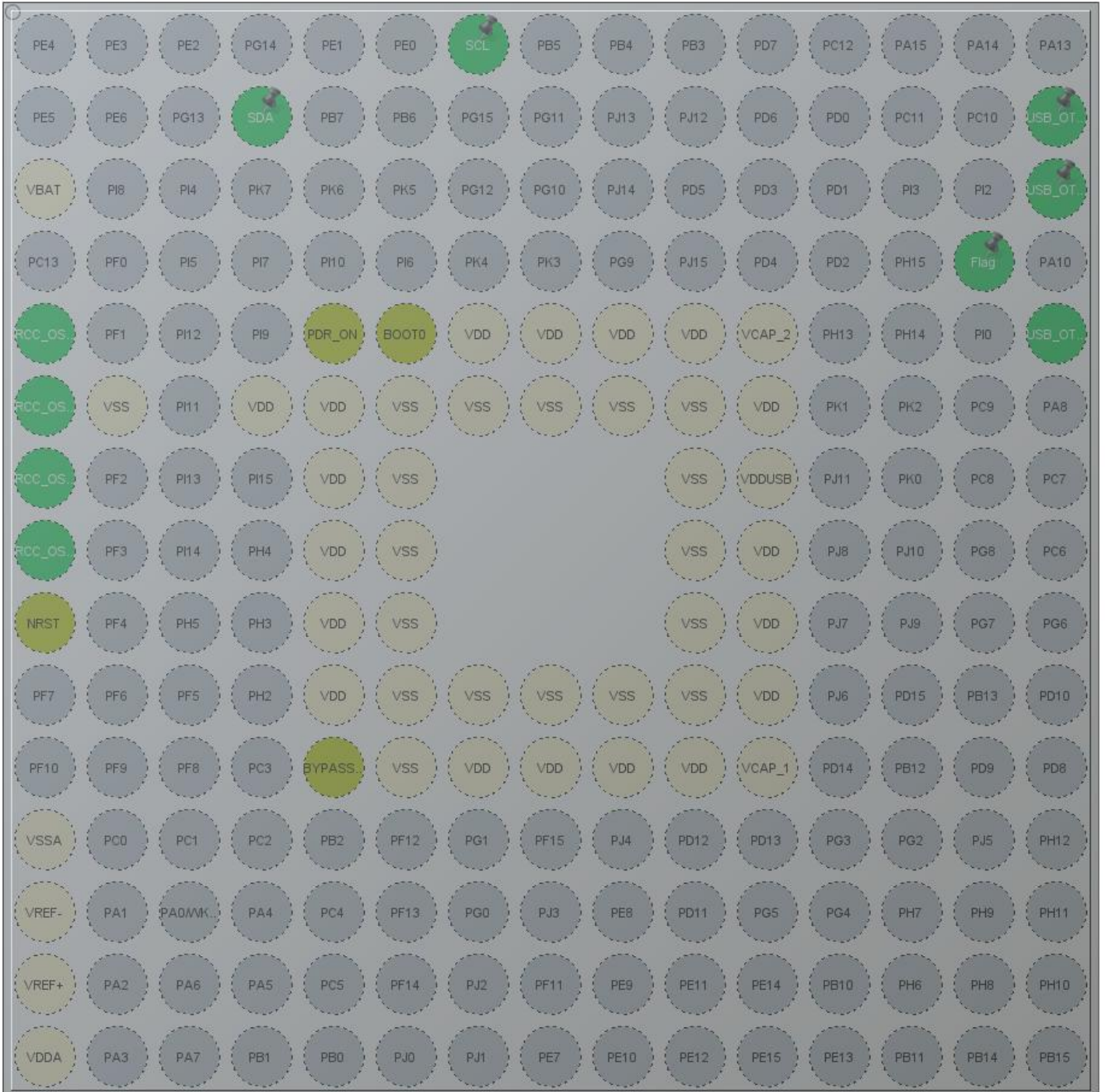
1.2. MCU

| | |
|----------------|---------------|
| MCU Series | STM32F7 |
| MCU Line | STM32F7x6 |
| MCU name | STM32F746NGHx |
| MCU Package | TFBGA216 |
| MCU Pin number | 216 |

1.3. Core(s) information

| | |
|---------|---------------|
| Core(s) | Arm Cortex-M7 |
|---------|---------------|

2. Pinout Configuration



TFBGA216 (Top view)

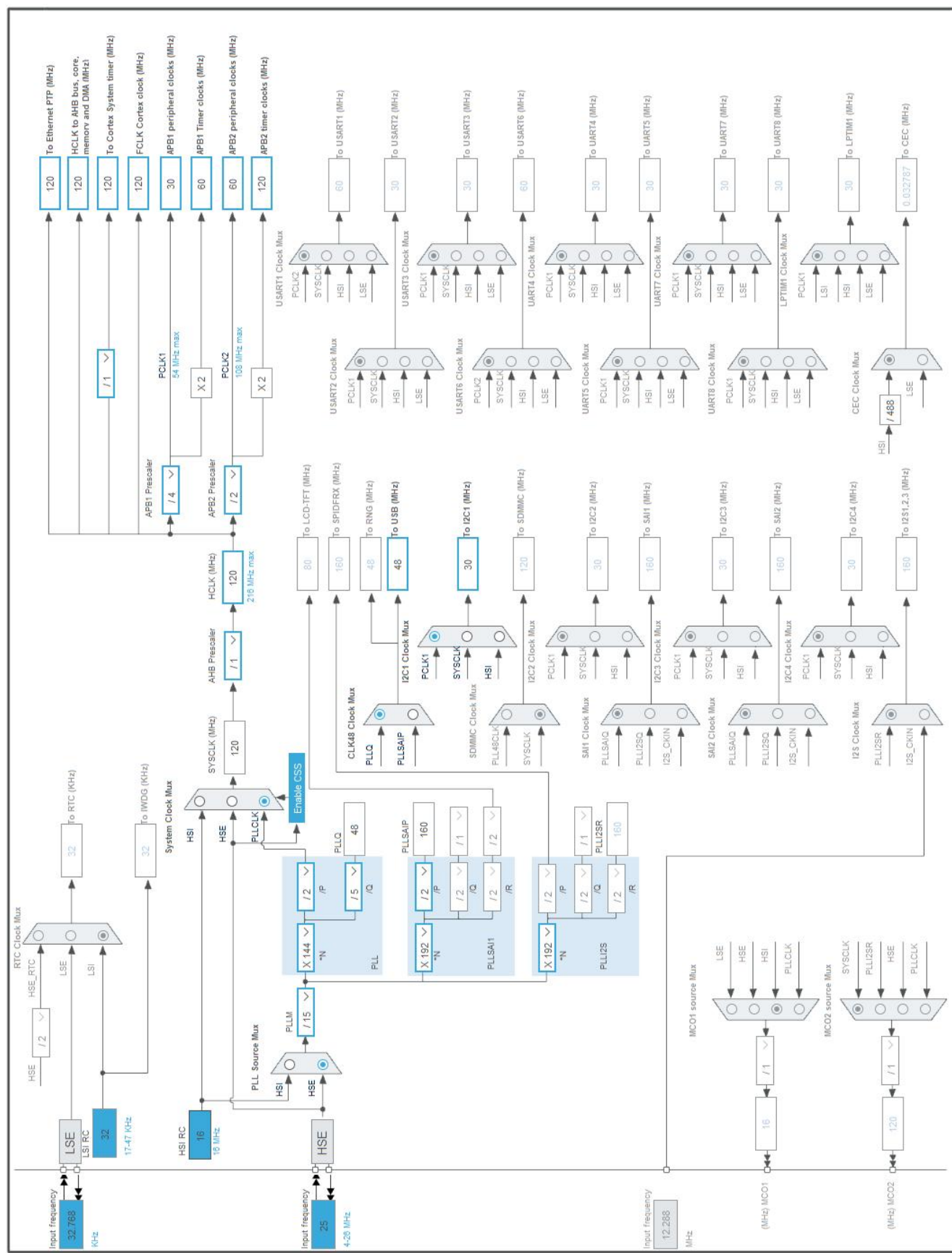
3. Pins Configuration

| Pin Number TFBGA216 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|------------------------|---------------------------------------|----------|--------------------------|-------|
| A7 | PB8 | I/O | I2C1_SCL | SCL |
| B4 | PB9 | I/O | I2C1_SDA | SDA |
| B15 | PA12 | I/O | USB_OTG_FS_DP | |
| C1 | VBAT | Power | | |
| C15 | PA11 | I/O | USB_OTG_FS_DM | |
| D14 | PI1 * | I/O | GPIO_Output | Flag |
| E1 | PC14/OSC32_IN | I/O | RCC_OSC32_IN | |
| E5 | PDR_ON | Reset | | |
| E6 | BOOT0 | Boot | | |
| E7 | VDD | Power | | |
| E8 | VDD | Power | | |
| E9 | VDD | Power | | |
| E10 | VDD | Power | | |
| E11 | VCAP_2 | Power | | |
| E15 | PA9 | I/O | USB_OTG_FS_VBUS | |
| F1 | PC15/OSC32_OUT | I/O | RCC_OSC32_OUT | |
| F2 | VSS | Power | | |
| F4 | VDD | Power | | |
| F5 | VDD | Power | | |
| F6 | VSS | Power | | |
| F7 | VSS | Power | | |
| F8 | VSS | Power | | |
| F9 | VSS | Power | | |
| F10 | VSS | Power | | |
| F11 | VDD | Power | | |
| G1 | PH0/OSC_IN | I/O | RCC_OSC_IN | |
| G5 | VDD | Power | | |
| G6 | VSS | Power | | |
| G10 | VSS | Power | | |
| G11 | VDDUSB | Power | | |
| H1 | PH1/OSC_OUT | I/O | RCC_OSC_OUT | |
| H5 | VDD | Power | | |
| H6 | VSS | Power | | |
| H10 | VSS | Power | | |
| H11 | VDD | Power | | |
| J1 | NRST | Reset | | |

| Pin Number TFBGA216 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|------------------------|---------------------------------------|----------|--------------------------|-------|
| J5 | VDD | Power | | |
| J6 | VSS | Power | | |
| J10 | VSS | Power | | |
| J11 | VDD | Power | | |
| K5 | VDD | Power | | |
| K6 | VSS | Power | | |
| K7 | VSS | Power | | |
| K8 | VSS | Power | | |
| K9 | VSS | Power | | |
| K10 | VSS | Power | | |
| K11 | VDD | Power | | |
| L5 | BYPASS_REG | Reset | | |
| L6 | VSS | Power | | |
| L7 | VDD | Power | | |
| L8 | VDD | Power | | |
| L9 | VDD | Power | | |
| L10 | VDD | Power | | |
| L11 | VCAP_1 | Power | | |
| M1 | VSSA | Power | | |
| N1 | VREF- | Power | | |
| P1 | VREF+ | Power | | |
| R1 | VDDA | Power | | |

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

| Name | Value |
|-----------------------------------|---|
| Project Name | 3D_Scanner |
| Project Folder | C:\Users\lenovo\Desktop\3D_Scanner\3D_Scanner |
| Toolchain / IDE | STM32CubeIDE |
| Firmware Package Name and Version | STM32Cube FW_F7 V1.17.2 |
| Application Structure | Advanced |
| Generate Under Root | Yes |
| Do not generate the main() | No |
| Minimum Heap Size | 0x200 |
| Minimum Stack Size | 0x400 |

5.2. Code Generation Settings

| Name | Value |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | Yes |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power consumption) | No |
| Enable Full Assert | No |

5.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|--------------------|--------------------------|
| 1 | SystemClock_Config | RCC |
| 2 | MX_GPIO_Init | GPIO |
| 3 | MX_I2C1_Init | I2C1 |
| 4 | MX_USB_DEVICE_Init | USB_DEVICE |

1. Power Consumption Calculator report

1.1. Microcontroller Selection

| | |
|-----------|---------------|
| Series | STM32F7 |
| Line | STM32F7x6 |
| MCU | STM32F746NGHx |
| Datasheet | DS10916_Rev4 |

1.2. Parameter Selection

| | |
|-------------|-----|
| Temperature | 25 |
| Vdd | 3.3 |

1.3. Battery Selection

| | |
|-------------------|--------------|
| Battery | Alkaline(9V) |
| Capacity | 625.0 mAh |
| Self Discharge | 0.3 %/month |
| Nominal Voltage | 9.0 V |
| Max Cont Current | 200.0 mA |
| Max Pulse Current | 0.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

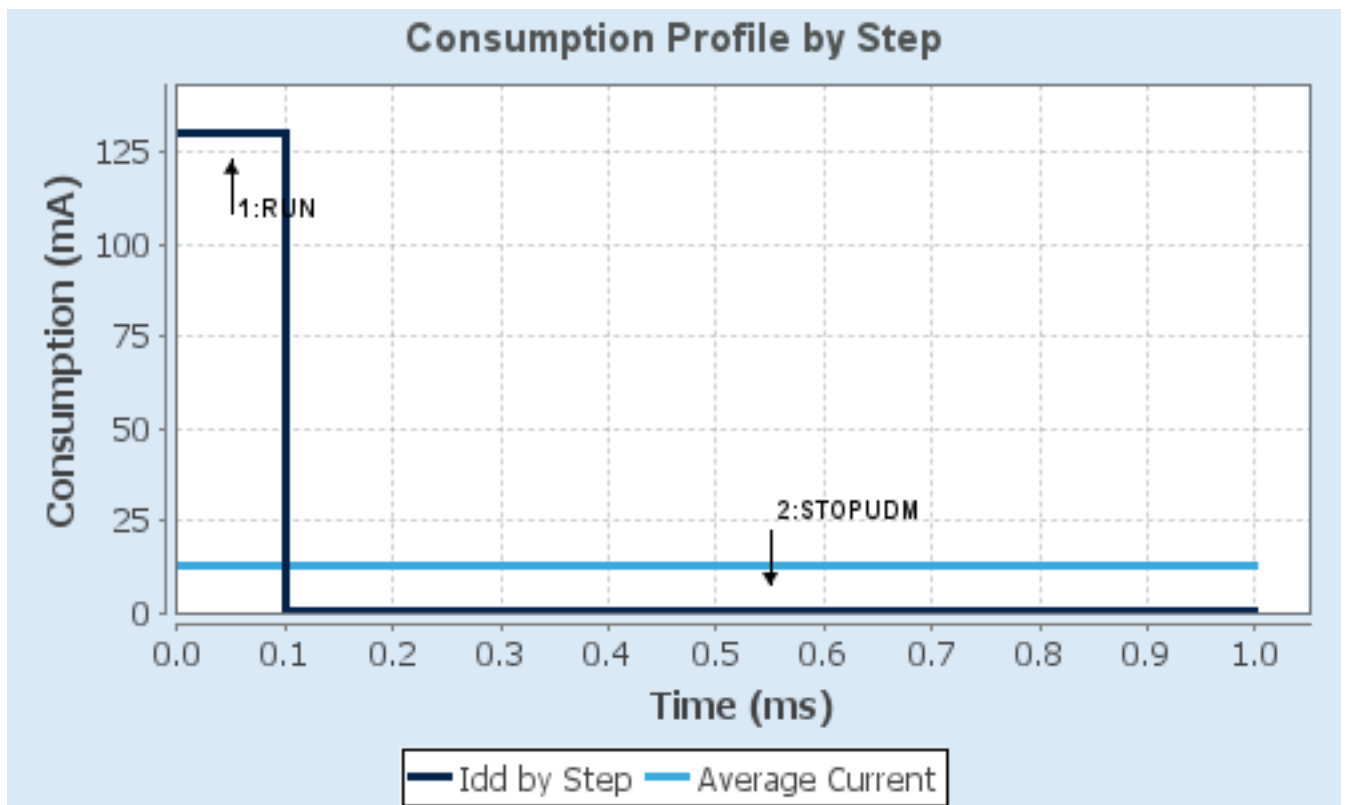
1.4. Sequence

| | | |
|-------------------------------|------------------|---------------------------|
| Step | Step1 | Step2 |
| Mode | RUN | STOP UDM (Under Drive) |
| Vdd | 3.3 | 3.3 |
| Voltage Source | Battery | Battery |
| Range | Scale1-High | No Scale |
| Fetch Type | ITCM/FLASH/REGON | n/a |
| CPU Frequency | 216 MHz | 0 Hz |
| Clock Configuration | HSE PLL | Regulator LP Flash-PwrDwn |
| Clock Source Frequency | 4 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 130 mA | 100 μ A |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 462.0 | 0.0 |
| Ta Max | 92.56 | 104.99 |
| Category | In DS Table | In DS Table |

1.5. Results

| | | | |
|---------------|-----------------|-----------------|-----------------|
| Sequence Time | 1 ms | Average Current | 13.09 mA |
| Battery Life | 1 day, 23 hours | Average DMIPS | 462.24005 DMIPS |

1.6. Chart



2. Peripherals and Middlewares Configuration

2.1. CORTEX_M7

2.1.1. Parameter Settings:

Speculation default mode Settings:

Speculation default mode **Enabled ***

Cortex Interface Settings:

| | |
|----------------------|---------------|
| Flash Interface | AXI Interface |
| ART ACCELERATOR | Disabled |
| Instruction Prefetch | Disabled |
| CPU ICache | Disabled |
| CPU DCache | Disabled |

Cortex Memory Protection Unit Control Settings:

MPU Control Mode Background Region Privileged accesses only + MPU Disabled during hard fault, NMI and FAULTMASK handlers

Cortex Memory Protection Unit Region 0 Settings:

| | |
|-----------------------------|--------------------------|
| MPU Region | Enabled |
| MPU Region Base Address | 0x0 * |
| MPU Region Size | 4GB |
| MPU SubRegion Disable | 0x87 * |
| MPU TEX field level | level 0 |
| MPU Access Permission | ALL ACCESS NOT PERMITTED |
| MPU Instruction Access | DISABLE |
| MPU Shareability Permission | ENABLE |
| MPU Cacheable Permission | DISABLE |
| MPU Bufferable Permission | DISABLE |

Cortex Memory Protection Unit Region 1 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 2 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 3 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 4 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 5 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 6 Settings:

MPU Region Disabled

Cortex Memory Protection Unit Region 7 Settings:

MPU Region Disabled

2.2. I2C1

I2C: I2C

2.2.1. Parameter Settings:

Timing configuration:

| | |
|-------------------------------|---------------------|
| I2C Speed Mode | Standard Mode |
| I2C Speed Frequency (KHz) | 100 |
| Rise Time (ns) | 0 |
| Fall Time (ns) | 0 |
| Coefficient of Digital Filter | 0 |
| Analog Filter | Enabled |
| Timing | 0x007074AF * |

Slave Features:

| | |
|----------------------------------|----------|
| Clock No Stretch Mode | Disabled |
| General Call Address Detection | Disabled |
| Primary Address Length selection | 7-bit |
| Dual Address Acknowledged | Disabled |
| Primary slave address | 0 |

2.3. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

Low Speed Clock (LSE) : Crystal/Ceramic Resonator

2.3.1. Parameter Settings:

System Parameters:

| | |
|-------------------|--------------------|
| VDD voltage (V) | 3.3 |
| Flash Latency(WS) | 3 WS (4 CPU cycle) |

RCC Parameters:

| | |
|--------------------------------|----------|
| HSI Calibration Value | 16 |
| TIM Prescaler Selection | Disabled |
| HSE Startup Timeout Value (ms) | 100 |
| LSE Startup Timeout Value (ms) | 5000 |

Power Parameters:

| | |
|-------------------------------|---------------------------------|
| Power Over Drive | Disabled |
| Power Regulator Voltage Scale | Power Regulator Voltage Scale 3 |

2.4. SYS

Timebase Source: SysTick

2.5. USB_OTG_FS

Mode: Device_Only

mode: Activate_VBUS

2.5.1. Parameter Settings:

| | |
|-----------------------|---------------------|
| Speed | Full Speed 12MBit/s |
| Low power | Disabled |
| Link Power Management | Disabled |
| VBUS sensing | Enabled |
| Signal start of frame | Disabled |

2.6. USB_DEVICE

Class For FS IP: Communication Device Class (Virtual Port Com)

2.6.1. Parameter Settings:

Basic Parameters:

| | |
|--|------------------------------------|
| USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces) | 1 |
| USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration) | 1 |
| USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors) | 512 |
| USBD_SELF_POWERED (Enabled self power) | Enabled |
| USBD_DEBUG_LEVEL (USBD Debug Level) | 0: No debug message |
| USBD_LPM_ENABLED (Link Power Management) | 1: Link Power Management supported |

Class Parameters:

| | |
|------------------------|------|
| USB CDC Rx Buffer Size | 2048 |
| USB CDC Tx Buffer Size | 2048 |

2.6.2. Device Descriptor:

Device Descriptor:

| | |
|---|------------------------|
| VID (Vendor Identifier) | 1155 |
| LANGID_STRING (Language Identifier) | English(United States) |
| MANUFACTURER_STRING (Manufacturer Identifier) | STMicroelectronics |

Device Descriptor FS:

| | |
|---|-----------------------|
| PID (Product Identifier) | 22336 |
| PRODUCT_STRING (Product Identifier) | STM32 Virtual ComPort |
| CONFIGURATION_STRING (Configuration Identifier) | CDC Config |
| INTERFACE_STRING (Interface Identifier) | CDC Interface |

* User modified value

3. System Configuration

3.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|------------|----------------|-----------------|-------------------------------|-----------------------------|-----------------------|------------|
| I2C1 | PB8 | I2C1_SCL | Alternate Function Open Drain | No pull-up and no pull-down | Very High * | SCL |
| | PB9 | I2C1_SDA | Alternate Function Open Drain | No pull-up and no pull-down | Very High * | SDA |
| RCC | PC14/OSC32_IN | RCC_OSC32_IN | n/a | n/a | n/a | |
| | PC15/OSC32_OUT | RCC_OSC32_OUT | n/a | n/a | n/a | |
| | PH0/OSC_IN | RCC_OSC_IN | n/a | n/a | n/a | |
| | PH1/OSC_OUT | RCC_OSC_OUT | n/a | n/a | n/a | |
| USB_OTG_FS | PA12 | USB_OTG_FS_DP | Alternate Function Push Pull | No pull-up and no pull-down | Very High * | |
| | PA11 | USB_OTG_FS_DM | Alternate Function Push Pull | No pull-up and no pull-down | Very High * | |
| | PA9 | USB_OTG_FS_VBUS | Input mode | No pull-up and no pull-down | n/a | |
| GPIO | PI1 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | Flag |

3.2. DMA configuration

nothing configured in DMA service

3.3. NVIC configuration

3.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| Memory management fault | true | 0 | 0 |
| Pre-fetch fault, memory access fault | true | 0 | 0 |
| Undefined instruction or illegal state | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Debug monitor | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 15 | 0 |
| USB On The Go FS global interrupt | true | 0 | 0 |
| PVD interrupt through EXTI line 16 | unused | | |
| Flash global interrupt | unused | | |
| RCC global interrupt | unused | | |
| I2C1 event interrupt | unused | | |
| I2C1 error interrupt | unused | | |
| FPU global interrupt | unused | | |

3.3.2. NVIC Code generation

| Enabled interrupt Table | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|---|-----------------------------------|----------------------|------------------|
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| Memory management fault | false | true | false |
| Pre-fetch fault, memory access fault | false | true | false |
| Undefined instruction or illegal state | false | true | false |
| System service call via SWI instruction | false | true | false |
| Debug monitor | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |
| USB On The Go FS global interrupt | false | true | true |

* User modified value

4. System Views

4.1. Category view

4.1.1. Current

5. Docs & Resources

| Type | Link |
|-------------------------|---|
| BSDL files | https://www.st.com/resource/en/bsdl_model/stm32f7_bsdl.zip |
| IBIS models | https://www.st.com/resource/en/ibis_model/stm32f7_ibis.zip |
| System View Description | https://www.st.com/resource/en/svd/stm32f7-svd.zip |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_eval_tools_portfolio.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/microcontrollers-stm32h7rs-lines-overview.pdf |
| Brochures | https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32nucleo.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstmcsuite.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32trust.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32gui.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf |

- Application Notes https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4031-using-the-stm32f2-stm32f4-and-stm32f7-series-dma-controller-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4566-extending-the-dac-performance-of-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4660-migration-of-microcontroller-applications-from-stm32f42xxxf43xxx-devices-to-stm32f7-series-devices-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4661-getting-started-with-stm32f7-series-mcu-hardware-development-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4676-stm32f7-series-peripheral-interconnections-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4731-stm32cube-mcu-package-examples-for-stm32f7-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4776-generalpurpose-

timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4839-level-1-cache-on-stm32f7-series-and-stm32h7-series-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4850-stm32-mcus-spreadspectrum-clock-generation-principles-properties-and-implementation-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4936-migration-of-microcontroller-applications-from-stm32f7-series-to-stm32h743753-line-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4946-migration-of-microcontroller-applications-between-stm32f72xxx73xxx-and-stm32f74xxx75xxx-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5020-digital-camera-interface-dcml-on-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5073-receiving-spdif-audio-stream-with-the-stm32f4f7h7-series-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4760-quadspi-interface-on-stm32-microcontrollers-and-microprocessors-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4899-stm32-microcontroller-gpio-hardware-settings-and-lowpower-consumption-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5612-esd-protection-of-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5293-migration-guide-from-stm32f7-series-to-stmh74x75x-stm32h72x73x-and-stmh7a37bx-

devices-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4838-introduction-to-memory-protection-unit-management-on-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4879-introduction-to-usb-hardware-and-pcb-guidelines-using-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5225-introduction-to-usb-typec-power-delivery-for-stm32-mcus-and-mpus-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an5537-how-to-use-adc-oversampling-techniques-to-improve-signal-to-noise-ratio-on-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5036-guidelines-for-thermal-management-on-stm32-applications-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4230-introduction-to-random-number-generation-validation-using-the-nist-statistical-test-suite-for-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2867-guidelines-for-oscillator-design-on-stm8afals-and-stm32-mcus-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4013-introduction-to-timers-for-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4277-how-to-use-pwm-shutdown-for-motor-control-and-digital-power-conversion-on-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4759-introduction-to-using-the-hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4861-introduction-to-lcd-ft-display-controller-ltcd-on-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4908-getting-started-with-usart-automatic-baud-rater-detection-for-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5156-introduction-to-security-for-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5543-guidelines-for-enhanced-spi-communication-on-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an1202_freertos_guide-for_related_Tools_freertos-guide-stmicroelectronics.pdf
& Software

Application Notes https://www.st.com/resource/en/application_note/an1602_semihosting_in_for_related_Tools_truestudio-how-to-do-semihosting-in-truestudio-stmicroelectronics.pdf
& Software

Application Notes https://www.st.com/resource/en/application_note/an1801_stm32cubeprog_for_related_Tools_rammer_in_truestudio-installing-stm32cubeprogrammer-in-truestudio-stmicroelectronics.pdf
& Software

Application Notes https://www.st.com/resource/en/application_note/atollic_editing_keyboard_for_related_Tools_shortcuts-atollic-editing-keyboard-shortcuts-stmicroelectronics.pdf
& Software

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