

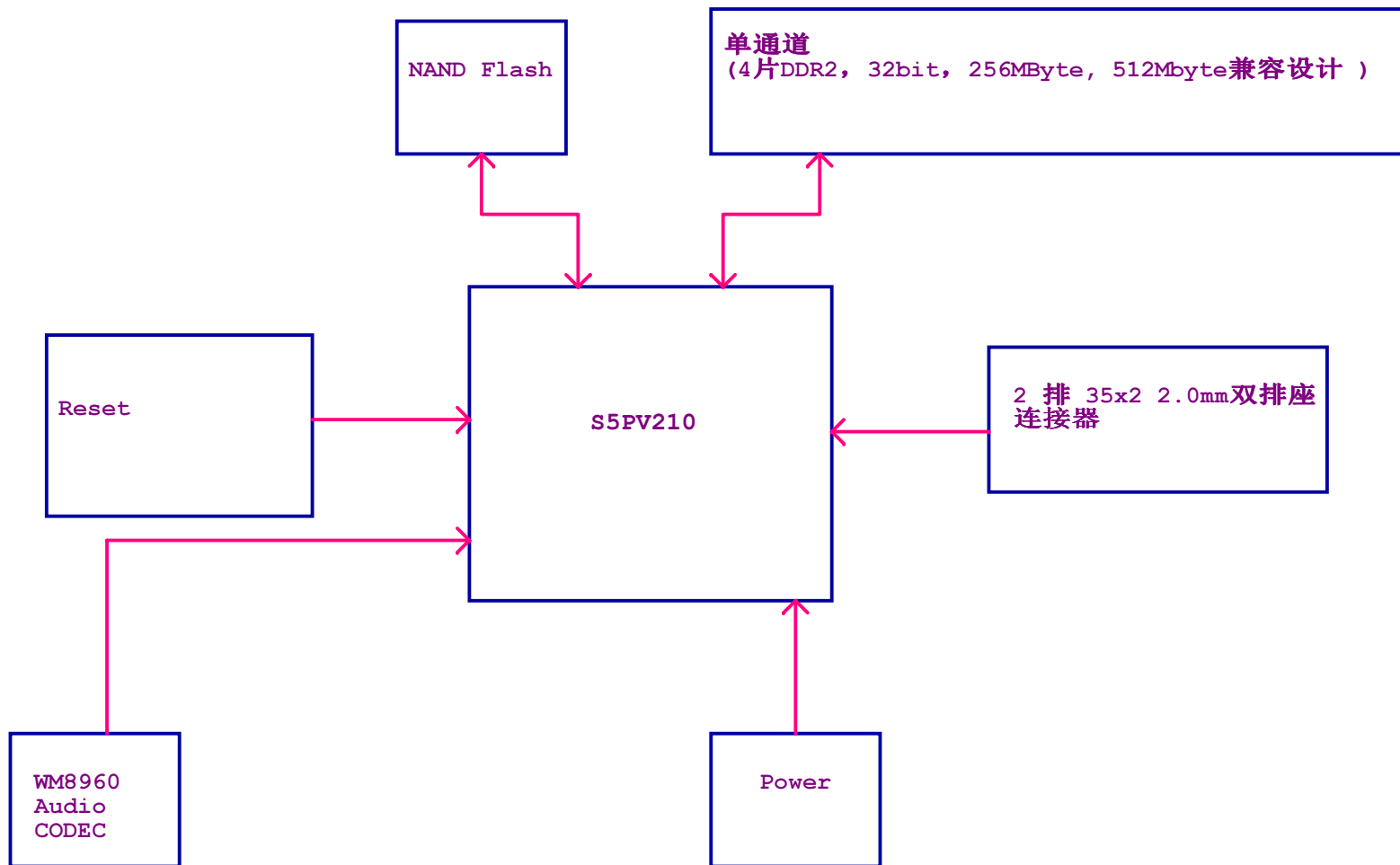
Revision History

Revision	Date	Comments
1.0	2010-12-9	初次版本

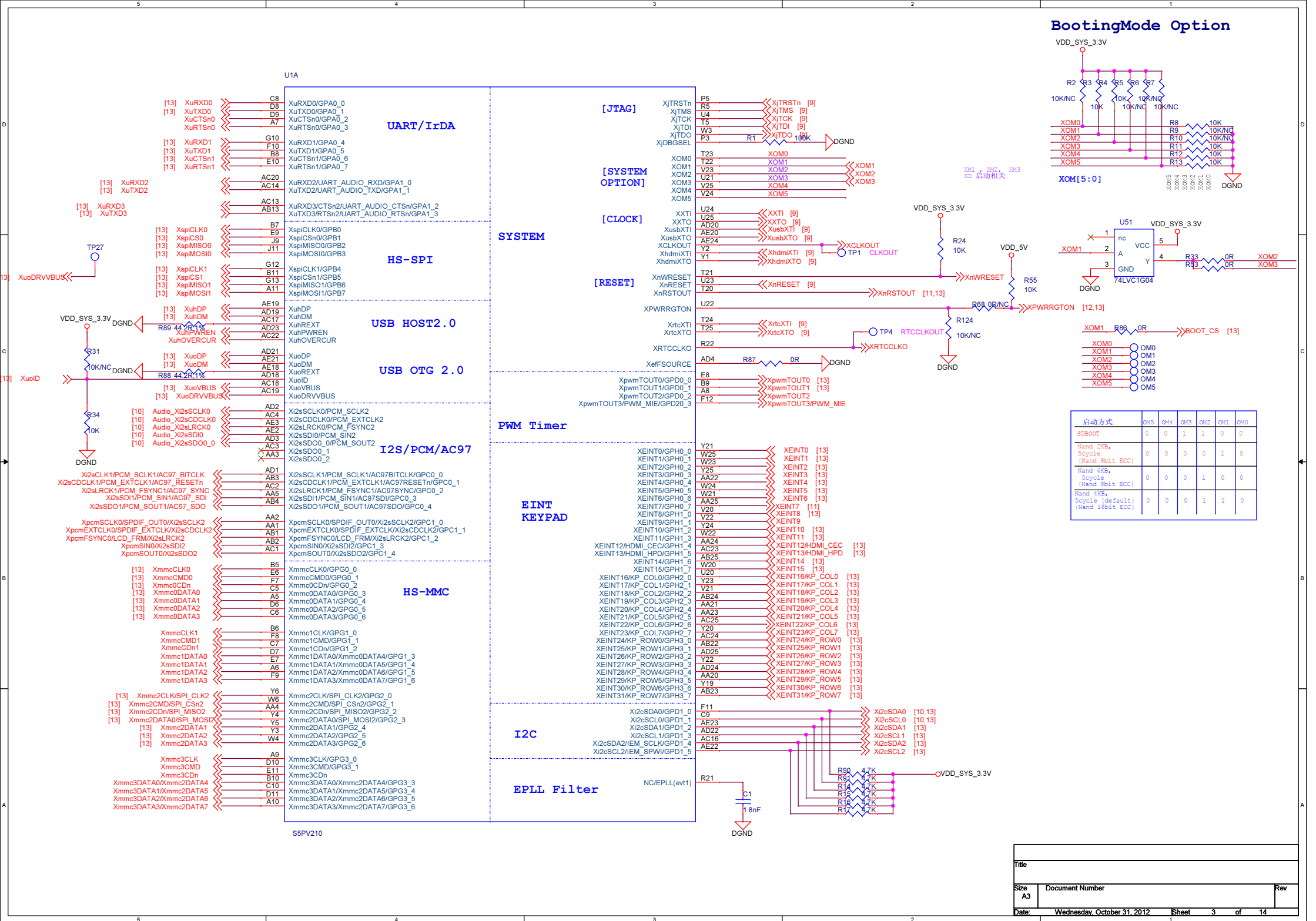
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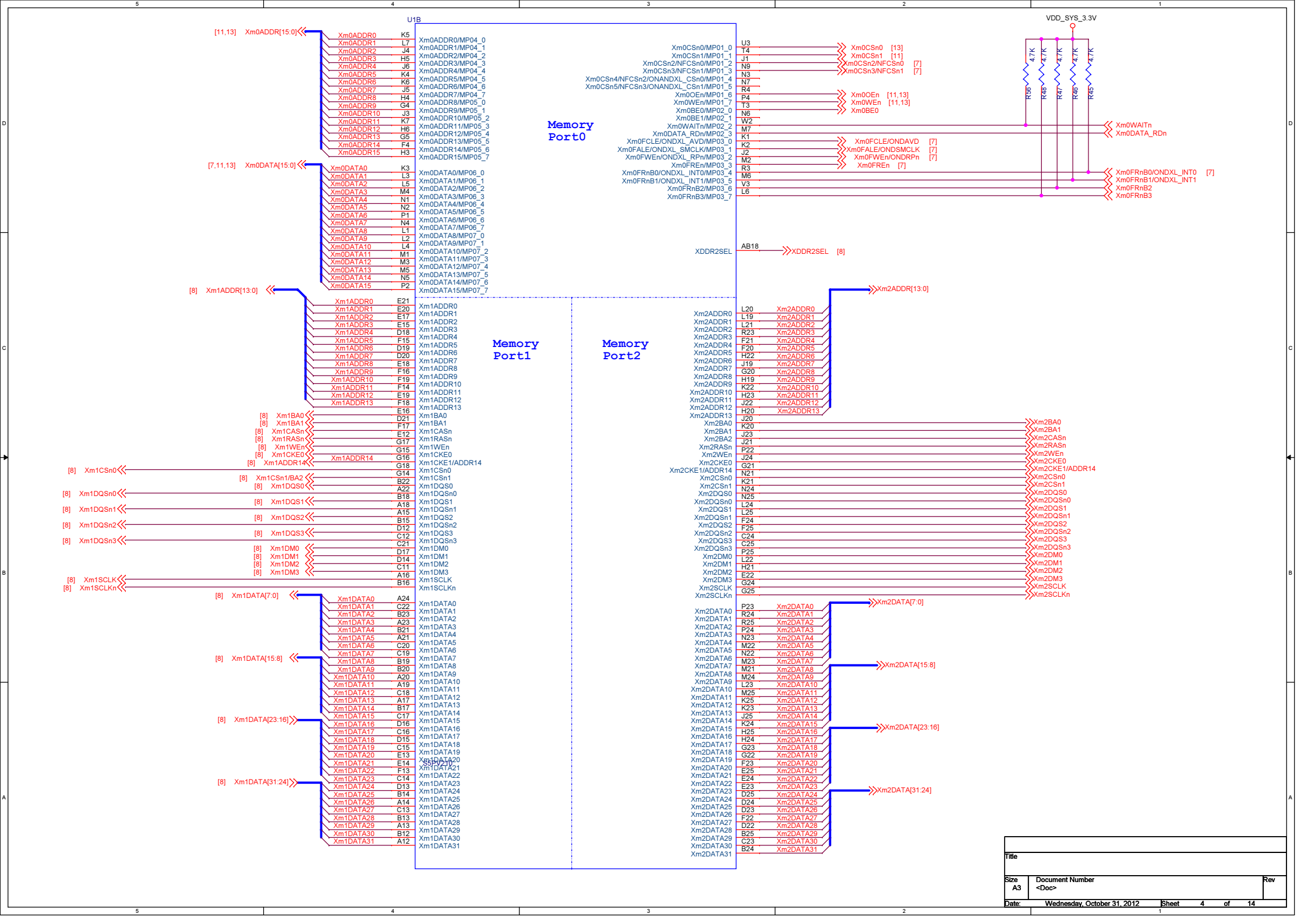
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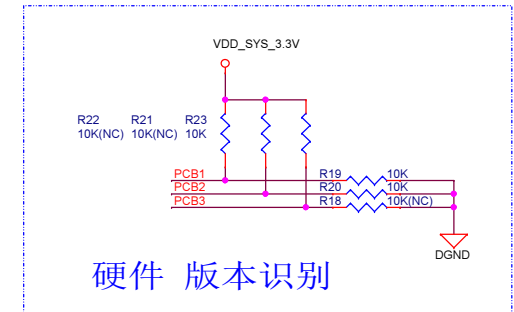
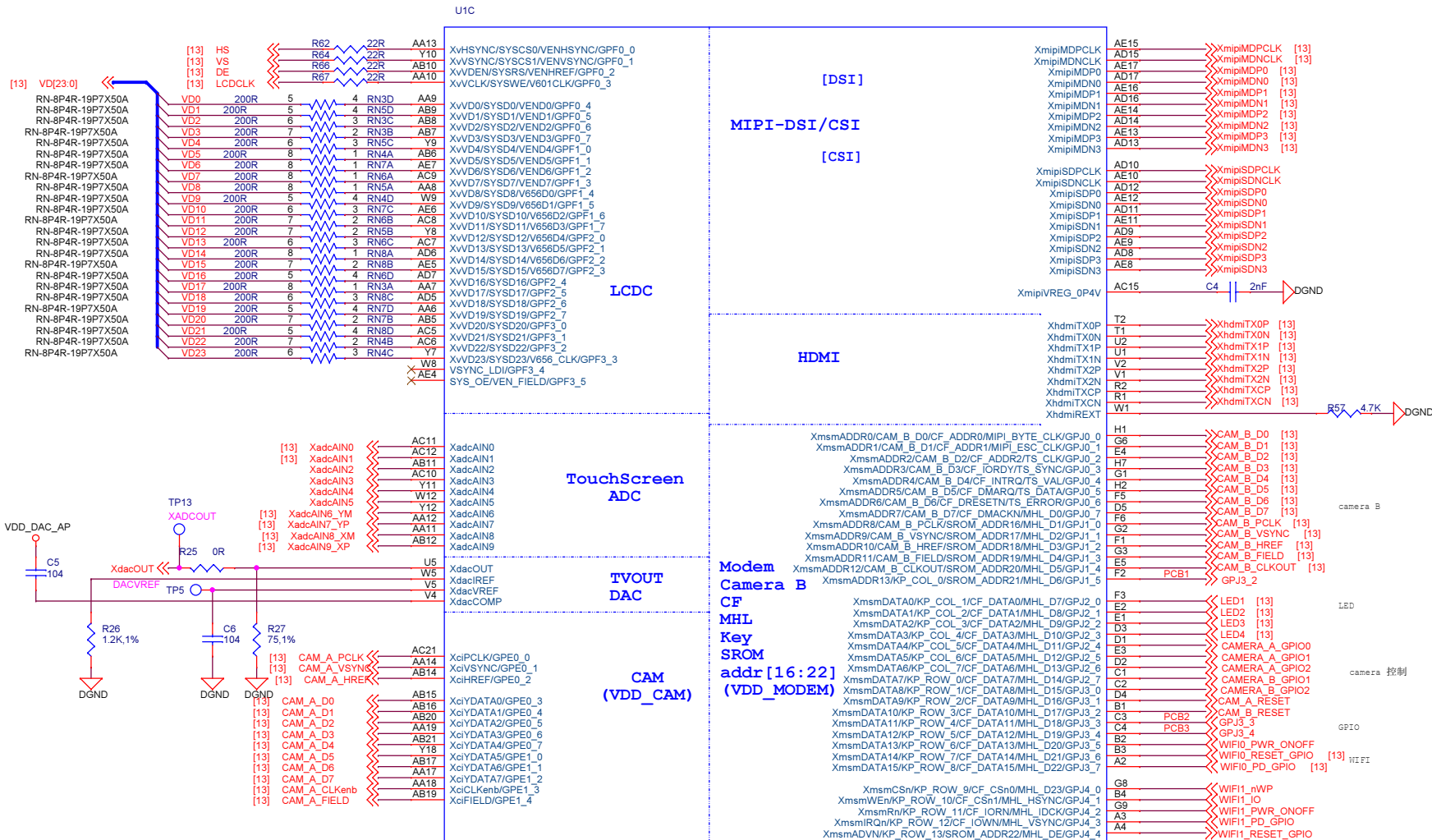
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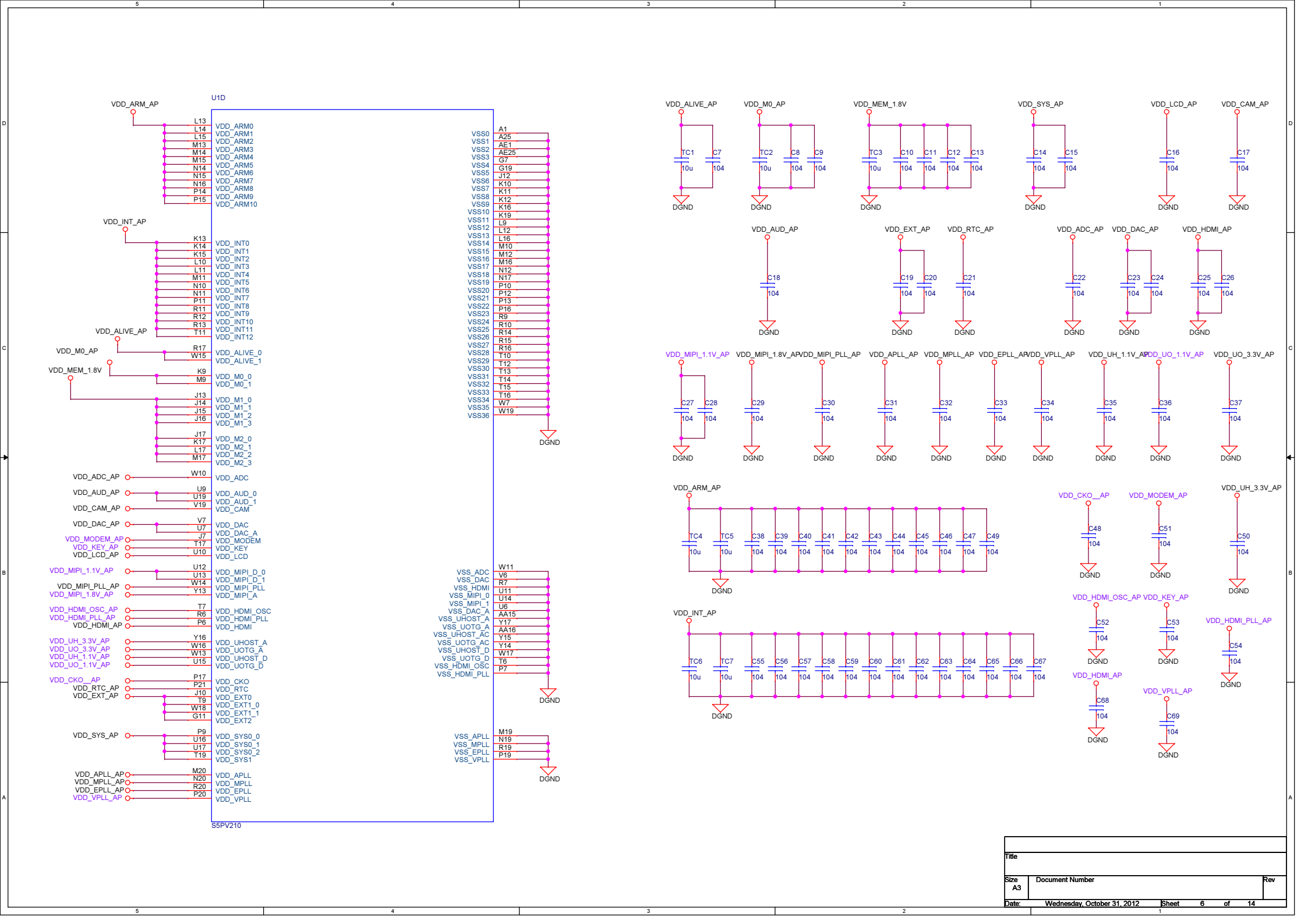
核心板面积大约75mmx75mm
周边4 排 50x2 2.0mm双排座连接器



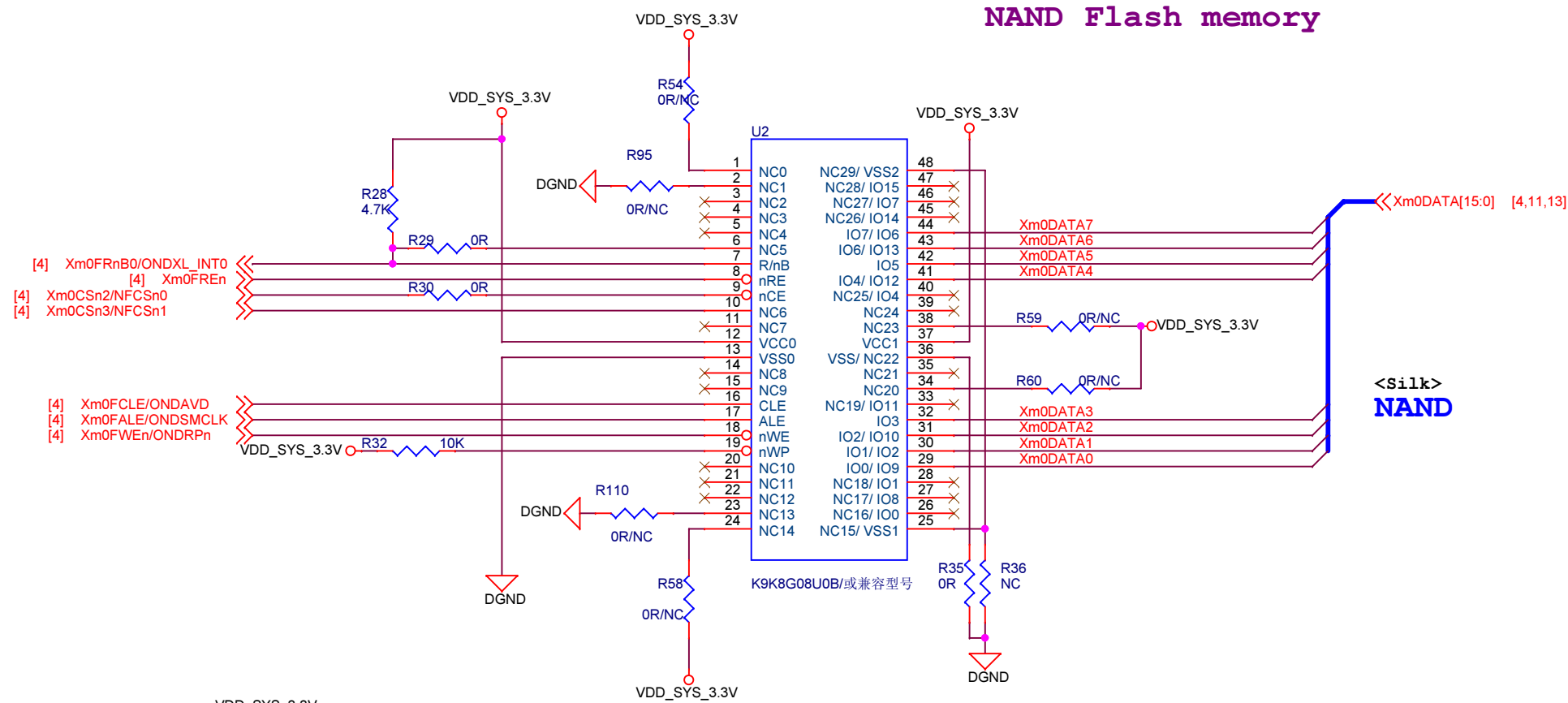




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NAND Flash memory



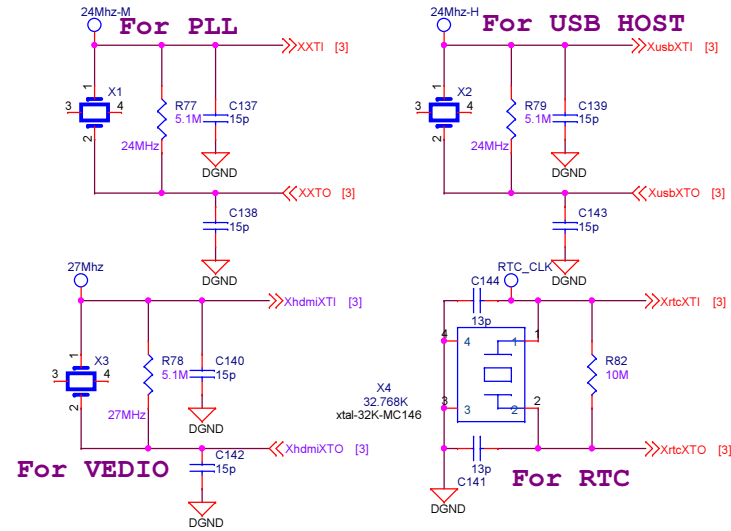
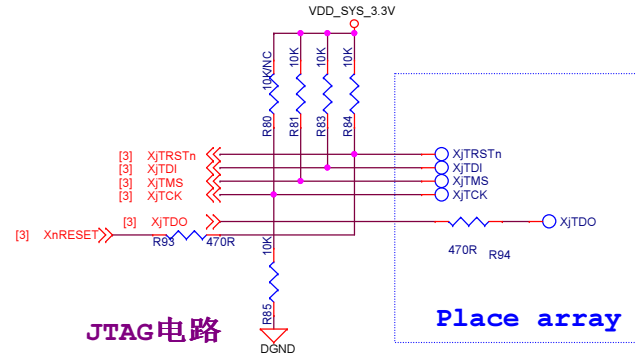
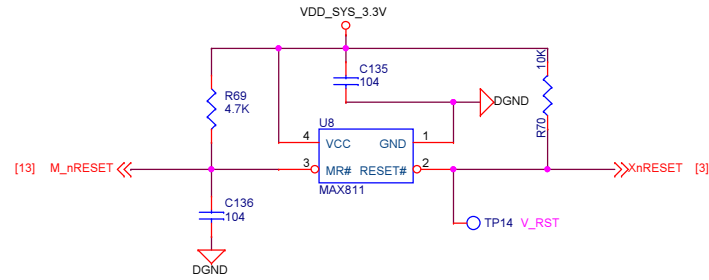
<Silk>
NAND

注：具体Flash容量及型号以客户实际订货为准

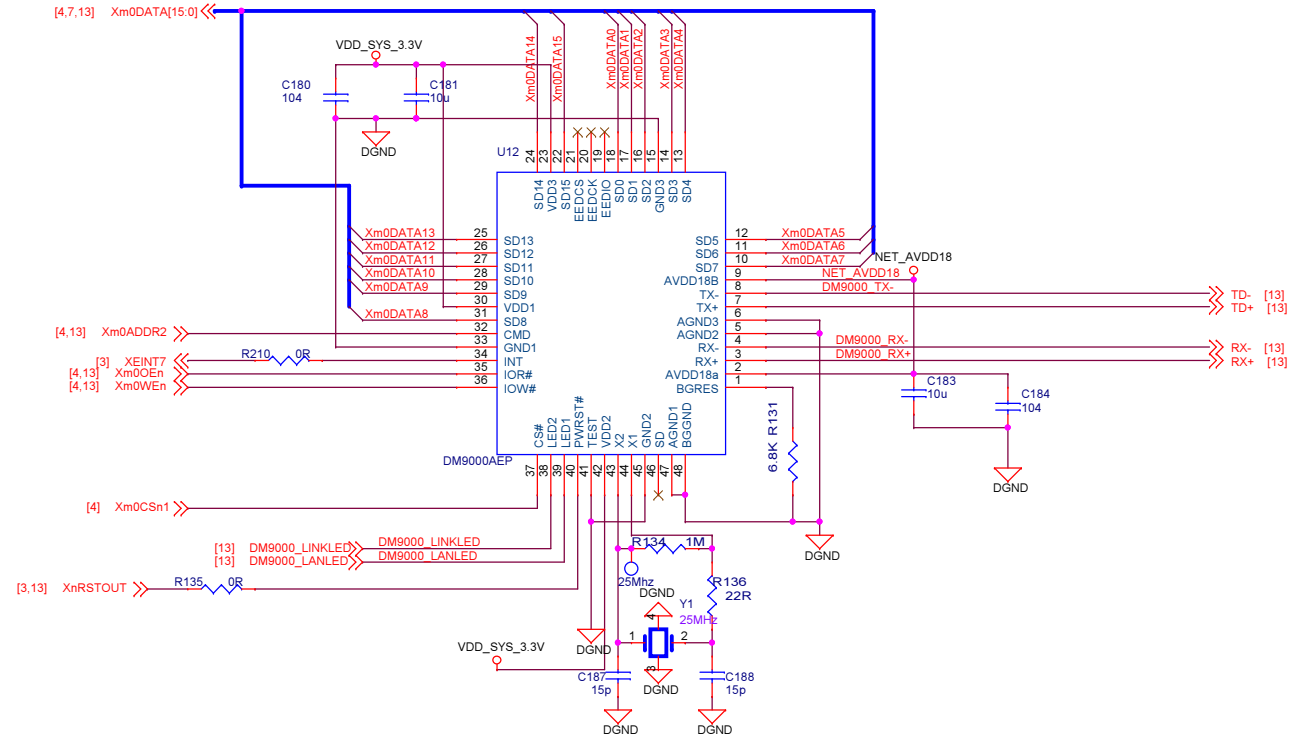
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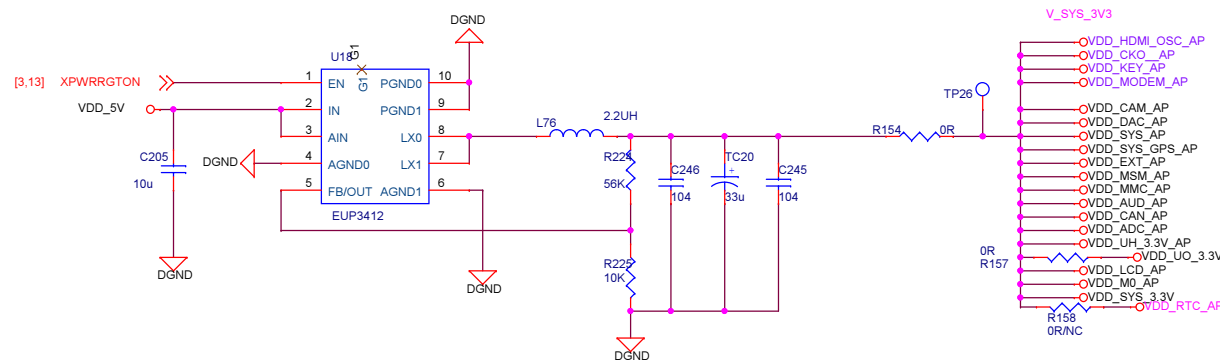
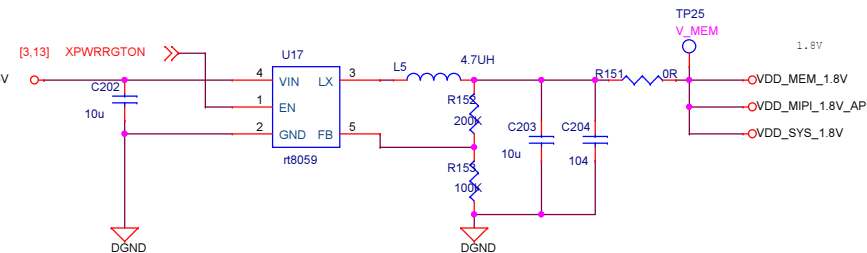
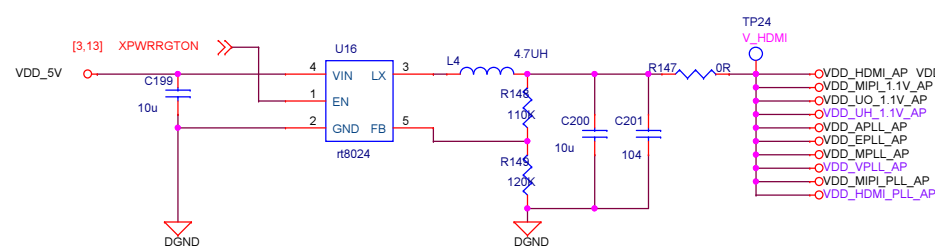
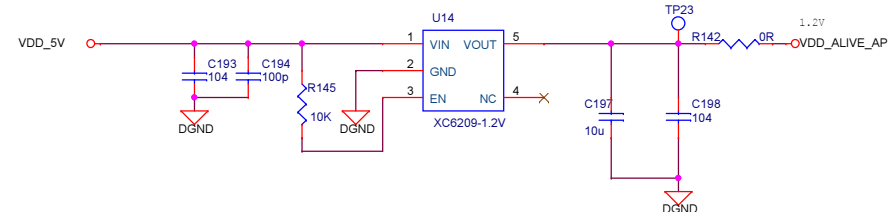
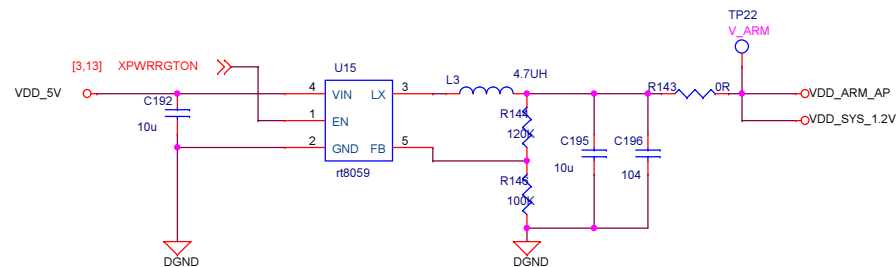
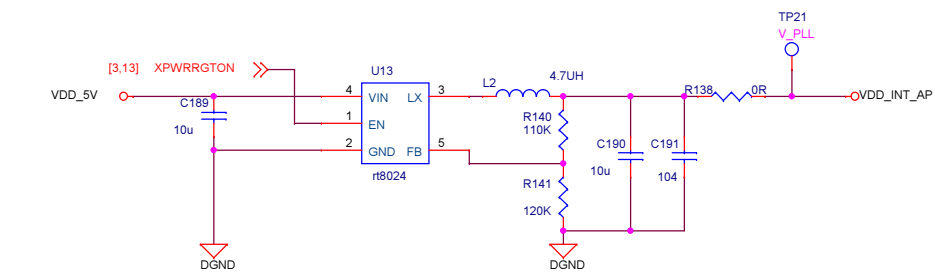
复位电路



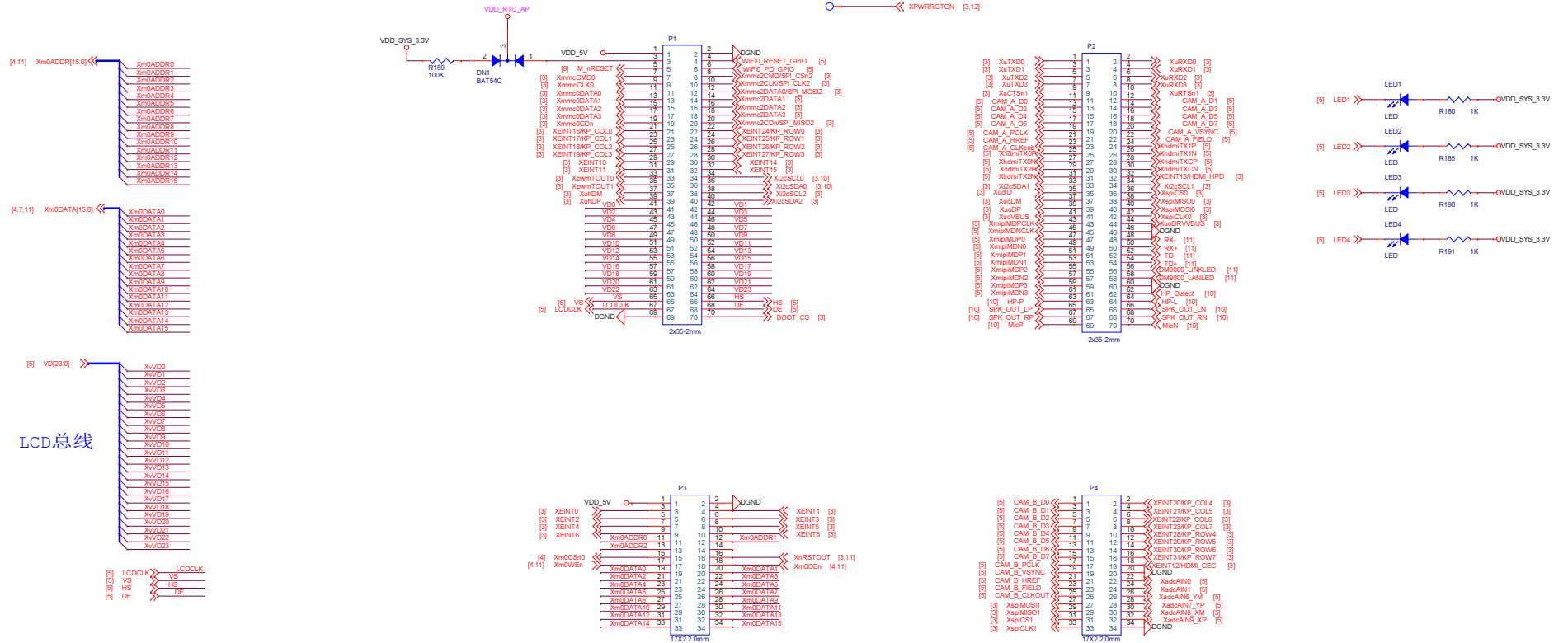
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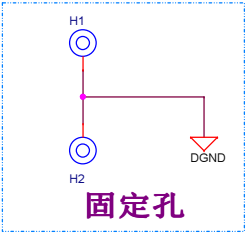
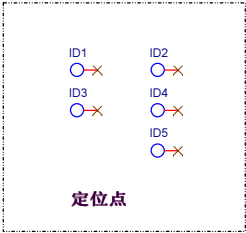


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