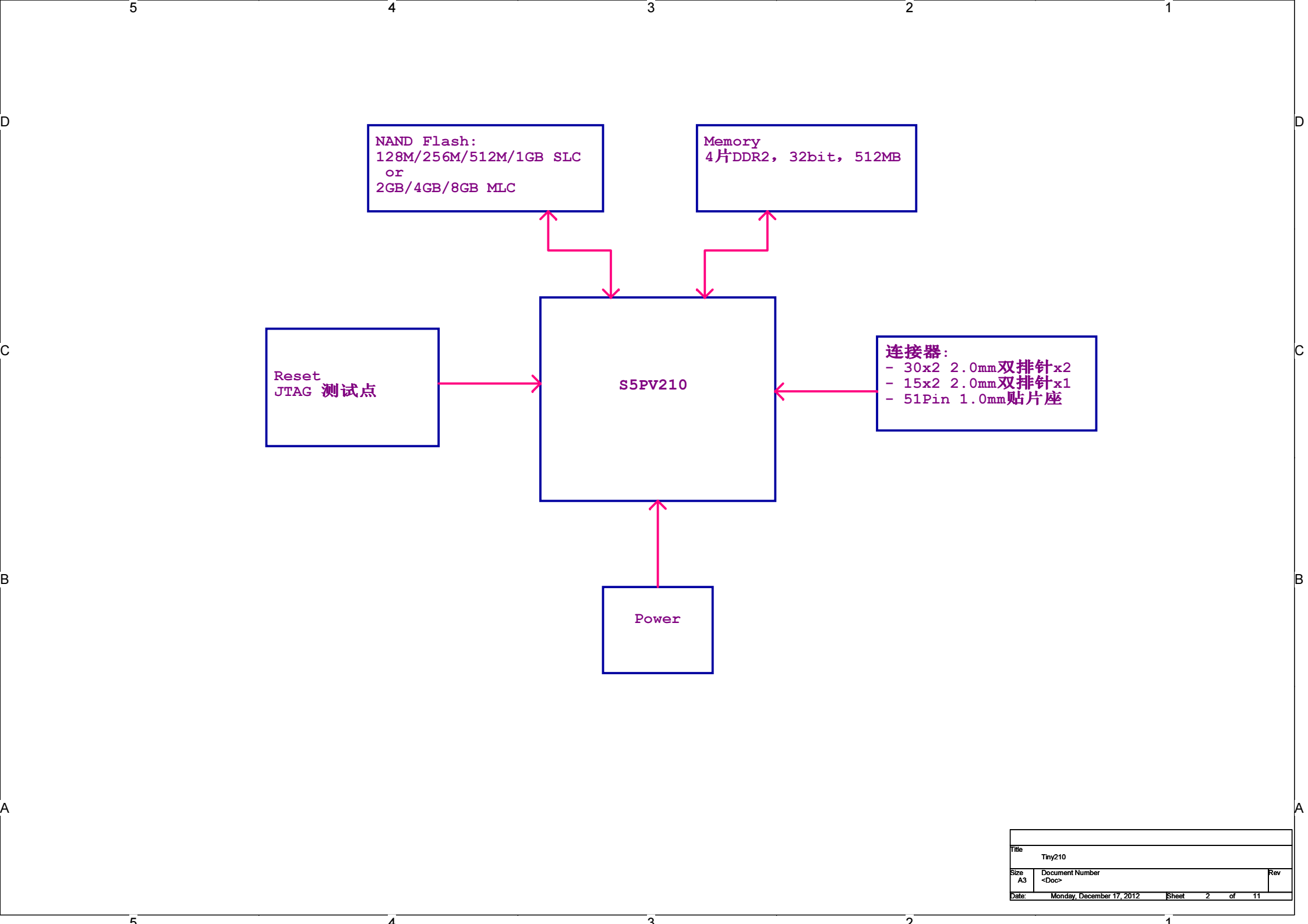


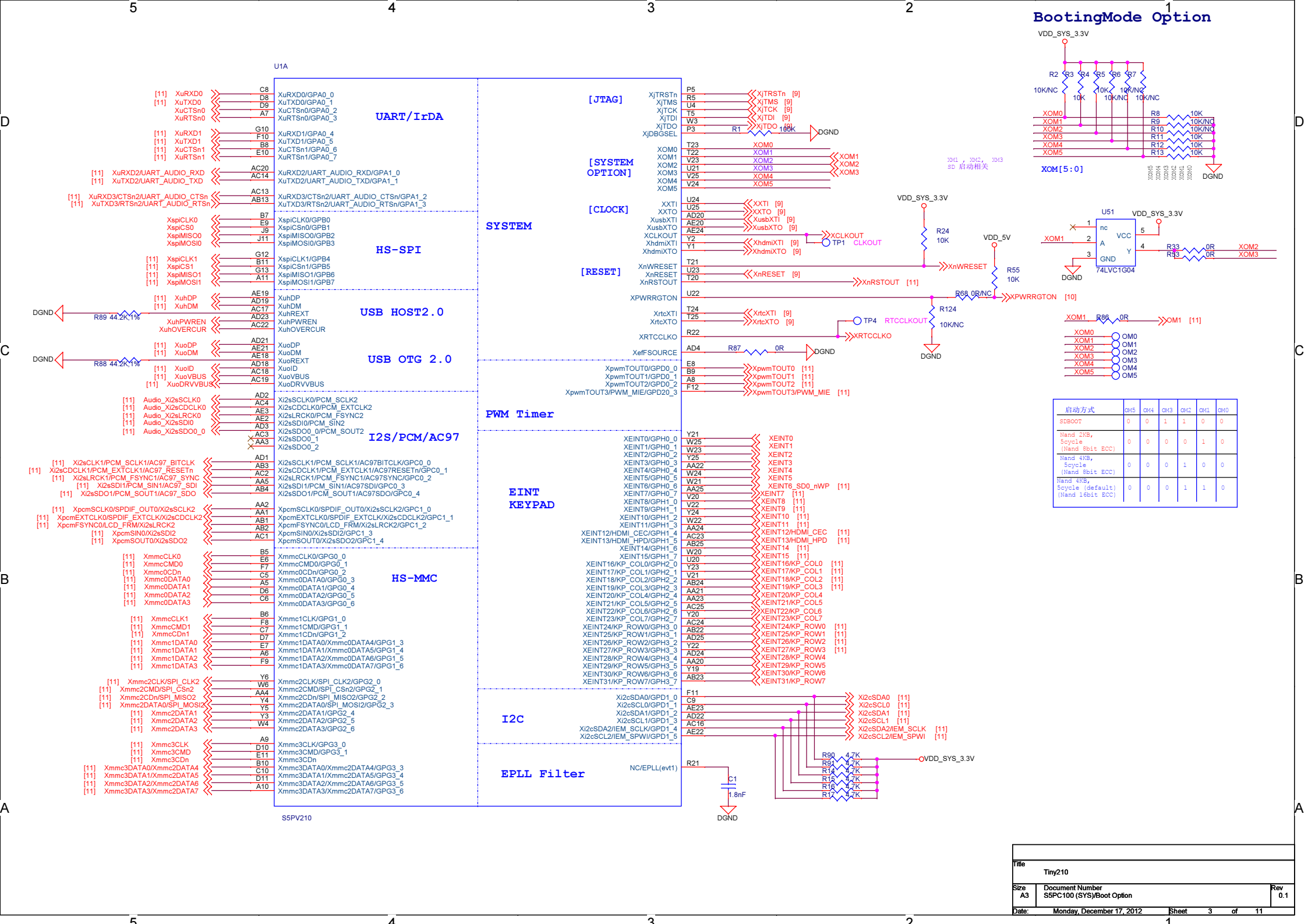
Revision History

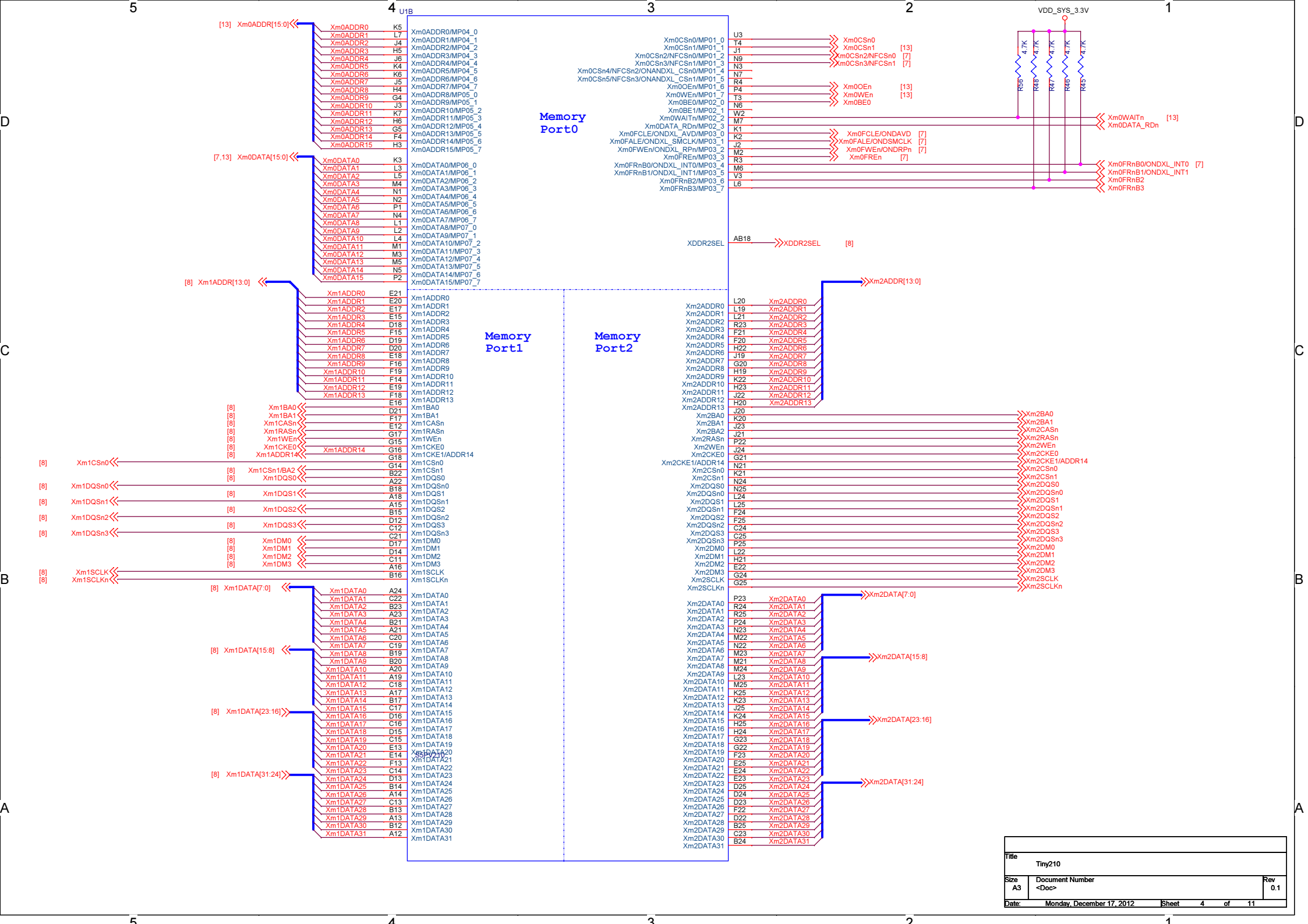
Revision	Date	Comments
1.0	2011-12-9	初次版本

Index

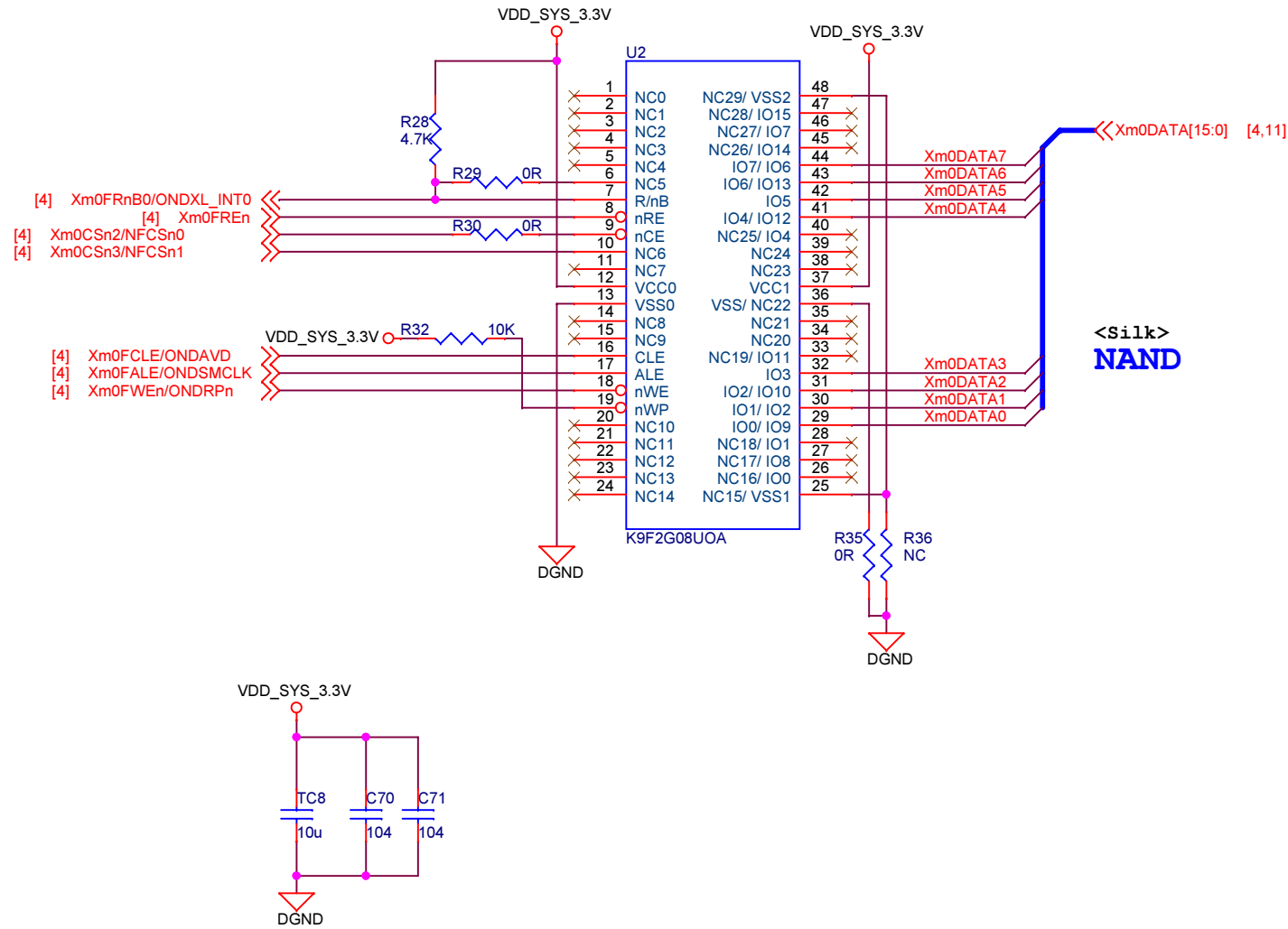
01:Revision History and Index	1
02:Function Block Diagram	2
03:S5PV210 (SYS)/Boot Option	3
04:S5PV210 (Memory)	4
05:S5PV210 (Media)	5
06:S5PV210 (Power)	6
07:NAND/NOR Flash	7
08:Memory (DDR2)	8
09:Reset/JTAG/SD/Clock	9
10: LCD IF	10
11:system power	11
12:board connector	12
13:ID Mark/Fixed Hold	13





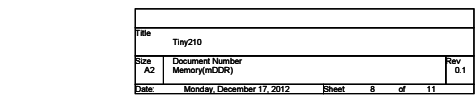
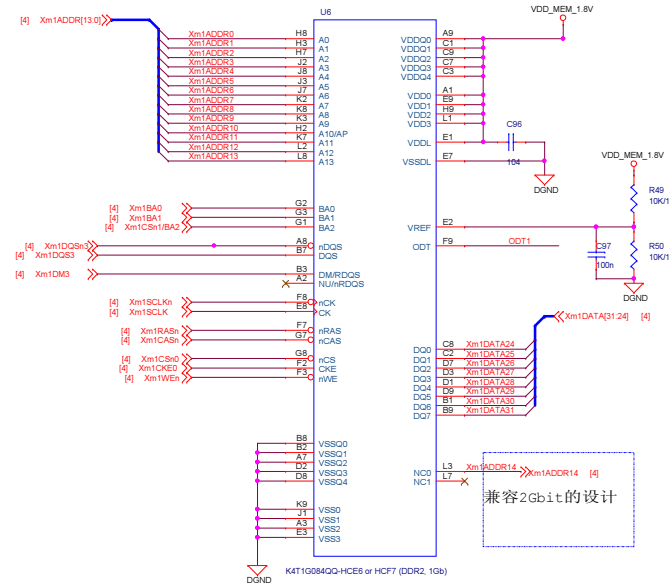
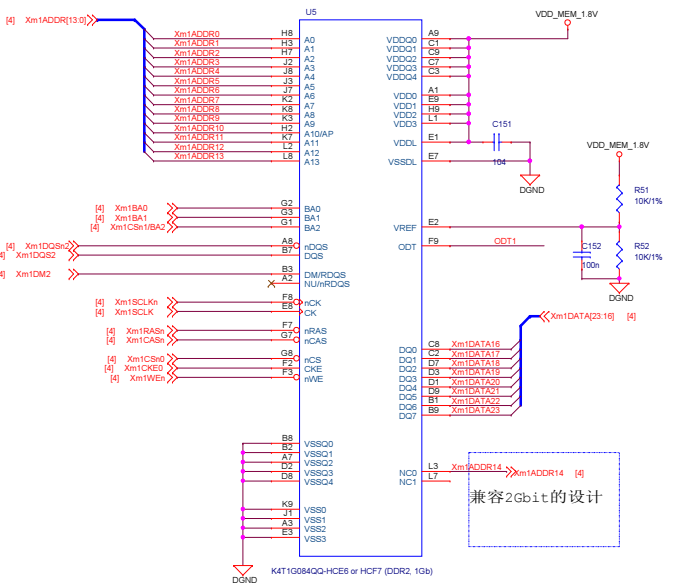
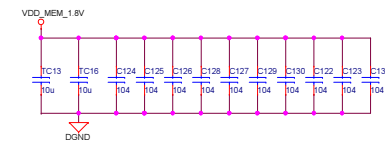
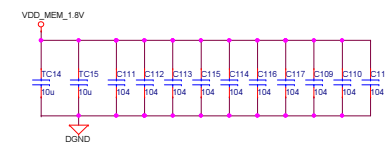
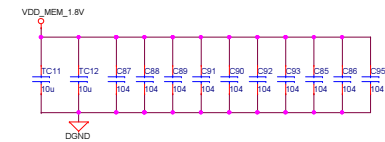
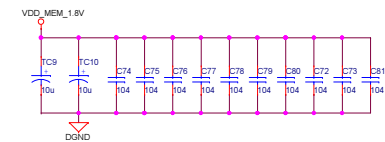
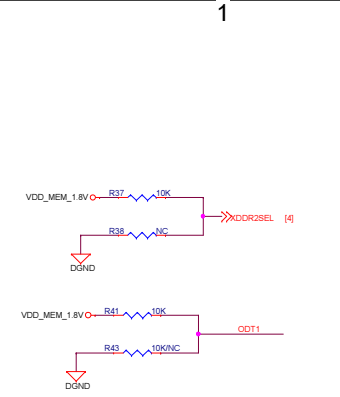
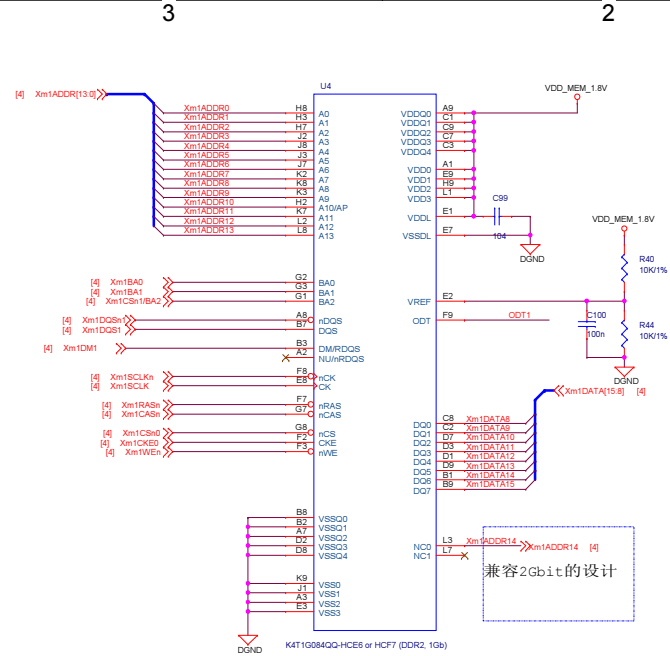
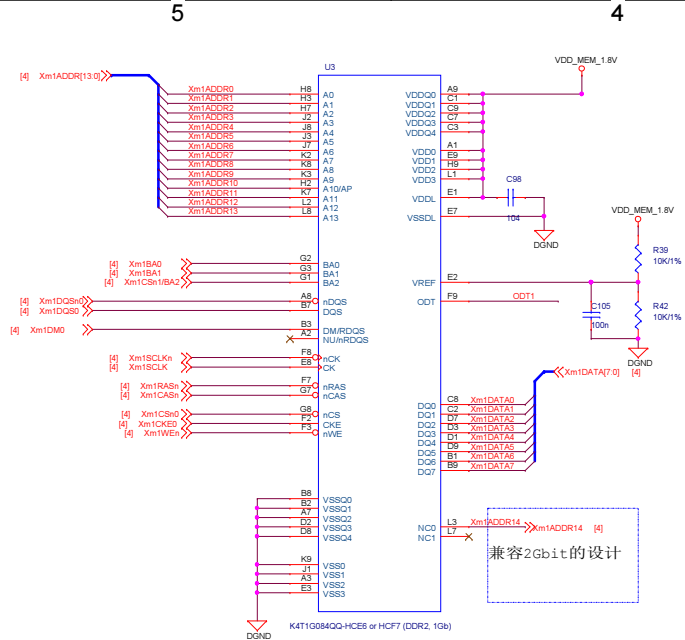


NAND Flash memory

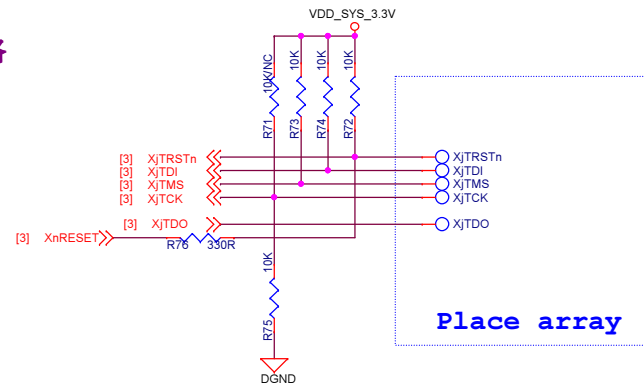


<Silk>
NAND

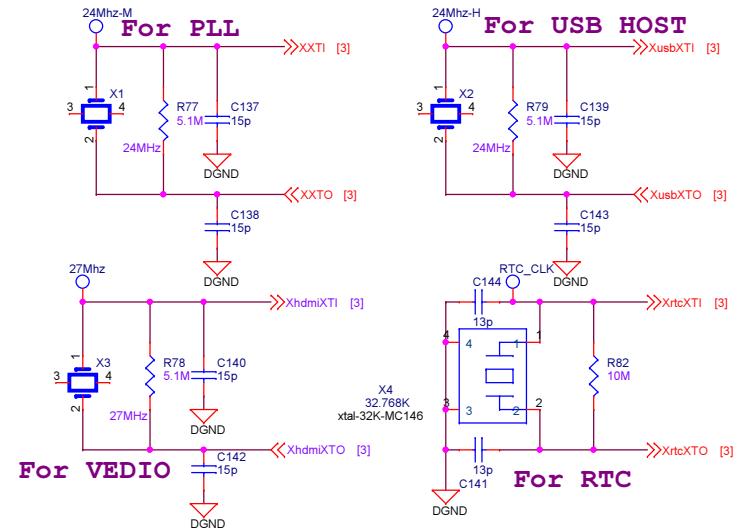
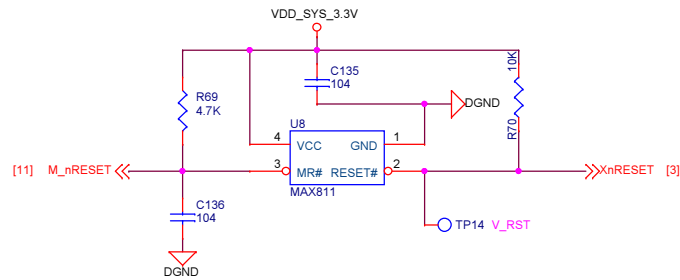
Title		
Tiny210		
Size	Document Number	Rev
A4	<Doc>	0.1
Date:	Monday, December 17, 2012	Sheet 7 of 11

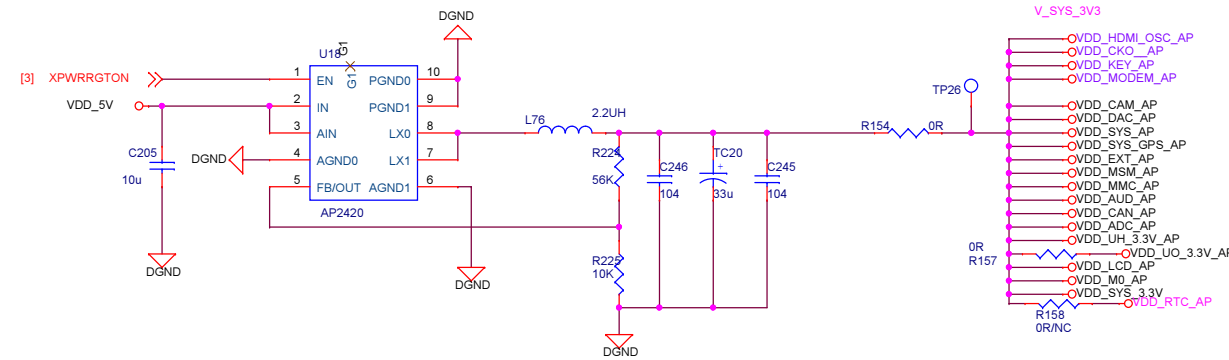
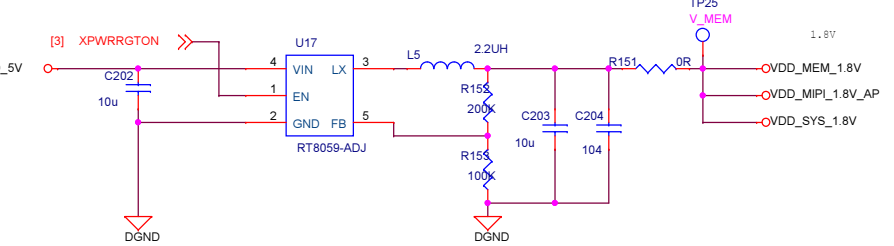
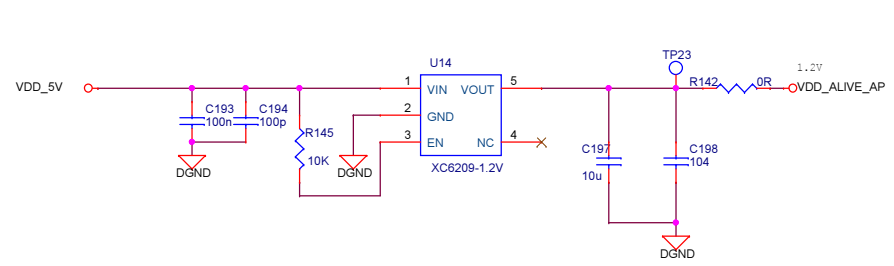
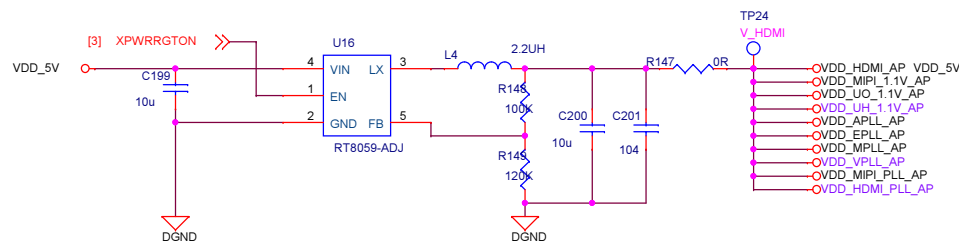
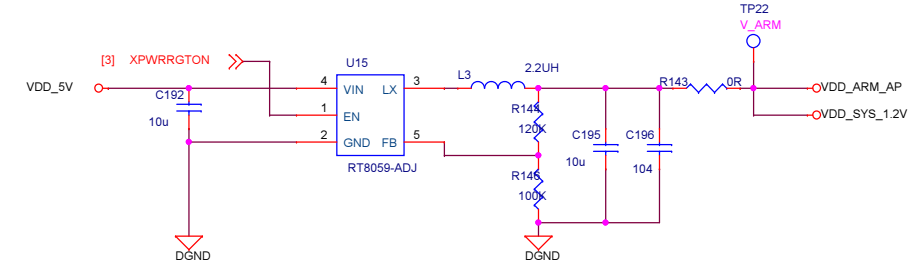
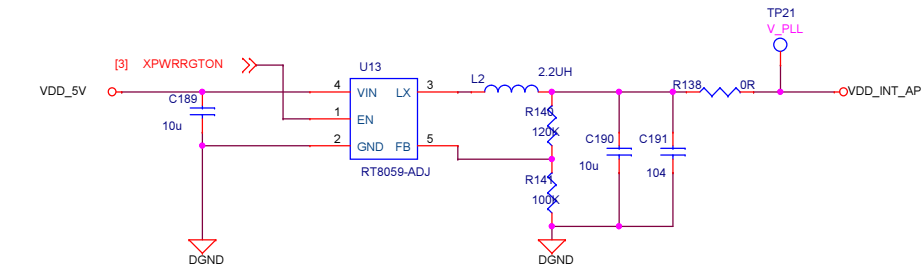


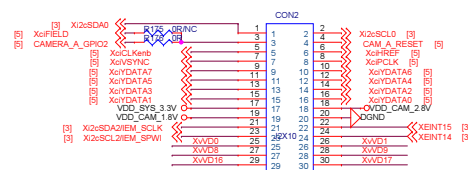
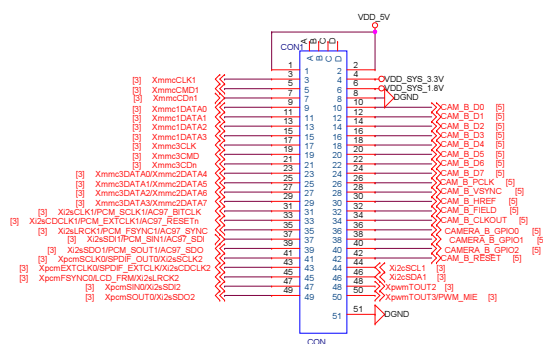
JTAG电路



复位电路

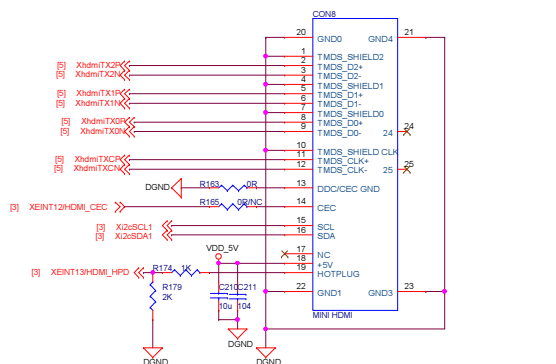






改为贴片座，也是2.0mm间距的IDC连接器

The schematic diagram illustrates the LED driver circuit for the LED module. It consists of four parallel branches, each containing an LED (LED1, LED2, LED3, LED4) in series with a resistor (R180, R185, R190, R191) connected to a 3.3V supply (VDDO_SYS_3.3V). The resistors are labeled with values 1K.



MINI HDMI接口电路

