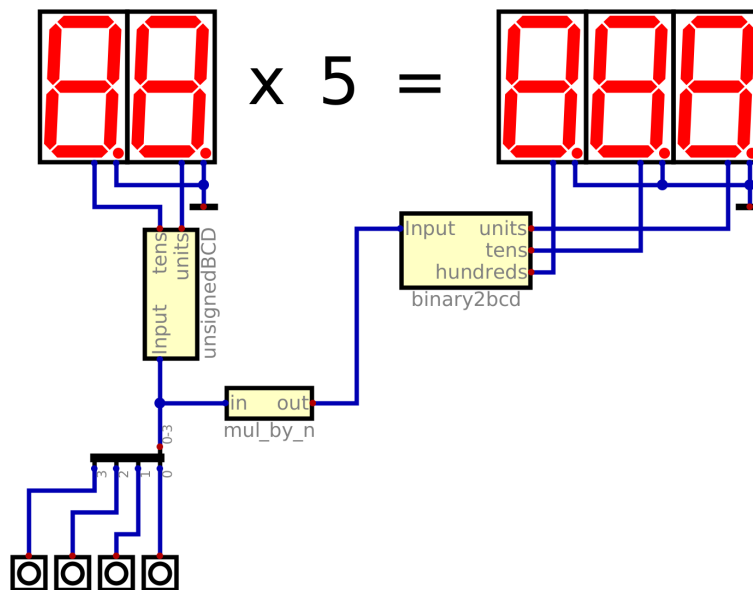You are provided with a template circuit for the design and implementation of a circuit that multiplies its 4-bit input by 5.

You are asked to design the *mul_by_n* block, which takes the 4-bit number as input, and generates the multiplication result in 8-bits, by only using an *adder* of appropriate bit size. You are not allowed to use the built-in multiplier block, just use an adder.

Upload *mul_by_n.dig* to SUcourse at the end of the session.

□