## Homework #1

## ENGR 3613 Microprocessors and Lab Spring 2023

Chapter 1: Problems: E1.4, E1.5 and E 1.9

Due Date:

February 1, 2023

Micro Hw#I **E1.4** How many bits can the HCS12 CPU manipulate in one operation?

I+ can handle 16 Bits during one operation.

E1.5 How many different memory locations can the HCS12 access without the expanded memory?

**E1.9** Convert 5K, 8K, and 13K to decimal representation.

) k = 2'0 = 1024

$$5k = 5.2'' = 5,120$$
  
 $8k = 8.2'' = 8,192$ 

13K = 13.210 = 13,312