Compiler Design _ HW 1 2

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- $1:S \rightarrow E$ \$
- $2: E \rightarrow E+T$
- $3: E \rightarrow T$
- $4: T \rightarrow T*P$
- $5: T \rightarrow P$
- 6: P → ID
- 7: $P \rightarrow (E)$

State 0

$$S \to \bullet E \ , \{\lambda\}$$
 $E \to \bullet E + T , \{\$+\}$
 $E \to \bullet T , \{\$+\}$
 $T \to \bullet T * P , \{\$+*\}$
 $T \to \bullet P , \{\$+*\}$
 $P \to \bullet id , \{\$+*\}$
 $P \to \bullet (E) , \{\$+*\}$

State 0 吃一個 '(' 生成state 6

State 6
$$P \rightarrow (\bullet E) , \{\$+*\}$$

點後的E是nonterminal,將E相關的production (2和3) 加入,lookahead為E後面的')'

State 6

$$P \rightarrow (\bullet E), \{\$+*\}$$
 $E \rightarrow \bullet E+T, \{\}$
 $E \rightarrow \bullet T, \{\}$

第二行點後的E是nonterminal,將E相關的production (2和3) 加入·lookahead 為E後面的 '+'所以進行合併

State 6

$$P \rightarrow (\bullet E), \{\$+*\}$$
 $E \rightarrow \bullet E+T, \{\}+\}$
 $E \rightarrow \bullet T, \{\}+\}$

又第三行點後的T是nonterminal,將T相關的production (4和5) 加入

State 6
$$P \rightarrow (\bullet E), \{\$+*\}$$

$$E \rightarrow \bullet E+T, \{\}+\}$$

$$E \rightarrow \bullet T, \{\}+\}$$

$$T \rightarrow \bullet T*P, \{\}+\}$$

$$T \rightarrow \bullet P, \{\}+\}$$

又第四行點後的T是nonterminal,將T相關的production (4和5) 加入·lookahead 為T後面的 '*' 所以進行合併

State 6

$$P \to (\bullet E), \{\$+*\}$$
 $E \to \bullet E+T, \{\}+\}$
 $E \to \bullet T, \{\}+\}$
 $T \to \bullet T*P, \{\}+*\}$
 $T \to \bullet P, \{\}+*\}$

又第五行點後的P是nonterminal,將P相關的production (6和7) 加入

State 6

$$P \rightarrow (\bullet E), \{\$+*\}$$
 $E \rightarrow \bullet E+T, \{\}+\}$
 $E \rightarrow \bullet T, \{\}+\}$
 $T \rightarrow \bullet T*P, \{\}+*\}$
 $T \rightarrow \bullet P, \{\}+*\}$
 $P \rightarrow \bullet id, \{\}+*\}$
 $P \rightarrow \bullet (E), \{\}+*\}$

至此便因點後所接的皆為terminal symbol便結束