

Date: July 09, 1984

This package consists of the following schematics for the Model II/12/16/16B:

DRAWING NUMBER	DESCRIPTION	NUMBER OF PAGES
8000042	F D C - Early	2
8000043	VDG/Keyboard	2
8000049	CRT Motorola 22 MHz	1
8000050	64K Memory	1
8000051	CPU Module	3
8000116	FDC - Late	2
8000117	Hires Graphics	2
8000122	HD Controller 8 Meg	2
8000125	HD Interface 8 Meg	2
8000127	M16 Memory (68000)	2
8000133	M16 CPU (68000)	3
8000136	8" TPI Logic	3
8000143	HD Interface 12 Meg	2
8000147	Arcnet Active Hub	1
8000150	144K Memory	1
8000166	M12 Main Logic	6
8000176	M12 VDG	1
8000178	Arcnet Interface	4
8000181	M16 CPU (68000)	3
8000184	CRT RCA 22 MHz	1
8000190	M12 TCE Monitor	1
8000193	8" TM848 Thinline	5
8000196	M12 Keytronics Keyboard	1
8000198	Multiterminal Board	1
8000202	HD Interface WDIOIOL	3
031095	HD Controller WD1000	4
DT-4	CRT Zenith 22 MHz	1
210196/210771	8" TM848-2E Thinline	3
25100-2	8" Shugart SA800 Discrete	2
50593-0/50596-0	8" Shugart SA800 LSI	4
	8" CDC Discrete	6
	8" CDC LSI	2
0120-0287	Cherry Keyboard	1