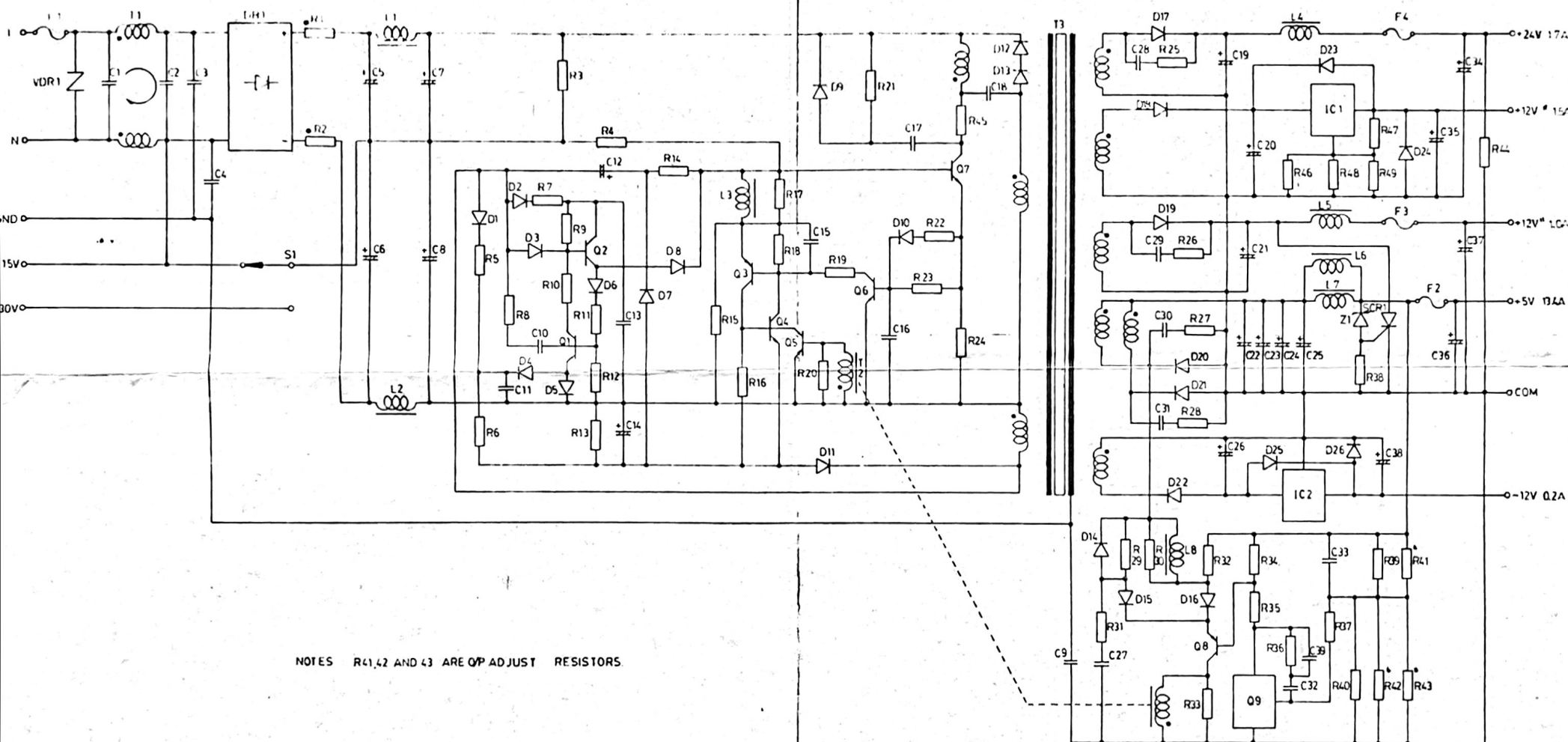


REV	ISSUE	ZONE	REVISIONS	DATE	CHK	APPD
			RELEASE DRAWING			

Mod 16  
Switching P/S

	R1,2				R5,6,8,7,3,9,10,4,11,12,13				R14	R15,16		R17,18	R19,20,21		R22,23,24,45		R31 R29,25,26,27,28,30,32,33,34,35,46,36,48,37,38,40,47,49,39,42,41,43,44									
C1	C2	C3	C4	C5,6		C7,8	C11	C10	C12,C13,14				C15	C16,17		C18	C9	C27	C28,29,30,31		C19,26,20,21,22,23,24,25,32,39,33,38		C35,36,34,37			
L1	L1,2								L3		L2				L3		L8				L4,5,6,7					
					D1	D2,3,4,5,6,1,2,10,6		D7	D8	D3	D4,5	D9,11	D6	D10	D7	D12,13		D14	D15	D17,18,19,20,21,22,16		D8,9	D25,23	D26	D24	
IC1	IC2				IC1				Z1		SCR1		F4		F3		F2									



NOTES R41,42 AND 43 ARE QP ADJUST RESISTORS

PRODUCT  
ENGINEERING

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TOLERANCES  
WHOLE NO  
±  
DECIMAL  
±  
XX ±

DO NOT SCALE DRAWING  
DATE  
CHK  
DATE  
APPD  
DATE

TITLE  
CIRCUIT DIAGRAM

FIRST USED ON  
AA11082

PART NO  
FINISH

ASTEC COMPONENT LTD.

HONG KONG

SH 1 OF 1 A2