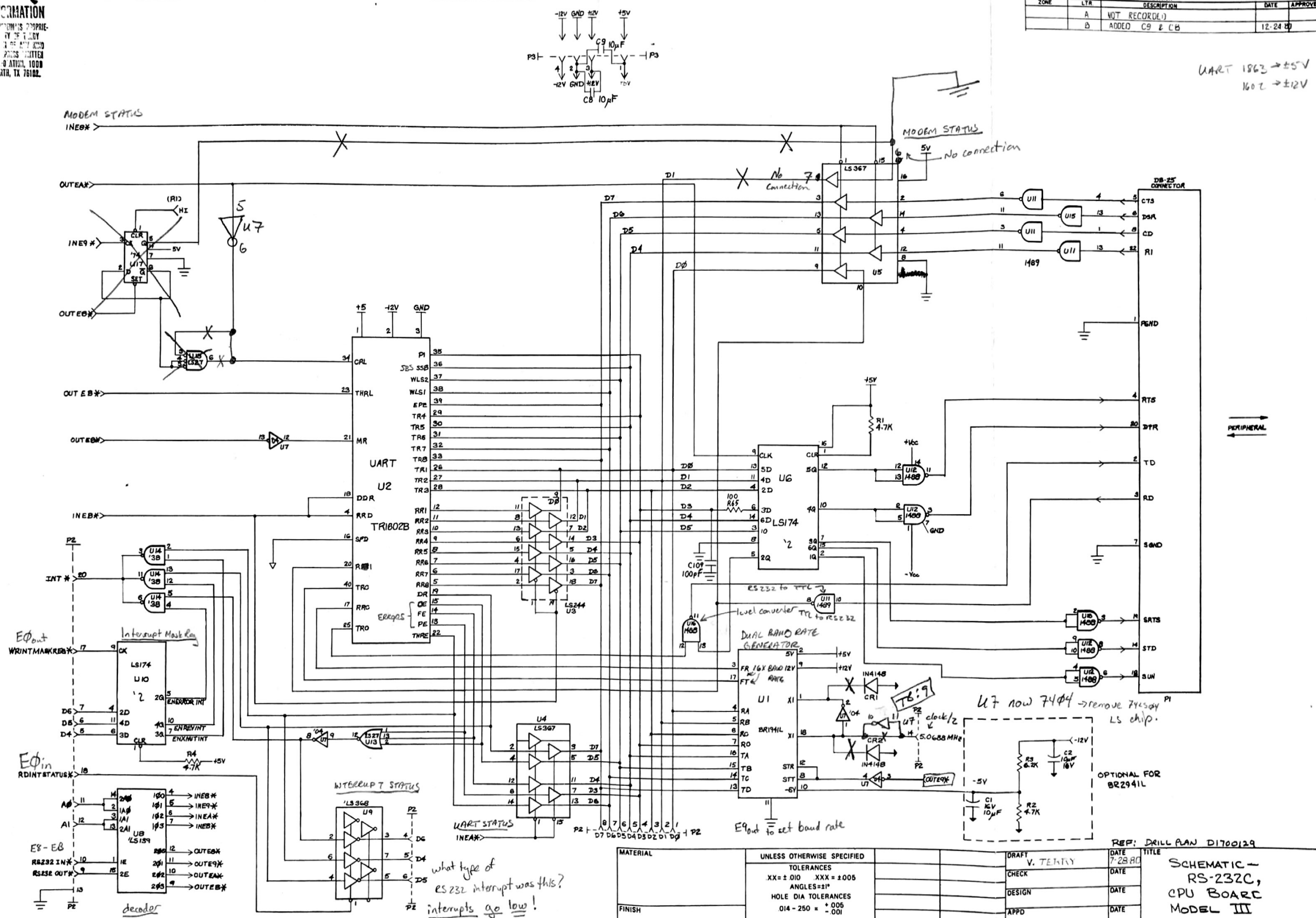


PROPRIETARY INFORMATION
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ZONE		REVISION		DATE	APPROVE
		DESCRIPTION			
	A	NOT RECORDED			
	B	ADDED C9 & C8		12-24-80	

UART 1863 $\rightarrow \pm 5V$
1602 $\rightarrow \pm 12V$



MATERIAL	UNLESS OTHERWISE SPECIFIED			DRAFT	DATE	TITLE SCHEMATIC - RS-232C, CPU BOARD MODEL III	
	TOLERANCES			V. TERRY	7-28-80		
FINISH	XX = ± .010 XXX = ± .005			CHECK	DATE		
	ANGLES ± 1°			DESIGN	DATE		
	HOLE DIA TOLERANCES			APPD	DATE		
	014 - .250 = + .005 - .001			APPD	DATE		
	251 - .750 = + .008 - .001						
	751 - UP = + .015 - .001						
	DIMENSIONS ARE IN INCHES AND APPLY AFTER PLATING			<div>tandy</div>		DWG. NO. 8000076	SIZE D
DO NOT SCALE THIS DRAWING	NEXT ASSY.	USED ON	SCALE NONE			SHEET 1	OF 1