



REFERENCE DESIGNATION	LAST USED	NOT USED
C40		C2B (RESERVED)
CR14		CR10
L9		
R8		
R74		
TP27		
VRI		

- NOTES, UNLESS OTHERWISE SPECIFIED:
1. ALL RESISTORS ARE IN OHMS, 1/4W, 5%.
 2. ALL CAPACITORS ARE IN MICROFARADS, 50V, 80, 10%.
 3. ALL INDUCTORS ARE 100MH, 10%.
 4. ALL DIODES ARE IN4148.
 5. ALL TRANSISTORS ARE 2N2907A.
 6. -D- INDICATES CUT-TRACE OPTION.
 7. -D- INDICATES JUMPER OPTION.
 8. CONDUCTOR SYMBOL REFERENCES:
(X)=J1, (V)=J2, (W)=J3, (Y)=J5, (Z)=J6.
I.C. LOCATION 4B IS A RESISTOR PACK, 150K, 1/4W, 5%.
 9. I.C. LOCATION 3B IS A RESISTOR PACK, 2K, 1/4W, 5%.
 10. ADD JUMPER L FOR -7V TO 16V ON ASSEMBLIES;
25103, 25105, 25107.
 11. PINS 8 AND 9 OF 14 ARE GROUND.
 12. SHORTING PLUG INSTALLED.
 13. REMOVE JUMPER FROM 800 AND INSTALL IN 801.

TYPE	POSITION	UNUSED ELEMENTS	VCC PIN	GND PIN	TYPE	POSITION	UNUSED ELEMENTS	VCC PIN	GND PIN
7400	5C, 2E, 5F	2E1, 4, 5F2, 4	14	7	74LS5	2B		16	8
7402	4E	4E3			75452P	1F		8	4
7404	3E				8T20	6C			
7405	4D	4D2, 3			9602	5A, 5E, 3G		16	8
7407	4G				LM339	1G	1G3	3	12
7410	5B, 2C	5B1			NES92A	6E			
743B	3C, 4C				2Q2222	6G			
7474	2D, 3D, 5D, 4F				2Q2905	5G			
7476	3F		5	13	742B	2F		14	7
7486	2G	2G4	14	7	7427	2A	2A1, 2	14	7

MUST CONFORM TO ENGINEERING SPEC. TS 10000-0		FC HISTORY		SHUGART ASSOCIATES	
MATERIAL		DATE	NO	TITLE	SCHEMATIC DIAGRAM
					DISKETTE, 800 SERIES
CASE DEPTH		DETAIL	4, 11, 3-75	DESIGN	T25100
HARDNESS		APPRO		SCALE	1 OF 2
SURFACE TREATMENT					

X5800

USED ON ASSEMBLY
25102-4
25103-4
25104-4
25105-4
25106-4
25107-4

