Proof from proofs/t102_relat_1__6_premises.tptp t2_boole t48_xboole_1 t100_relat_1 c_0_15 c_0_23 I32_xboole_1 c_0_20 d10_xboole_0 t36_xboole_1 c_0_25 c_0_27 t102_relat_1

FALSE