

# 800mA Low Dropout Voltage Regulator

## **FEATURES**

- Guaranteed 0.8A Output Current
- Guaranteed 1A Peak Current
- Three Terminal Adjustable or Fixed 1.5V, 1.8V, 2.5V, 2.85V, 3.0V, 3.3V and 5V
- Low Quiescent Current
- Low Dropout Voltage of 1.1V at 0.8A
- 0.1% Line and 0.2% Load Regulation
- Stable with 2.2uF Ceramic Capacitor
- Overcurrent and Thermal Protection
- Available Packages: SOT-223,TO-252, TO-220, and TO-263

#### **APPLICATIONS**

- Desktop PC's Servers
- SCSI-II Active Terminator
- Portable/ Palm Top / Notebook Computers
- Cordless Phones
- Battery Chargers
- Disk Drives
- Portable Consumer Equipment
- Portable Instrumentation
- SMPS Post-Regulator

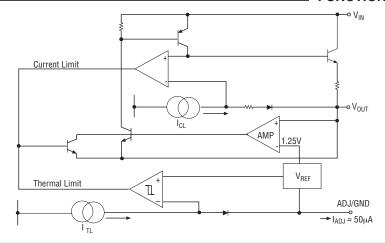
Now Available in Lead Free Packaging

#### **DESCRIPTION**

The SPX1117 is a low power positive-voltage regulator designed to satisfy moderate power requirements with a cost effective, small footprint solution. This device is an excellent choice for use in battery-powered applications and portable computers. The SPX1117 features very low quiescent current and a low dropout voltage of 1.1V at a full load. As output current decreases, quiescent current flows into the load, increasing efficiency. SPX1117 is available in adjustable or fixed 1.5V, 1.8V, 2.5V, 2.5V, 2.85V, 3.0V, 3.3V and 5V output voltages.

The SPX1117 is offered in several 3-pin surface mount packages: SOT-223, TO-252, TO-220 and TO-263. An output capacitor of  $10\mu F$  provides unconditional stability while a smaller  $2.2\mu F$  capacitor is sufficient for most applications.

#### **FUNCTIONAL DIAGRAM**



## **ABSOLUTE MAXIMUM RATINGS**

Power Dissipation	.Internally Limited
Lead Temperature (soldering, 5 seconds)	260°C
Storage Temperature Range	65°C to +150°C
Operating Junction Temperature Range	40°C to +125°C
Input Supply Voltage	+20V
Input to Output Voltage	18.8V
ESD Rating	2kV min

These are sterss ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

## **ELECTRICAL CHARACTERISTICS**

 $T_A = 25$ °C,  $C_{IN} = C_{OUT} = 10 \mu F$ , unless otherwise specified. The Boldface applies over the full operating temperature range.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
1.5V Version					
Output Voltage	$I_{OUT} = 10 \text{mA}, V_{IN} = 3.0 \text{V}, T_J = 25 ^{\circ}\text{C}$ $10 \text{mA} \le I_{OUT} \le 800 \text{mA}, 2.9 \text{V} \le V_{IN} \le 10 \text{V}$	1.485 <b>1.470</b>	1.500	1.515 <b>1.530</b>	V
1.8V Version	•				
Output Voltage	$\begin{split} I_{OUT} &= 10 \text{mA}, \ V_{IN} = 3.3 \text{V}, \ T_{J} = 25 ^{\circ} \text{C} \\ 10 \text{mA} &\leq I_{OUT} \leq 800 \text{mA}, \ 3.2 \text{V} \leq V_{IN} \leq 10 \text{V} \end{split}$	1.782 <b>1.764</b>	1.800	1.818 <b>1.836</b>	V
2.5V Version					
Output Voltage	$\begin{split} &I_{OUT} = 10\text{mA}, \ V_{\text{IN}} = 4.0\text{V}, \ T_{\text{J}} = 25^{\circ}\text{C} \\ &10\text{mA} \leq I_{OUT} \leq 800\text{mA}, \ 3.9\text{V} \leq V_{\text{IN}} \leq 10\text{V} \end{split}$	2.475 <b>2.450</b>	2.500	2.525 <b>2.550</b>	V
2.85V Version					
Output Voltage	$I_{OUT} = 10 \text{mA}, V_{IN} = 4.35 \text{V}, T_{J} = 25^{\circ}\text{C}$ $10 \text{mA} \le I_{OUT} \le 800 \text{mA}, 4.25 \text{V} \le V_{IN} \le 10 \text{V}$	2.821 <b>2.793</b>	2.850	2.878 <b>2.907</b>	V
3.00V Version	•				
Output Voltage	$\begin{split} I_{OUT} &= 10 \text{mA}, \ V_{IN} = 4.5 \text{V}, \ T_{J} = 25 \text{°C} \\ 10 \text{mA} &\leq I_{OUT} \leq 800 \text{mA}, \ 4.4 \text{V} \leq V_{IN} \leq 10 \text{V} \end{split}$	2.970 <b>2.940</b>	3.000	3.030 <b>3.060</b>	V
3.30V Version		•	·		•
Output Voltage	$\begin{split} I_{OUT} &= 10\text{mA}, \ V_{IN} = 4.8\text{V}, \ T_{J} = 25^{\circ}\text{C} \\ 10\text{mA} \leq I_{OUT} \leq 800\text{mA}, \ 4.7\text{V} \leq V_{IN} \leq 10\text{V} \end{split}$	3.267 <b>3.234</b>	3.300	3.333 <b>3.366</b>	V
5V Version	•				
Output Voltage	$I_{OUT} = 10 \text{mA}, V_{IN} = 6.5 \text{V}$ $10 \text{mA} \le I_{OUT} \le 800 \text{mA}, 6.4 \text{V} \le V_{IN} \le 12 \text{V}$	4.950 <b>4.90</b>	5	5.050 <b>5.10</b>	V
All Voltage Options					
Reference Voltage	$\begin{split} &I_{\text{OUT}} = 10\text{mA},  (V_{\text{IN}} - V_{\text{OUT}}) = 2\text{V},  T_{\text{J}} = 25^{\circ}\text{C} \\ &10\text{mA} \leq I_{\text{OUT}} \leq 800\text{mA},  1.4\text{V} \leq (V_{\text{IN}} - V_{\text{OUT}}) \leq 10\text{V} \end{split}$	1.238 <b>1.225</b>	1.250	1.262 <b>1.270</b>	V
Output Voltage Temperature Stability			0.3		%
Line Regulation (Note 1)	$V_{INMIN} \le V_{IN} \le 12V, V_{OUT} = Fixed/Adj,$ $I_{OUT} = 10mA$		3	7	mV
Load Regulation (Note 1)	10mA≤I <sub>OUT</sub> ≤ 800mA,V <sub>OUT</sub> =Fixed/Adj		6	12	mV
Dropout Voltage (Note 2)	$ \begin{array}{c} \rm I_{out} = 100mA \\ \rm I_{out} = 500mA \\ \rm I_{out} = 800mA \end{array} $		1.00 1.05 1.10	1.10 1.15 1.20	V
Quiescent Current	4.25V≤V <sub>IN</sub> ≤ 6.5V		5	10	mA

## **ELECTRICAL CHARACTERISTICS**

 $T_{_{A}} = 25^{\circ}C$ ,  $C_{_{IN}} = C_{_{OUT}} = 10 \mu F$ , unless otherwise specified. The Boldface applies over the full operating temperature range.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Adjust Pin Current			50	120	μΑ
Current Limit	$(V_{IN}^{-}V_{OUT}^{-})=5V$	1.0	1.5	2.0	A
Thermal Regulation	25°C, 30mS pulse		0.01	0.1	%/W
Ripple Rejection	$\begin{array}{c} f_{RIPPLE} \!\!=\!\! 120 Hz,  (V_{IN} \!\!-\!\! V_{OUT}) \!\!=\!\! 2V, \\ V_{RIPPLE} \!\!=\!\! 1V_{PP} \end{array}$	60	75		dB
Long Term Stability	125°C, 1000Hrs		0.03		%
RMS Output Noise	% of V <sub>OUT</sub> , 10Hz≤f≤10kHz		0.003		%
Thermal Resistance	TO-220 Junction to Case, at Tab		3		°C/W
	TO-220 Junction to Ambient		60		
	TO-263 Junction to Case, at Tab		3		
	TO-263 Junction to Ambient		60		
	TO-252 Junction to Case, at Tab		6		
	TO-252 Junction to Ambient		126		
	SOT-223 Junction to Case, at Tab		15		
	SOT-223 Junction to Ambient		156		

<sup>\*</sup>Note 1 - For fixed option,  $V_{INMIN} = V_{OUT} + 1.5V$  - For adjustable option,  $V_{INMIN} = V_{IN} - V_{OUT} = 1.4V$ 

<sup>\*</sup>Note 2 - Dropout voltage is the input voltage minus output voltage that produces a 1% decrease in output voltage with respect to the nominal output voltage at  $V_{IN} = V_{OUT} + 1.5 V$ 

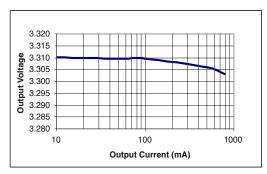


Figure 1. Load Regulation for SPX1117M3-3.3;  $V_{IN}$ =4.8V

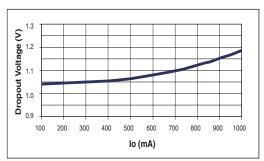


Figure 3. Dropout Voltage vs Output Current for SPX1117M3-3.3;  $V_{IN}$ =4.8V,  $C_{OUT}$ =2.2 $\mu$ F

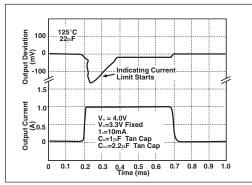


Figure 5. Current Limit for SPX1117M3-3.3, Output Voltage Deviation with I<sub>OUT</sub>=10mA to 1A Step

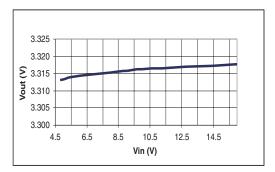


Figure 2. Line Regulation for SPX1117M3-3.3;  $I_{OUT}$ =10mA

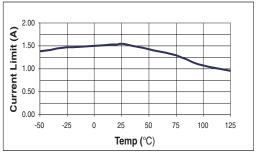


Figure 4. Current Limit for SP1117M3-3.3;  $V_{IN}$ =4.8V,  $C_{IN}$ = $C_{OUT}$ = $I\mu F$ ,  $I_{OUT}$  pulsed from 10mA to Current Limit

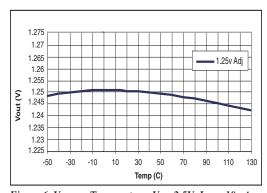


Figure 6.  $V_{OUT}$  vs Temperature,  $V_{IN}$ =2.5V,  $I_{OUT}$ =10mA

1.83

1.82

1.81 3

2.88

2.82

-50 -30

€<sub>2.86</sub> **7** 2.84

1.8v Adi

2.85v Adj

2.85v Fixed

70 90 110 130

1.8v Fixed

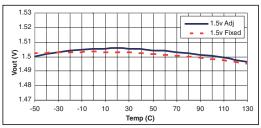


Figure 7. V<sub>IN</sub>=3.0V, I<sub>OUT</sub>=10mA

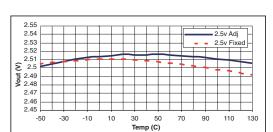
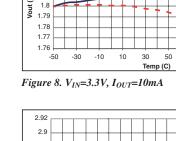


Figure 9. V<sub>IN</sub>=4.0V, I<sub>OUT</sub>=10mA



-10 Figure 10.  $V_{IN}$ =4.85V,  $I_{OUT}$ =10mA

10 30 50 70 90 110 130

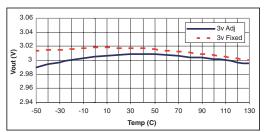


Figure 11. V<sub>IN</sub>=4.85V, I<sub>OUT</sub>=10mA

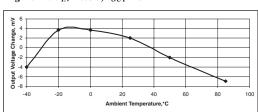


Figure 13. Line Regulation vs Temperature. V<sub>OUT</sub>=1.8V (adjustable),  $V_{IN}=3.3V$ 

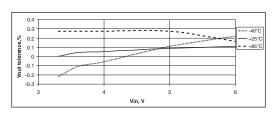
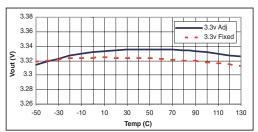


Figure 15. Line Regulation at I<sub>LOAD</sub>=800mA over Temperature,  $V_{OUT}$ =1.8V adjustable



Temp (C)

Figure 12.  $V_{IN}$ =5.0V,  $I_{OUT}$ =10mA

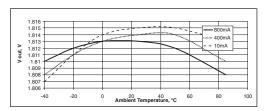


Figure 14. Output Voltage vs Temperature at different Current Loads,  $V_{IN}$ =3.3V,  $V_{OUT}$  = 1.8V Adjustable

## **Output Capacitor**

To ensure the stability of the SPX1117, an output capacitor of at least  $2.2\mu F$  (tantalum or ceramic) or  $10\mu F$  (aluminum) is required. The value may change based on the application requirements of the output load or temperature range. The value of ESR can vary based on the type of capacitor used in the applications to guarantee stability. The recommended value for ESR is  $0.5\Omega$  or less. A larger value of output capacitance (up to  $100\mu F$ ) can improve the load transient response.

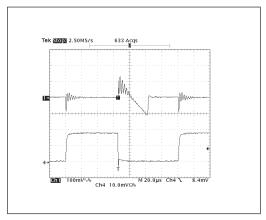


Figure 16. Load Step Response (0 to 800mA), Vin=3.3V, Vout=1.8V, Cin=10 $\mu$ F, Cout=2.2 $\mu$ F, Ceramic; I = Vout, 4= Iload

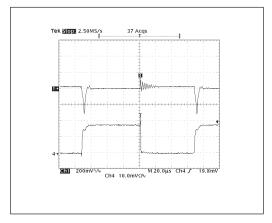
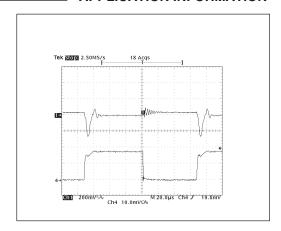


Figure 17. Load Step Response (0 to 800mA), Vin=3.3V, Vout=1.8V, Cin=10µF, Cout=2.2µF, OSCON; 1 = Vout, 4= Iload



#### **Soldering Methods**

The SPX1117 SOT-223 package is designed to be compatible with infrared reflow or vapor-phase reflow soldering techniques. During soldering, the non-active or mildly active fluxes may be used. The SPX1117 die is attached to the heatsink lead which exits opposite the input, output, and ground pins.

Hand soldering and wave soldering should be avoided since these methods can cause damage to the device with excessive thermal gradients on the package. The SOT-223 recommended soldering method are as follows: vapor phase reflow and infrared reflow with the component preheated to within 65°C of the soldering temperature range.

#### Thermal Characteristics

The thermal resistance of SPX1117 (SOT-223) Package) is 15°C/W from junction to tab and 31 °C/W from tab to ambient for a total of 46 °C/W from junction to ambient (Table 1). The SPX1117 features the internal thermal limiting to protect the device during overload conditions. Special care needs to be taken during continuous load conditions such that the maximum junction temperature does not exceed 125 °C. Thermal protection is activated at >155°C and deactiviated at <140 °C.

Taking the FR-4 printed circuit board and 1/16 thick with 1 ounce copper foil as an experiment (fig.13), the PCB material is effective at transmitting heat with the tab attached to the pad area and a ground plane layer on the backside of the substrate. Refer to table 1 for the results of the experiment.

The thermal interaction from other components in the application can effect the thermal resistance of the SPX1117. The actual thermal resistance can be determined with experimentation.

SPX1117 power dissipation is calculated as follows:

$$P_{D} = (V_{IN} - V_{OUT})(I_{OUT})$$

 $\begin{aligned} P_{_{D}} &= (V_{_{IN}} - V_{_{OUT}})(I_{_{OUT}}) \\ Maximum Junction Temperature range: \end{aligned}$ 

 $T_{J} = T_{A}(max) + P_{D}^{*}$  thermal resistance (junction-to-ambient)

Maximum junction temperature must not exceed the 125°C.

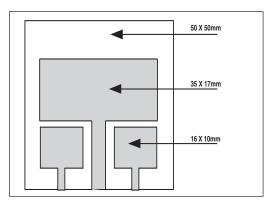


Figure 19. Substrate Layout for SOT-223

#### **Ripple Rejection**

Ripple rejection can be improved by adding a capacitor between the ADJ pin and ground as shown in Figure 23. When ADJ pin bypassing is used, the value of the output capacitor required increases to its maximum. If the ADJ pin is not bypassed, the value of the output capacitor can be lowered to 10µF for an electrolytic aluminum capacitor or 2.2 µF for a ceramic or solid tantalum capacitor (Fig 22).

However the value of the ADJ-bypass capacitor should be chosen with respect to the following equation:

$$C = 1 / (6.28 * F_R * R_1)$$

Where

C = value of the capacitor in Farads (select an equal or larger standard value),

 $F_R$  = ripple frequency in Hz,

 $R_{i}$  = value of resistor R1 in Ohms.

If an ADJ-bypass capacitor is used, the amplitude of the output ripple will be independent of the output voltage. If an ADJ-bypass capacitor is not used, the output ripple will be proportional to the ratio of the output voltage to the reference voltage:

$$M = V_{OUT} / V_{REF}$$

Where M = multiplier for the ripple seen when the ADJ pin is optimally bypassed.

$$V_{REF} = 1.25V$$

Ripple rejection for the adjustable version is shown in Figure 20.

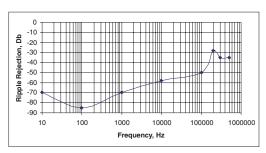


Figure 20. Ripple Rejection; Vin=3.3V, Vout=1.8V (adj.), Iload=200mA

TABLE 1

PC BOARD	TOPSIDE COPPER	BACKSIDE COPPER	THERMAL RESISTANCE JUNC. TO AMB.
mm <sup>2</sup>	mm <sup>2</sup>	$mm^2$	°C/W
2500	2500	2500	46
2500	1250	2500	47
2500	950	2500	49
2500	2500	0	51
2500	1800	0	53
1600	600	1600	55
2500	1250	0	58
2500	915	0	59
1600	600	0	67
900	240	900	72
900	240	0	85

## **Output Voltage**

The output of the adjustable regulator can be set to any voltage between 1.25V and 15V. The value of  $V_{\rm OUT}$  can be quickly approximated using the formula

$$V_{OUT} = 1.25 * (R_1 + R_2)/R_1$$

A small correction to this formula is required depending on the values of resistors  $R_1$  and  $R_2$ , since the adjustable pin current (approx  $50\mu A$ ) flows through  $R_2$ . When  $I_{ADJ}$  is taken into account, the formula becomes

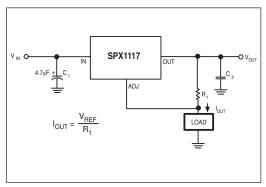


Figure 21. Current Source

$$V_{\rm OUT} = V_{\rm REF}(1+(R_2/R_1)) + I_{\rm ADJ} * R_2,$$
 where 
$$V_{\rm REF} = 1.25 V.$$

# **Layout Considerations**

Parasitic line resistance can degrade load regulation. In order to avoid this, connect  $R_1$  directly to  $V_{\text{OUT}}$  as illustrated in Figure 25. For the same reason,  $R_2$  should be connected to the negative side of the load.

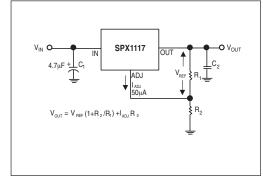


Figure 22. Typical Adjustable Regulator

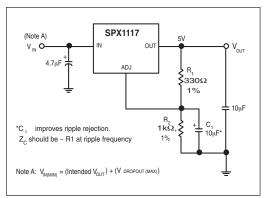


Figure 23. Improving Ripple Rejection

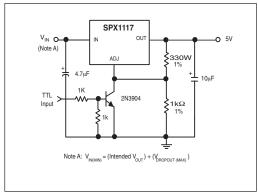


Figure 24. 5V Regulator with Shutdown

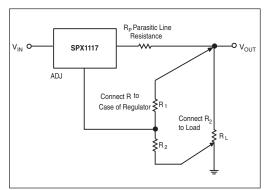
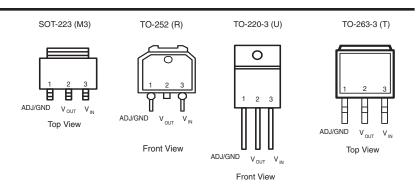
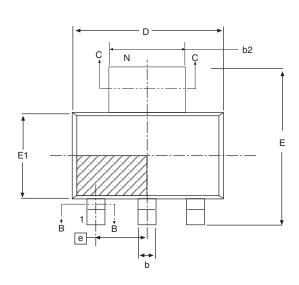


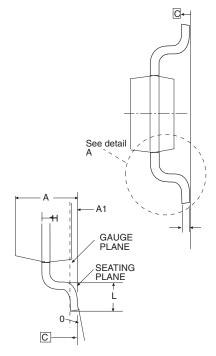
Figure 25. Recommended Connections for Best Results

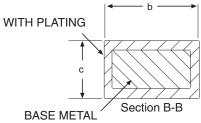
#### **PINOUTS**

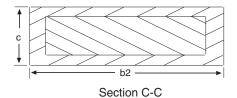




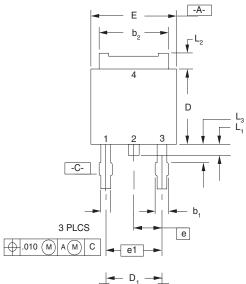
3 PIN SOT-223 JEDEC TO-261	Dimensions in (mm)		
(AA) Variation	MIN	NOM	MAX
A	-	-	1.80
A1	0.02	-	0.10
A2	1.50	1.60	1.70
b	0.66	0.76	0.84
b2	2.90	3.00	3.10
С	0.23	0.30	0.35
D	6.30	6.50	6.70
Е	6.70	7.00	7.30
E1	3.30	3.50	3.70
e	2.30 BASIC		
e1	4.60 BASIC		
L	0.75	_	_
ø	0°	-	10°

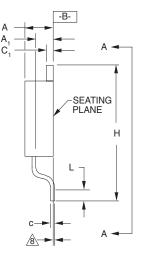






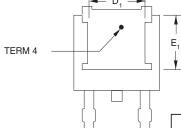
**3 PIN SOT-223** 





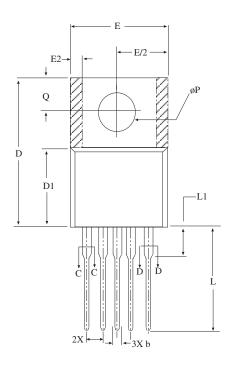
#### NOTES

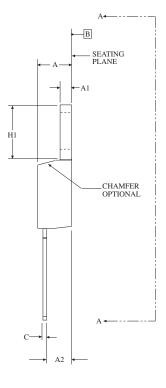
- Refer To Applicable Symbol List.
   Dimensions And Tolerancing Per Ansi Y14.5m 1982.
- 3. Lead Dimension Uncontrolled in L<sub>3</sub>
- 4. Tab Contour Optional Within Dim. b<sub>2</sub> & L<sub>2</sub> And E<sub>1</sub> & D<sub>1</sub>
- 5. D<sub>1</sub> & E<sub>1</sub> Establishes A Minimum Mounting Surface for Terminal 4.
- 6. L is the Termal Length for Soldering.
- 7. Controlling Dimension: Inch
- 8. 2 Mils Suggested For Postive Contact At Mounting.



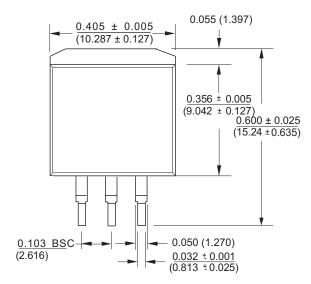
**BACK VIEW A-A** 

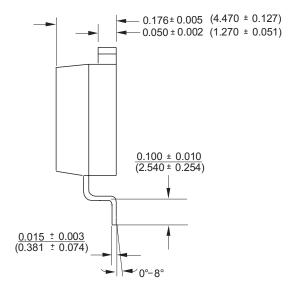
S Y					
M B	INCH	HES	N	ИΜ	N O
O L	MIN.	MAX	MIN.	MAX	T E
Α	0.086	0.094	2.184	2.3876	
A1	0.035	0.045	0.889	1.143	
b	0.025	0.035	0.635	0.889	
b1	0.300	0.045	7.620	1.143	
b2	0.205	0.215	5.207	5.461	4
С	0.018	0.023	0.457	0.5842	
c1	0.018	0.023	0.457	0.5842	
D	0.235	0.245	5.969	6.223	
D1	0.170	-	4.318	-	4,5
E	0.250	0.265	6.350	6.731	
E1	0.170	-	4.318	-	4,5
е	0.0	98	2.4	189	
e1	0.	180	4.	572	
Н	0.370	0.410	9.398	10.414	
L	0.020	-	0.508	-	6
L1	0.025	0.040	0.635	1.016	
L2	0.035	0.050	0.889	1.270	4
L3	0.045	0.060	1.143	1.524	3





	Dimensions in (mm)	3 PIN TO-220 JEDEC TO-220 (AB) Variation MIN NOM MAX		
ŀ	A	.140	-	.190
ľ	A1	.020	-	.055
Ī	A2	.080	-	.115
ľ	b	.015	.02	7 .040
Ī	b1	.015	-	.038
	b2	.045	-	.070
	b3	.045	-	.068
	c	.014 -		.024
	c1	.014 -		.022
	D	.560	-	.650
Ī	D1	.330	-	.355
	D2	.480	) -	.507
	E	.380	-	.420
	E1	.270	-	.350
	E2	-	-	.030
	e	.1	00 BSC	2
	e1	.2	200 BSC	7
	N		4	
Ī	H1	.230	-	.270
Ī	L1	-	-	.250
Ī	L2	-	-	-
-	ΔР	.139	-	.161
Ī	Q	.100	-	.135





PART NUMBER	ACC.	OUTPUT VOLTAGE	PACKAGE
SPX1117M3	1%	OUTPUT VOLTAGE Adj	3 Pin SOT-223
		Adj	
SPX1117M3-1.5	1%	1.5V	3 Pin SOT-223
SPX1117M3-1.5/TR	1%	1.5V	3 Pin SOT-223
		1.8V	
SPX1117M3-1.8/TR	1%	1.8V	3 Pin SOT-223
		2.5V	
		2.5V	
		2.85V	
SPX1117M3-2.85/TR	1%	2.85V	3 Pin SOT-223
		3.0V	
SPX1117M3-3.0/TR	1%	3.0V	3 Pin SOT-223
SPX1117M3-3.3	1%	3.3V	3 Pin SOT-223
SPX1117M3-3.3/TR	1%	3.3V	3 Pin SOT-223
SPX1117M3-5.0	1%	5.0V	3 Pin SOT-223
		5.0V	
SPX1117R	1%	Adj	3 Pin TO-252
		Adj	
		1.5V	
SPX1117R-1.5/TR	1%	1.5V	3 Pin TO-252
		1.8V	
SPX1117R-1.8/TR	1%	1.8V	3 Pin TO-252
		2.5V	
SPX1117R-2.5/TR	1%	2.5V	3 Pin TO-252
SPX1117R-2.85	1%	2.85V	3 Pin TO-252
		2.85V	
SPX1117R-3.0	1%	3.0V	3 Pin TO-252
SPX1117R-3.0/TR	1%	3.0V	3 Pin TO-252
SPX1117R-3.3	1%	3.3V	3 Pin TO-252
SPX1117R-3.3/TR	1%	3.3V	3 Pin TO-252

Available in lead free packaging. To order add "-L" suffix to part number.

Example: SPX1117M3-5.0/TR = standard; SPX1117M3-L-5.0/TR = lead free

/TR = Tape and Reel

Pack quantity is 500 for TO-263, 2,000 for TO-252, and 2,500 for SOT223.



#### **ANALOG EXCELLENCE**

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	ACC.		PACKAGE
SPX1117T	1%	Adj	3 Pin TO-263
SPX1117T/TR	1%	Adj	3 Pin TO-263
SPX1117T-1.5	1%	1.5V	3 Pin TO-263
SPX1117T-1.5/TR	1%	1.5V	3 Pin TO-263
SPX1117T-1.8	1%	1.8V	3 Pin TO-263
SPX1117T-1.8/TR	1%	1.8V	3 Pin TO-263
SPX1117T-2.5	1%	2.5V	3 Pin TO-263
SPX1117T-2.5/TR	1%	2.5V	3 Pin TO-263
SPX1117T-2.85	1%	2.85V	3 Pin TO-263
		2.85V	
SPX1117T-3.0	1%	3.0V	3 Pin TO-263
SPX1117T-3.0/TR	1%	3.0V	3 Pin TO-263
SPX1117T-3.3	1%	3.3V	3 Pin TO-263
SPX1117T-3.3/TR	1%	3.3V	3 Pin TO-263
SPX1117U	1%	Adj	3 Pin TO220
SPX1117U-1.5	1%	1.5V	3 Pin TO220
		1.8V	
		2.5V	
		2.85V	
		3.0V	
SPX1117U-3.3	1%	3.3V	3 Pin TO220

Available in lead free packaging. To order add "-L" suffix to part number.

Example: SPX1117M3-5.0/TR = standard; SPX1117M3-L-5.0/TR = lead free

/TR = Tape and Reel

Pack quantity is 500 for TO-263, 2,000 for TO-252, and 2,500 for SOT223.



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