

Binary	Mnemonic	Instruction	Meaning
0000	ADD	Addition	$r1 := r1 + r2$
0001	AND	Boolean AND	$r1 := r1 \text{ AND } r2$
0010	MOVE	Move register	$r1 := r2$
0011	COMPL	Complement	$r1 := \text{inv}(r2)$
0100	LSHIFT	Left shift	$r1 := \text{lshift}(r2)$
0101	RSHIFT	Right shift	$r1 := \text{rshift}(r2)$
0110	GETMBR	Store MBR in register	$r1 := \text{mbr}$
0111	TEST	Test register	If $r2 < 0$ then $n := \text{true}$ ; If $r2 = 0$ then $z := \text{true}$
1000	BEGRD	Begin read	$\text{mar} := r1, rd$
1001	BEGWR	Begin write	$\text{mar} := r1, \text{mbr} := r2, \text{wr}$
1010	CONRD	Continue read	$rd$
1011	CONWR	Continue write	$\text{wr}$
1100		(not used)	
1101	NJUMP	Jump if N=1	If $n$ then goto $r$
1110	ZJUMP	Jump if Z=1	If $z$ then goto $r$
1111	UJUMP	Unconditional jump	goto $r$

Microinstruction opcode	ALUH	SHH	NZ	AND	MBR	WR	MSLH	MSLI
0	ADD			+				
1	AND	+		+				
2	MOVE	+		+				
3	COMPL	+	+					
4	LSHIFT	+	+					
5	RSHIFT	+	+					
6	GETMBR	+		+	+			
7	TEST	+		+				
8	BEGRD	+			+	+		
9	BEGWR	+			+	+	+	
10	CONRD	+				+		
11	CONWR	+					+	
12								
13	NJUMP	+						+
14	ZJUMP	+						+
15	UJUMP	+						+

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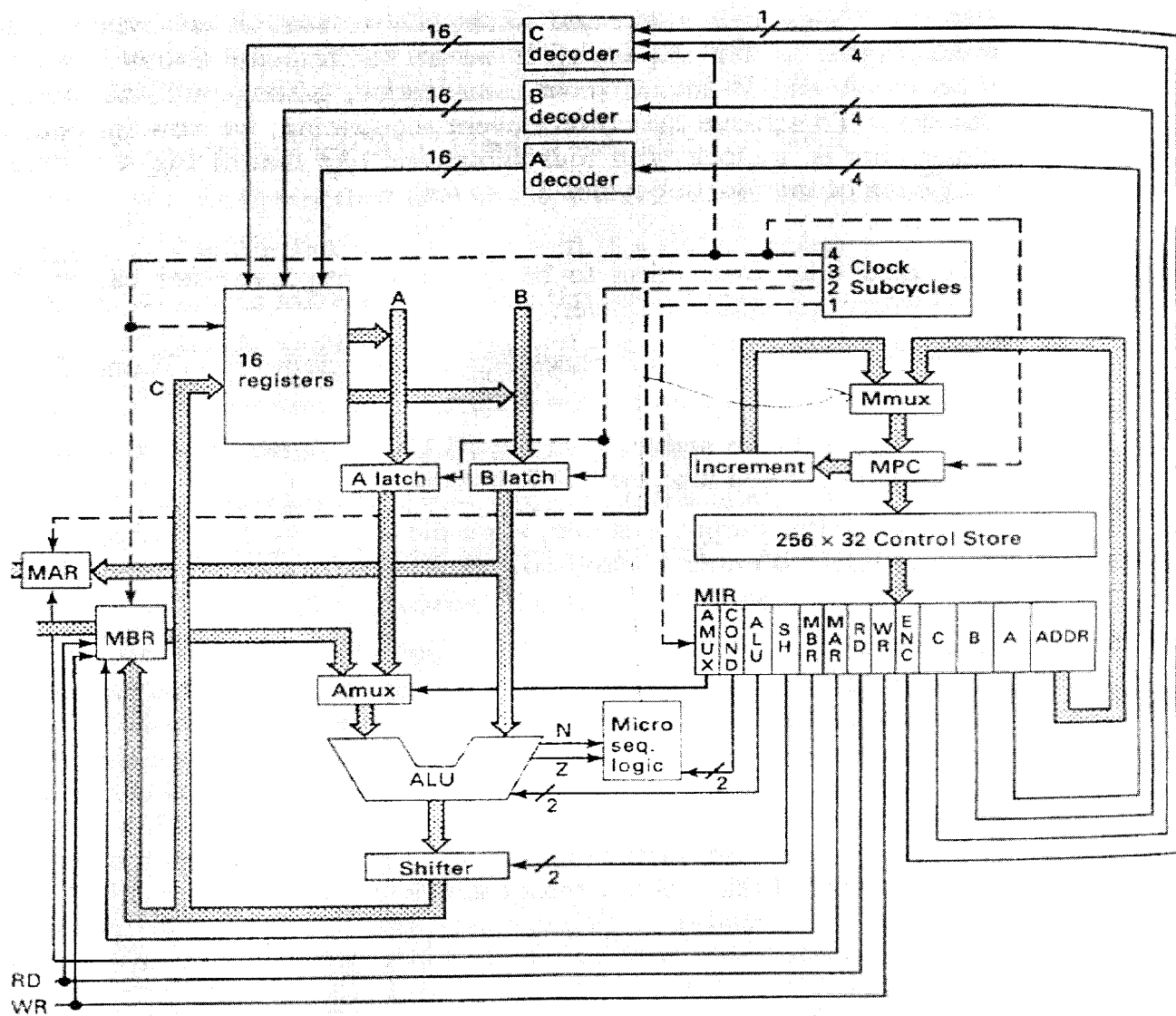
0:  mar := pc; rd;
1:  rd;
2:  ir := mbr;
   tir := lshift(ir);
   if n then goto 28;
3:  ir := lshift(tir);
   if n then goto 19;
4:  tir := lshift(tir);
   if n then goto 11;
5:  alu := tir;
   if n then goto 09;
6:  mar := ir; rd; {LODD}
7:  rd;
8:  ac := mbr;
   goto 0;
9:  mar := ir; mbr := ac; wr; {STOD}
10: wr;
   goto 0;
11: alu := tir;
   if n then goto 15;
12: mar := ir; rd; {ADDD}
13: rd;
14: a := mbr;
   ac := ac + a;
   goto 0;
15: mar := ir; rd; {SUBD}
16: rd;
17: ac := ac + 1;
18: a := mbr;
   a := inv(a);
   ac := ac + a;
   goto 0;
19: tir := lshift(tir);
   if n then goto 25;
20: alu := tir;
   if n then goto 23;
21: alu := ac; {JPOS}
   if n then goto 0;
22: pc := ir;
   pc := band(pc, amask);
   goto 0;

23: alu := ac; {JZER}
   if z then goto 22;
24: goto 0;
25: alu := tir;
   if n then goto 27;
26: pc := ir; {JUMP}
   pc := band(pc, amask);
   goto 0;
27: ac := ir; {LOCO}
   ac := band(ac, amask);
   goto 0;
28: tir := lshift(tir);
   if n then goto 40;
29: tir := lshift(tir);
   if n then goto 35;
30: alu := tir;
   if n then goto 33;
31: a := ir; {LODL}
   a := a + sp;
32: mar := a; rd;
   rd;
   ac := mbr;
   goto 0;
33: a := ir; {STOL}
   a := a + sp;
34: mar := a; mbr := ac; wr;
   wr;
   goto 0;
35: alu := tir;
   if n then goto 38;
36: a := ir; {ADDL}
   a := a + sp;
37: mar := a; rd;
   rd;
   a := mbr;
   ac := ac + a;
   goto 0;

38: a := ir; {SUBU}
   a := a + sp;
39: mar := a; rd;
   rd;
   goto 99;
40: tir := lshift(tir);
   if n then goto 46;
41: alu := tir;
   if n then goto 44;
42: alu := ac; {JNEG}
   if n then goto 22;
43: goto 0;
44: alu := ac; {JNZE}
   if z then goto 0;
45: pc := ir;
   pc := band(pc, amask);
   goto 0;
46: tir := lshift(tir);
   if n then goto 50;
47: sp := sp + (-1); {CALL}
48: mar := sp; mbr := pc; wr;
   wr;
49: pc := ir;
   pc := band(pc, amask);
   goto 0;
50: tir := lshift(tir);
   if n then goto 65;
51: tir := lshift(tir);
   if n then goto 59;
52: alu := tir;
   if n then goto 56;
53: mar := ac; rd; {PSHI}
   rd;
54: sp := sp + (-1);
55: a := mbr;
   mar := sp; mbr := a; wr;
   wr;
   goto 0;
56: mar := sp; rd; {POPL}
57: rd;
   sp := sp + 1;

58: a := mbr;
   mar := ac; mbr := a; wr;
   wr;
   goto 0;
59: alu := tir;
   if n then goto 62;
60: sp := sp + (-1); {PUSH}
61: mar := sp; mbr := ac; wr;
   wr;
   goto 0;
62: mar := sp; rd; {POP}
63: rd;
   sp := sp + 1;
64: ac := mbr;
   goto 0;
65: tir := lshift(tir);
   if n then goto 73;
66: alu := tir;
   if n then goto 70;
67: mar := sp; rd; {RETN}
68: rd;
   sp := sp + 1;
69: pc := mbr;
   goto 0;
70: a := ac; {SWAP}
71: ac := sp;
72: sp := a;
   goto 0;
73: alu := tir;
   if n then goto 76;
74: a := ir; {INSP}
   a := band(a, smask);
75: sp := sp + a;
   goto 0;
76: a := ir; {DESP}
   a := band(a, smask);
77: a := inv(a);
78: a := a + 1;
   sp := sp + a;
   goto 0;

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Binary	Mnemonic	Instruction	Meaning
0000xxxxxxxxxxxx	LODD	Load direct	$ac := m[x]$
0001xxxxxxxxxxxx	STOD	Store direct	$m[x] := ac$
0010xxxxxxxxxxxx	ADD	Add direct	$ac := ac + m[x]$
0011xxxxxxxxxxxx	SUBD	Subtract direct	$ac := ac - m[x]$
0100xxxxxxxxxxxx	JPOS	Jump positive	If $ac \geq 0$ then $pc := x$
0101xxxxxxxxxxxx	JZER	Jump zero	If $ac = 0$ then $pc := x$
0110xxxxxxxxxxxx	JUMP	Jump	$pc := x$
0111xxxxxxxxxxxx	LOCD	Load constant	$ac := x$ ( $0 \leq x \leq 4095$ )
1000xxxxxxxxxxxx	LODL	Load local	$ac := m[sp + x]$
1001xxxxxxxxxxxx	STOL	Store local	$m[sp + x] := ac$
1010xxxxxxxxxxxx	ADDL	Add local	$ac := ac + m[sp + x]$
1011xxxxxxxxxxxx	SUBL	Subtract local	$ac := ac - m[sp + x]$
1100xxxxxxxxxxxx	JNEG	Jump negative	If $ac < 0$ then $pc := x$
1101xxxxxxxxxxxx	JNZE	Jump nonzero	If $ac \neq 0$ then $pc := x$
1110xxxxxxxxxxxx	CALL	Call procedure	$sp := sp - 1; m[sp] := pc; pc := x$
11110000000000	PSHI	Push indirect	$sp := sp - 1; m[sp] := m[ac]$
11110010000000	POPI	Pop indirect	$m[ac] := m[sp]; sp := sp + 1$
11110100000000	PUSH	Push onto stack	$sp := sp - 1; m[sp] := ac$
11110110000000	POP	Pop from stack	$ac := m[sp]; sp := sp + 1$
11111000000000	RETN	Return	$pc := m[sp]; sp := sp + 1$
11111010000000	SWAP	Swap $ac, sp$	$tmp := ac; ac := sp; sp := tmp$
11111100yyyyyy	INSP	Increment $sp$	$sp := sp + y$ ( $0 \leq y \leq 255$ )
11111110yyyyyy	DESP	Decrement $sp$	$sp := sp - y$ ( $0 \leq y \leq 255$ )

xxxxxxxxxxxx is a 12-bit machine address; in column 4 it is called  $x$ .  
 yyyyyyy is an 8-bit constant; in column 4 it is called  $y$ .

Statement	A M U X	C O N D	A L U	S H	M B R	M A R	R D	W R	E N C	C	B	A	ADDR
$mar := pc; rd$	0	0	2	0	0	1	1	0	0	0	0	0	00
$rd$	0	0	2	0	0	0	1	0	0	0	0	0	00
$ir := mbr$	1	0	2	0	0	0	0	0	1	3	0	0	00
$pc := pc + 1$	0	0	0	0	0	0	0	0	1	0	6	0	00
$mar := ir; mbr := ac; wr$	0	0	2	0	1	1	0	1	0	0	3	1	00
$alu := tir; \text{if } n \text{ then goto } 15$	0	1	2	0	0	0	0	0	0	0	0	4	15
$ac := inv(mbr)$	1	0	3	0	0	0	0	0	1	1	0	0	00
$tir := lshift(tir); \text{if } n \text{ then goto } 25$	0	1	2	2	0	0	0	0	1	4	0	4	25
$alu := ac; \text{if } z \text{ then goto } 22$	0	2	2	0	0	0	0	0	0	0	0	1	22
$ac := band(ir, amask); \text{goto } 0$	0	3	1	0	0	0	0	0	1	1	8	3	00
$sp := sp + (-1); rd$	0	0	0	0	0	0	1	0	1	2	2	7	00
$tir := lshift(tir + ir); \text{if } n \text{ then goto } 69$	0	1	0	2	0	0	0	0	1	4	3	3	69

0: <i>mar</i> := <i>pc</i> ; <i>rd</i> ;	{main loop}	
1: <i>pc</i> := <i>pc</i> + 1; <i>rd</i> ;	{increment <i>pc</i> }	{10x or 11xx?}
2: <i>ir</i> := <i>mbr</i> ; if <i>n</i> then goto 28;	{save, decode <i>mbr</i> }	{100 or 1101?}
3: <i>tir</i> := <i>lshift</i> ( <i>ir</i> + <i>ir</i> ); if <i>n</i> then goto 19;		{1100 = JNEG}
4: <i>tir</i> := <i>lshift</i> ( <i>tir</i> ); if <i>n</i> then goto 11;	{000x or 001xx?}	
5: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 9;	{0000 or 0001?}	{1101 = JNZE}
6: <i>mar</i> := <i>ir</i> ; <i>rd</i> ;	{0000 = LODD}	
7: <i>rd</i> ;		
8: <i>ac</i> := <i>mbr</i> ; goto 0;		{1110 = CALL}
9: <i>mar</i> := <i>ir</i> ; <i>mbr</i> := <i>ac</i> ; <i>wr</i> ;	{0001 = STOD}	
10: <i>wr</i> ; goto 0;		{1111, examine <i>addr</i> }
11: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 15;	{0010 or 0011?}	
12: <i>mar</i> := <i>ir</i> ; <i>rd</i> ;	{0010 = ADDD}	
13: <i>rd</i> ;		
14: <i>ac</i> := <i>mbr</i> + <i>ac</i> ; goto 0;		{1111000 = PSHH}
15: <i>mar</i> := <i>ir</i> ; <i>rd</i> ;	{0011 = SUBD}	
16: <i>ac</i> := <i>ac</i> + 1; <i>rd</i> ;	{Note: <i>x</i> - <i>y</i> = <i>x</i> + 1 + not <i>y</i> }	{1111001 = POPH}
17: <i>a</i> := <i>inv</i> ( <i>mbr</i> );		
18: <i>ac</i> := <i>ac</i> + <i>a</i> ; goto 0;		
19: <i>ir</i> := <i>lshift</i> ( <i>tir</i> ); if <i>n</i> then goto 25;	{010x or 011xx?}	
20: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 23;	{0100 or 0101?}	
21: <i>alu</i> := <i>ac</i> ; if <i>n</i> then goto 0;	{0100 = JPOS}	{1111010 = PUSH}
22: <i>pc</i> := <i>band</i> ( <i>ir</i> , <i>amask</i> ); goto 0;	{perform the jump}	
23: <i>alu</i> := <i>ac</i> ; if <i>z</i> then goto 22;	{0101 = JZER}	{1111011 = POP}
24: goto 0;	{jump failed}	
25: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 27;	{0110 or 0111?}	
26: <i>pc</i> := <i>band</i> ( <i>ir</i> , <i>amask</i> ); goto 0;	{0110 = JUMP}	
27: <i>ac</i> := <i>band</i> ( <i>ir</i> , <i>amask</i> ); goto 0;	{0111 = LOCO}	
28: <i>ir</i> := <i>lshift</i> ( <i>ir</i> + <i>ir</i> ); if <i>n</i> then goto 40;	{10xx or 11xx?}	{1111100 = RETN}
29: <i>ir</i> := <i>lshift</i> ( <i>tir</i> ); if <i>n</i> then goto 35;	{100x or 101x?}	
30: <i>alu</i> := <i>ir</i> ; if <i>n</i> then goto 33;	{1000 or 1001?}	
31: <i>a</i> := <i>ir</i> + <i>sp</i> ;	{1000 = LODL}	
32: <i>mar</i> := <i>a</i> ; <i>rd</i> ; goto 7;		
33: <i>a</i> := <i>ir</i> + <i>sp</i> ;	{1001 = STOL}	
34: <i>mar</i> := <i>a</i> ; <i>mbr</i> := <i>ac</i> ; <i>wr</i> ; goto 10;		
35: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 38;	{1010 or 1011?}	
36: <i>a</i> := <i>ir</i> + <i>sp</i> ;	{1010 = ADDL}	
37: <i>mar</i> := <i>a</i> ; <i>rd</i> ; goto 13;		
38: <i>a</i> := <i>ir</i> + <i>sp</i> ;	{1011 = SUBL}	
39: <i>mar</i> := <i>a</i> ; <i>rd</i> ; goto 16;		
40: <i>ir</i> := <i>lshift</i> ( <i>tir</i> ); if <i>n</i> then goto 46;		
41: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 44;		
42: <i>alu</i> := <i>ac</i> ; if <i>n</i> then goto 22;		
43: goto 0;		
44: <i>alu</i> := <i>ac</i> ; if <i>z</i> then goto 0;		
45: <i>pc</i> := <i>band</i> ( <i>ir</i> , <i>amask</i> ); goto 0;		
46: <i>ir</i> := <i>lshift</i> ( <i>tir</i> ); if <i>n</i> then goto 50;		
47: <i>sp</i> := <i>sp</i> + (-1);		
48: <i>mar</i> := <i>sp</i> ; <i>mbr</i> := <i>pc</i> ; <i>wr</i> ;		
49: <i>pc</i> := <i>band</i> ( <i>ir</i> , <i>amask</i> ); <i>wr</i> ; goto 0;		
50: <i>tir</i> := <i>lshift</i> ( <i>tir</i> ); if <i>n</i> then goto 65;		
51: <i>tir</i> := <i>lshift</i> ( <i>tir</i> ); if <i>n</i> then goto 59;		
52: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 56;		
53: <i>mar</i> := <i>ac</i> ; <i>rd</i> ;		
54: <i>sp</i> := <i>sp</i> + (-1); <i>rd</i> ;		
55: <i>mar</i> := <i>sp</i> ; <i>wr</i> ; goto 10;		
56: <i>mar</i> := <i>sp</i> ; <i>sp</i> := <i>sp</i> + 1; <i>rd</i> ;		
57: <i>rd</i> ;		
58: <i>mar</i> := <i>ac</i> ; <i>wr</i> ; goto 10;		
59: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 62;		
60: <i>sp</i> := <i>sp</i> + (-1);		
61: <i>mar</i> := <i>sp</i> ; <i>mbr</i> := <i>ac</i> ; <i>wr</i> ; goto 10;		
62: <i>mar</i> := <i>sp</i> ; <i>sp</i> := <i>sp</i> + 1; <i>rd</i> ;		
63: <i>rd</i> ;		
64: <i>ac</i> := <i>mbr</i> ; goto 0;		
65: <i>tir</i> := <i>lshift</i> ( <i>tir</i> ); if <i>n</i> then goto 73;		
66: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 70;		
67: <i>mar</i> := <i>sp</i> ; <i>sp</i> := <i>sp</i> + 1; <i>rd</i> ;		
68: <i>rd</i> ;		
69: <i>pc</i> := <i>mbr</i> ; goto 0;		
70: <i>a</i> := <i>ac</i> ;		
71: <i>ac</i> := <i>sp</i> ;		
72: <i>sp</i> := <i>a</i> ; goto 0;		
73: <i>alu</i> := <i>tir</i> ; if <i>n</i> then goto 76;		
74: <i>a</i> := <i>band</i> ( <i>ir</i> , <i>smask</i> );		
75: <i>sp</i> := <i>sp</i> + <i>a</i> ; goto 0;		
76: <i>a</i> := <i>band</i> ( <i>tir</i> , <i>smask</i> );		
77: <i>a</i> := <i>inv</i> ( <i>a</i> );		
78: <i>a</i> := <i>a</i> + 1; goto 75;		