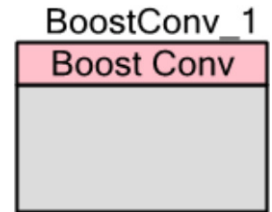


# Boost Converter (BoostConv)

5.0

## Features

- Produces a selectable output voltage that is higher than the input voltage
- Input voltage range between 0.5 V and 3.6 V
- Boosted output voltage range between 1.8 V and 5.25 V
- Source up to 75 mA depending on the selected input and output voltage parameter values
- Two modes of operation: Active and Standby for PSoC 3 or Sleep for PSoC 5LP



## General Description

The Boost Converter (BoostConv) component allows you to configure and control the PSoC boost converter hardware block. The boost converter enables input voltages that are lower than the desired system voltage to be boosted to the desired system voltage level. The converter uses an external inductor to convert the input voltage to the desired output voltage.

The BoostConv component is enabled by default at chip startup with an output voltage of 1.9 V. This allows the chip to start up in scenarios where the input voltage to the boost is below the minimum allowable voltage to power the chip. The configuration parameters defined in the component customizer (default  $V_{IN} = 1.8$  V,  $V_{OUT} = 3.3$  V, Switching Frequency = 400 kHz) will not take effect until the BoostConv\_Start() API is called. The BoostConv component parameters can also be adjusted during run time using the provided APIs.

The boost converter has two main operating modes:

- **Active** – Active mode is the normal mode of operation where the boost regulator actively generates a regulated output voltage.
- **Standby** – Standby mode is a low-power mode of operation with PSoC 3.
- **Sleep** - Sleep mode is a low-power mode of operation with PSoC 5LP.

For more details please refer to Boost Converter section of PSoC Family Datasheet.

## When to Use the Boost Component

Use the BoostConv component when the available voltage source for a system is less than the required voltage level to operate the system. The BoostConv component accepts a battery or other input voltage and produces a higher output voltage.

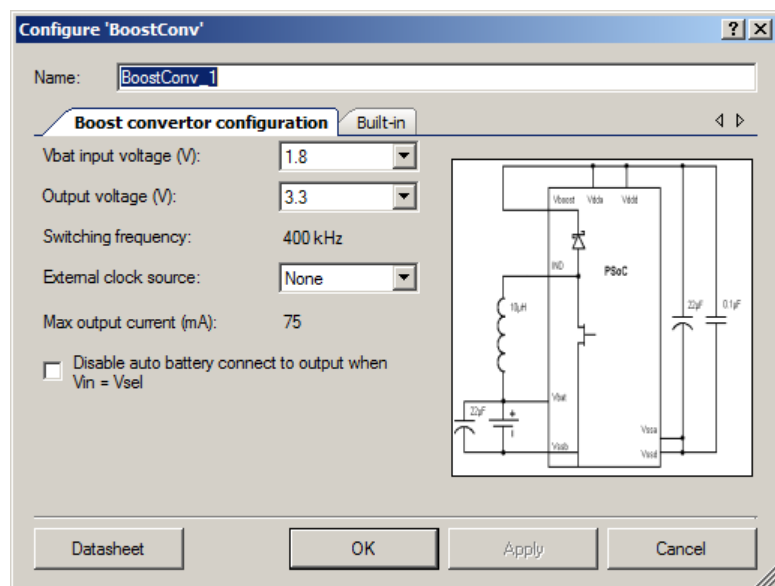
As an example, the system may use a 0.5-V solar cell as the primary power source and rely on the boost block to power the 1.8-V PSoC 3 core. In another application, a 3.3-V system could use the BoostConv component to power a 5.0-V LCD glass.

## Input/Output Connections

The BoostConv component requires no connections in the project schematic view. Fixed-function pins support the boost converter block circuit. The system circuit must provide connections for the input voltage (Vbat), output voltage (Vout), and inductor pin (Ind), and battery ground (Vssb). Refer to the schematic representation shown in the [Functional Description](#) section.

## Component Parameters

Drag a BoostConv component onto your design and double-click it to open the **Configure** dialog.



### Vbat input voltage (V)

This is the  $V_{BAT}$  or other voltage source that is used as the input voltage to the boost converter block. This system circuit connects this voltage to the Vbat PSoC pin. The input voltage can be between 0.5 V and 3.6 V. This value is used to calculate the estimated maximum output current. The default value is 1.8 V.

## Output voltage (V)

This is the target output voltage that the boost converter block will maintain. Use the drop-down list to select the desired output voltage. Output voltage levels are provided in 0.1-V increments from 1.8 V to 3.6 V and in 0.25-V increments from 4.00 V to 5.25 V. The default value is 3.3 V.

An external Schottky diode is required for output voltages above 3.6 V.

The output voltage value can be modified at run time by using the BoostConv\_SelVoltage() function.

## Switching frequency

This is the switching frequency at which the boost converter block will operate. The Switching frequency value is set to 400 kHz for Boost Active mode operation.

The 400-kHz switching frequency is generated using an oscillator internal to the boost converter block.

## External clock source

The External 32 kHz to the Boost frequency is the source of the switching signal when the boost converter block is configured to use an external clock. This value is applicable only for PSoC 3 and can be set to any of the following frequencies:

- None
- ECO 32kHz
- ILO 32kHz

For PSoC 5LP this parameter should be set to None.

## Max output current (mA)

This is an estimate of the maximum output current available from the boost converter block based on the specified **Vbat input voltage** and **Output voltage** values. This is a read-only value.

## Disable auto battery connect to output when Vin = Vsel

When set, disables auto battery connection to output when  $V_{in} = V_{sel}$ . For more details, see [Functional Description](#).



## Application Programming Interface

Application Programming Interface (API) routines allow you to configure the component using software. The following table lists and describes the interface to each function. The subsequent sections cover each function in more detail.

By default, PSoC Creator assigns the instance name “BoostConv\_1” to the first instance of a component in a given design. You can rename it to any unique value that follows the syntactic rules for identifiers. The instance name becomes the prefix of every global function name, variable, and constant symbol. For readability, the instance name used in the following table is “BoostConv.”

Function	Description
BoostConv_Start()	Starts the BoostConv component and puts the boost block into Active mode.
BoostConv_Stop()	Disables the BoostConv component. Turns off power to the boost converter circuitry.
BoostConv_EnableInt()	Enables the boost block undervoltage interrupt generation.
BoostConv_DisableInt()	Disables the boost block undervoltage interrupt generation.
BoostConv_SetMode()	Sets the boost converter mode to Active or Standby (PSoC 3) / Sleep (PSoC 5LP).
BoostConv_SelVoltage()	Selects the target output voltage the boost converter will maintain.
BoostConv_ManualThump()	Forces a single pulse of the boost converter switch transistors.
BoostConv_ReadStatus()	Returns the boost block status register.
BoostConv_ReadIntStatus()	Returns the contents of the boost block interrupt status register.
BoostConv_Init()	Initializes BoostConv registers with initial values provided from customizer.
BoostConv_Enable()	This function enables the boost block (only valid when in Active mode). Component is enabled by default.
BoostConv_Disable()	Disables the boost block.

### PSoC 3 APIs

BoostConv_EnableAutoThump()	Enables automatic thump mode (only available when the boost block is in Standby mode and the switching frequency is set to 32 kHz).
BoostConv_DisableAutoThump()	Disables automatic thump mode.
BoostConv_SelExtClk()	Sets the source of 32-kHz frequency: the 32-kHz ECO or 32-kHz ILO.
BoostConv_SelFreq()	Sets the switching frequency to one of two possible values: 400 kHz (generated internal to the boost converter block) or 32 kHz (sourced external to the boost converter block from the chip ECO-32kHz or ILO-32kHz oscillator). The 32kHz frequency is only applicable for PSoC 3.



## Global Variables

Function	Description
BoostConv_initVar	Indicates whether the Boost Converter has been initialized. The variable is initialized to 0 and set to 1 the first time BoostConv_Start() is called. This allows the component to restart without reinitialization after the first call to the BoostConv_Start() routine.  If reinitialization of the component is required, then the BoostConv_Init() function can be called before the BoostConv_Start() or BoostConv_Enable() function.

## void BoostConv\_Start(void)

**Description:** Starts the BoostConv component and puts the boost block into Active mode. The component is in this state when the chip powers up. This is the preferred method to begin component operation. BoostConv\_Start() sets the initVar variable, calls the BoostConv\_Init() function, and then calls the BoostConv\_Enable() function.

**Parameters:** None

**Return Value:** None

**Side Effects:** If the initVar variable is already set, this function: (1) Sets the initial value of the target output voltage (from the customizer) and mode (Active mode) or restores target output voltage and mode saved in the BoostConv\_Stop() function; (2) Calls the BoostConv\_Enable() function.

## void BoostConv\_Stop(void)

**Description:** Saves boost converter target output voltage and mode. Disables the BoostConv component.

**Parameters:** None

**Return Value:** None

**Side Effects:** Turns off power to the boost converter circuitry. For PSoC 3, sets the boost converter to Standby mode, For PSoC 5LP, sets the boost converter to Sleep mode.

## void BoostConv\_EnableInt(void)

**Description:** This function enables the boost block output undervoltage interrupt generation.

**Parameters:** None

**Return Value:** None

**Side Effects:** None



**void BoostConv\_DisableInt(void)**

**Description:** This function disables the boost block output undervoltage interrupt generation.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

**void BoostConv\_SetMode(uint8 mode)**

**Description:** This function sets the boost converter mode: Active and Standby for PSoC 3 or Sleep for PSoC 5LP.

**Parameters:** uint8 mode: Sets the operational mode for the boost block:

Mode	Notes
BoostConv_BOOSTMODE_ACTIVE	In the active mode, the boost block maintains the selected output voltage.
BoostConv_BOOSTMODE_STANDBY	Low power state, only bandgap and comparator circuitry is active. Automatic thump mode is used with the external 32-kHz clock to regulate output voltage
BoostConv_BOOSTMODE_SLEEP	This mode used with chip sleep mode to reduce power consumption.

**Return Value:** None

**Side Effects:** PSoC 3:

- 1) For Standby mode, this function enables automatic thump mode and sets the switching frequency clock source to the 32-kHz external clock.
- 2) For Active mode this function disables automatic thump mode and sets the switching frequency clock source to the 400-kHz internal clock.

**void BoostConv\_SelVoltage(uint8 voltage)**

**Description:** This function selects the target output voltage the boost converter will maintain.

**Parameters:** uint8 voltage: The target output voltage for the boost converter block. Output voltages above 3.6 V require an external Schottky diode.

Power Setting	Value	Notes
BoostConv_VOUT_OFF	0x00	Off – HI-Z
BoostConv_VOUT_1_8V	0x03	1.8 V
BoostConv_VOUT_1_9V	0x04	1.9 V
BoostConv_VOUT_2_0V	0x05	2.0 V
BoostConv_VOUT_2_1V	0x06	2.1 V
BoostConv_VOUT_2_2V	0x07	2.2 V
BoostConv_VOUT_2_3V	0x08	2.3 V
BoostConv_VOUT_2_4V	0x09	2.4 V
BoostConv_VOUT_2_5V	0x0A	2.5 V
BoostConv_VOUT_2_6V	0x0B	2.6 V
BoostConv_VOUT_2_7V	0x0C	2.7 V
BoostConv_VOUT_2_8V	0x0D	2.8 V
BoostConv_VOUT_2_9V	0x0E	2.9 V
BoostConv_VOUT_3_0V	0x0F	3.0 V
BoostConv_VOUT_3_1V	0x10	3.1 V
BoostConv_VOUT_3_2V	0x11	3.2 V
BoostConv_VOUT_3_3V	0x12	3.3 V
BoostConv_VOUT_3_4V	0x13	3.4 V
BoostConv_VOUT_3_5V	0x14	3.5 V
BoostConv_VOUT_3_6V	0x15	3.6 V
BoostConv_VOUT_4_0V	0x16	4.00 V (external Schottky diode required)
BoostConv_VOUT_4_25V	0x17	4.25 V (external Schottky diode required)
BoostConv_VOUT_4_5V	0x18	4.50 V (external Schottky diode required)
BoostConv_VOUT_4_75V	0x19	4.75 V (external Schottky diode required)
BoostConv_VOUT_5_0V	0x1A	5.00 V (external Schottky diode required)
BoostConv_VOUT_5_25V	0x1B	5.25 V (external Schottky diode required)

**Return Value:** None

**Side Effects:** Function will take affect only when the Boost is enabled.



## void BoostConv\_ManualThump(void)

**Description:** This function forces a single pulse of the boost converter switch transistors.

**Parameters:** None

**Return Value:** None

**Theory:**

**Side Effects:** Thump produces one ~500-ns pulse when set. This routine writes a '0' followed by a '1' to the bit 7 "thump" bit in the boost block BOOST\_CR0 register.

## uint8 BoostConv\_ReadStatus(void)

**Description:** This function returns the contents of the boost block status register.

**Parameters:** None

**Return Value:** uint8 boost block status register: BOOST\_SR:

Bit	Name	Description
7	BoostConv_RDY	When set, internal circuits have been initialized
6	BoostConv_START	When set, converter is in startup mode
5	—	Reserved
4	BoostConv_OV	Output above overvoltage limit when 1, below limit when 0
3	BoostConv_VHI	Output is above vhigh limit when 1, below limit when 0
2	BoostConv_VNOM	Output is above nominal when 1, below nominal when 0
1	BoostConv_VLO	Output is above vlow limit when 1, below limit when 0
0	BoostConv_UV	Output is above undervoltage limit when 1, below limit when 0

**Side Effects:** None

## void BoostConv\_ReadIntStatus(void)

**Description:** This function returns the contents of the boost block interrupt status register.

**Parameters:** None

**Return Value:** uint8 Boost interrupt status register BOOST\_SR2 bit 0: When set, a Boost Output Undervoltage event has occurred.

**Side Effects:** None



## void BoostConv\_Init(void)

**Description:** Initializes or restores the component according to the customizer Configure dialog settings. It is not necessary to call BoostConv\_Init() because the BoostConv\_Start() API calls this function and is the preferred method to begin component operation.

**Parameters:** None

**Return Value:** None

**Side Effects:** All registers will be set to values according to the customizer Configure dialog.

## void BoostConv\_Enable(void)

**Description:** This function enables the boost block when in Active mode. The component is enabled by default. Activates the hardware and begins component operation. It is not necessary to call BoostConv\_Enable() because the BoostConv\_Start() API calls this function, which is the preferred method to begin component operation.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

## void BoostConv\_Disable(void)

**Description:** This function disables the boost block.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

## PSoC 3 API

### void BoostConv\_EnableAutoThump(void)

**Description:** This function enables automatic thump mode. The AutoThump mode is available only when the boost block is in the Standby mode. The switching frequency clock source for the boost block must be set to the 32-kHz external clock. In this mode, standby boost operation is accomplished by generating a boost switch pulse on each edge of the switching clock when the output voltage is below the selected value.

**Parameters:** None

**Return Value:** None

**Side Effects:** None



**void BoostConv\_DisableAutoThump(void)****Description:** This function disables automatic thump mode.**Parameters:** None**Return Value:** None**Side Effects:** None**void BoostConv\_SelExtClk(uint8 source)****Description:** This function sets the source of 32-kHz frequency: the chip's ECO-32kHz or ILO-32kHz.**Parameters:** uint8 source: The source of 32-kHz frequency.

Name	Description
BoostConv__EXTCLK_ECO	Set chip ECO-32kHz as the source of 32-kHz frequency
BoostConv__EXTCLK_ILO	Set chip ILO-32kHz as the source of 32-kHz frequency

**Return Value:** None**Side Effects:** None**void BoostConv\_SelFreq(uint8 frequency)****Description:** This function sets the switching frequency to one of two possible values. 400kHz (which is generated internal to the Boost Converter block with a dedicated oscillator) or 32kHz (which comes from the chips ECO-32kHz or ILO-32kHz). The 32kHz frequency is only applicable for PSoC 3.**Parameters:** uint8 switch\_freq: The desired switching frequency.

Switch Frequency	Notes
BoostConv__SWITCH_FREQ_400KHZ	Generated internal to the boost converter block with a dedicated oscillator
BoostConv__SWITCH_FREQ_32KHZ	Comes from the ECO-32kHz or ILO-32kHz

**Return Value:** None**Side Effects:** None

## MISRA Compliance

This section describes the MISRA-C:2004 compliance and deviations for the component. There are two types of deviations defined:

- project deviations – deviations that are applicable for all PSoC Creator components
- specific deviations – deviations that are applicable only for this component

This section provides information on component-specific deviations. Project deviations are described in the MISRA Compliance section of the *System Reference Guide* along with information on the MISRA compliance verification environment.

The Boost Converter component does not have any specific deviations.

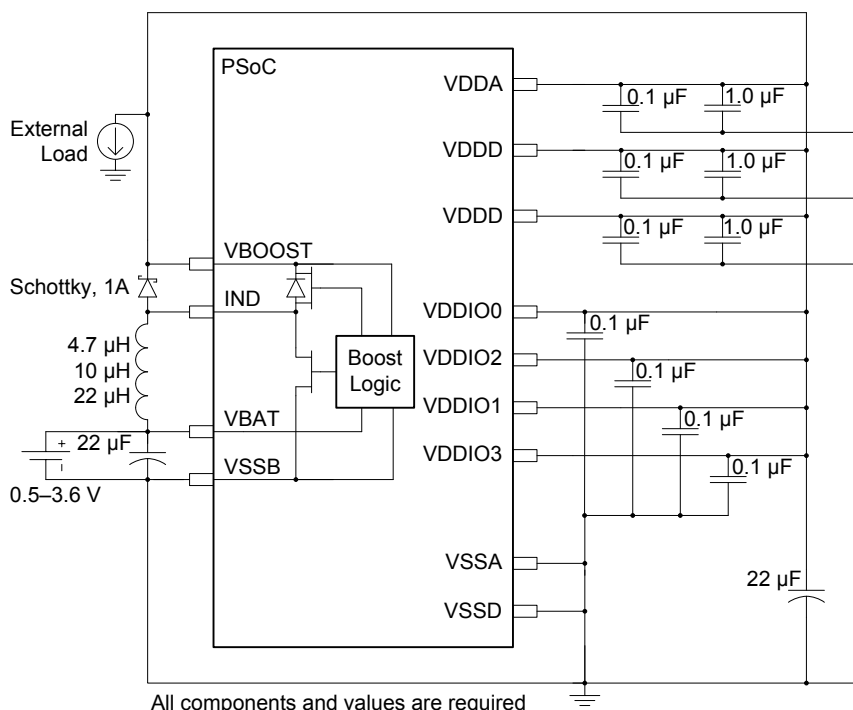
## Sample Firmware Source Code

PSoC Creator provides many example projects that include schematics and example code in the Find Example Project dialog. For component-specific examples, open the dialog from the Component Catalog or an instance of the component in a schematic. For general examples, open the dialog from the Start Page or **File** menu. As needed, use the **Filter Options** in the dialog to narrow the list of projects available to select.

Refer to the “Find Example Project” topic in the PSoC Creator Help for more information.

## Functional Description

**Figure 1. Application for Boost Converter**



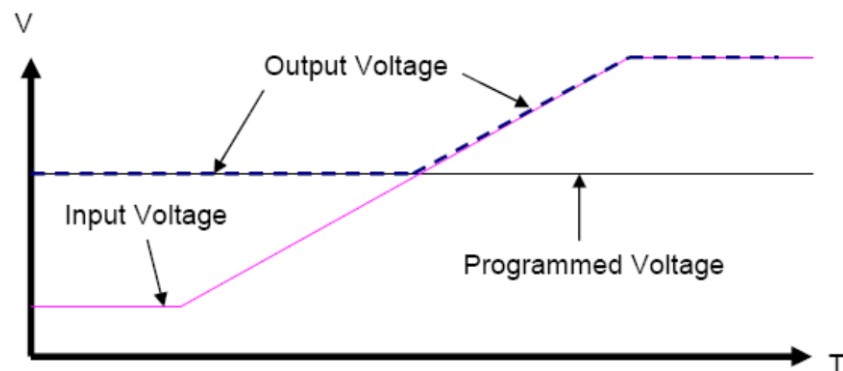
The boost block circuit is enabled by default to support scenarios in which processor startup is powered by the  $V_{\text{BOOST}}$  voltage. The boost block is configured for Active mode with an output voltage of 1.9 V by default. When a BoostConv component is placed in a project, it provides access to the configuration registers for the boost hardware block. The BoostConv\_Start() function configures the BoostConv component with the settings made in the component configuration dialog.

Boost block registers are powered from the  $V_{\text{BOOST}}$  supply. The  $V_{\text{BOOST}}$  output cannot be allowed to fall below 1.4 V. You can expect to lose register contents if  $V_{\text{BOOST}}$  is allowed to fall below 1.4 V. The firmware must reload these registers if such these conditions occur.

The boost converter hardware uses the fixed-function pins on the chip shown in the schematic above. These signals are not shown on the BoostConv component.

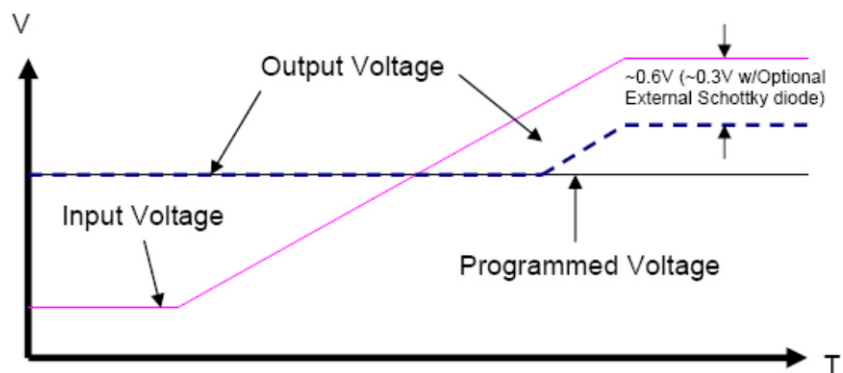
## Operation with Input Voltage Greater Than Programmed Output Voltage

When Control register 2 (BOOST\_CR2): Bit 1 (eqoff) = 0, the output voltage will track the input voltage when the input is greater than the programmed output voltage. This is shown below:



- Output Voltage = Programmed voltage when Input < Programmed
- Output Voltage = Input voltage when Input > Programmed

When Control register 2 (BOOST\_CR2): Bit 1 (eqoff) = 1, the output voltage does not track until either the optional external Schottky diode or inherent internal silicon diode between the inductor pin and output are forward biased. The effect of this is that the output voltage tracks input with a diode drop, as shown below:



- Output Voltage = Programmed voltage when Input < Programmed
- Output Voltage = Input voltage – diode drop when Input > Programmed + diode drop

For Boost modes and Chip and Boost Power Modes Compatibility details please refer to Boost Converter section of PSoC Family Datasheet.

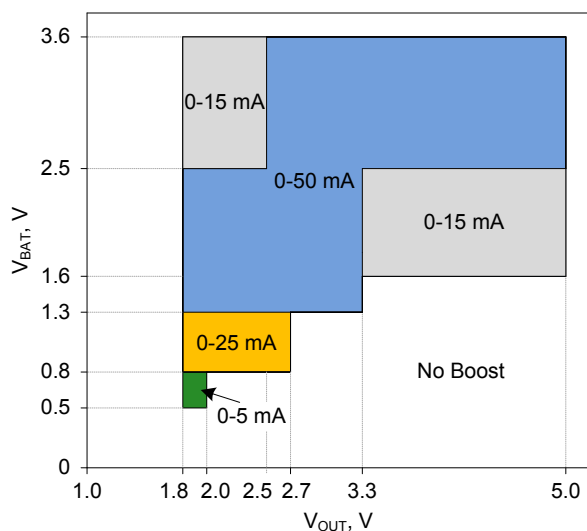
## Design Guide

The Boost Converter component supports features that are not supported by the silicon. Refer to the Component Errata section. This design guide must be followed in order to meet the characterization specification.

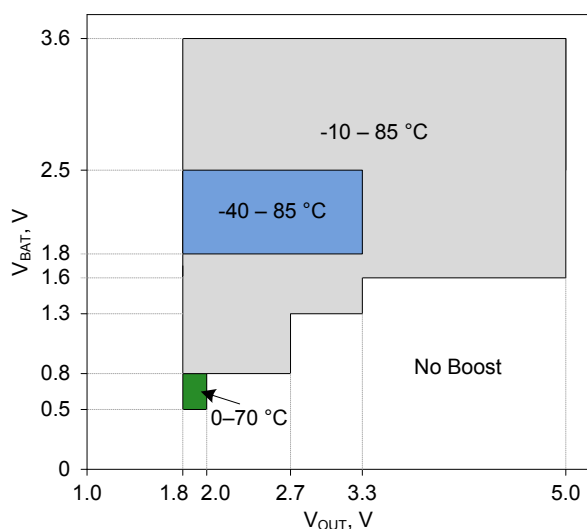
## Limitations

1. The valid output voltage range must be between 1.8 V to 5.0 V. Refer to the highlighted sections in the figure below for the compatible Vbat and Vout values.

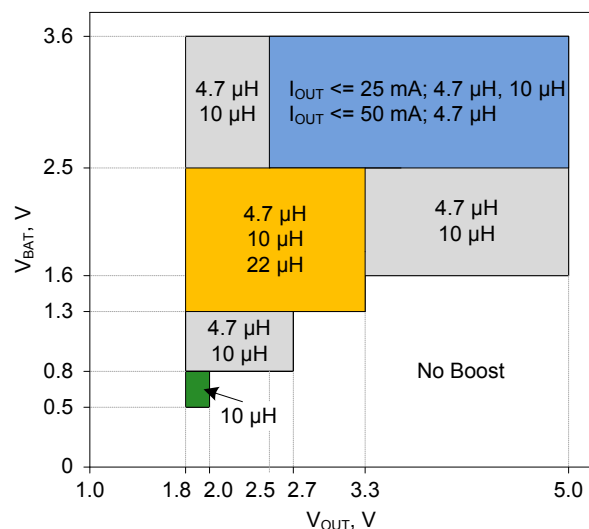
Maximum output current is 50 mA. Lower limits apply to some Vbat/Vout voltage combinations.



2. There are temperature range restrictions based on the boost Vbat to Vout voltages. Refer to the figure below.



3. The allowed inductor values depend on the boost  $V_{BAT}$  to  $V_{OUT}$  voltages. Refer to the figure below.



4. A Schottky diode is always required for all output voltages.
5. The switching frequency can only be 400 kHz. Do not use other switching frequencies.
6. The **Disable auto battery connect to output when  $V_{in} = V_{sel}$**  feature is not supported and should not be used.

## Boost Design Process

Correct operation of the boost converter requires specific component values determined for each design's unique operating conditions. The  $C_{BAT}$  capacitor, Inductor, Schottky diode, and  $C_{BOOST}$  capacitor components are required with the values specified in the Device datasheet. The only variable component value is the inductor  $L_{BOOST}$  which is primarily sized for correct operation of the boost across operating conditions and secondarily for efficiency. Additional operating region constraints exist for  $V_{OUT}$ ,  $V_{BAT}$ ,  $I_{OUT}$ , and  $T_A$ .

The following steps must be followed to determine boost converter operating parameters and  $L_{BOOST}$  value.

1. Choose desired  $V_{BAT}$ ,  $V_{OUT}$ ,  $T_A$ , and  $I_{OUT}$  operating condition ranges for the application.
2. Determine if  $V_{BAT}$  and  $V_{OUT}$  ranges, ambient temperature ( $T_A$ ) range, and desired output current ( $I_{OUT}$ ) range meet the limitation requirements as outlined in **Limitations** section.
3. Find the allowed inductor values based on inductor limitation as shown in the figure in **Limitations** section.
4. Based on the allowed inductor values, inductor dimensions, inductor cost, boost efficiency, and  $V_{RIPPLE}$  choose the optimum inductor value for the system. Boost efficiency and  $V_{RIPPLE}$  typical values are provided in the **Efficiency vs  $V_{BAT}$**  and  **$V_{RIPPLE}$  vs  $V_{BAT}$**  charts, available in the boost electrical specification section of the device datasheet. In general, if high efficiency and low  $V_{RIPPLE}$  are most important, then the highest allowed



inductor value should be used. If low inductor cost or small inductor size are most important, then one of the smaller allowed inductor values should be used. If the allowed inductor(s) efficiency,  $V_{RIPPLE}$ , cost or dimensions are not acceptable for the application than an external boost regulator should be used.

## Resources

The BoostConv component uses the dedicated boost converter hardware block in the silicon.

## API Memory Usage

The component memory usage varies significantly, depending on the compiler, device, number of APIs used and component configuration. The following table provides the memory usage for all APIs available in the given component configuration.

The measurements have been done with the associated compiler configured in Release mode with optimization set for Size. For a specific design the map file generated by the compiler can be analyzed to determine the memory usage.

Configuration	PSoC 3 (Keil_PK51)		PSoC 5LP (GCC)	
	Flash Bytes	SRAM Bytes	Flash Bytes	SRAM Bytes
Default	861	3	472	3

## PSoC 3 DC and AC Electrical Characteristics

Unless otherwise specified in the tables below, operating conditions are:  $V_{BAT} = 2.4\text{ V}$ ,  $V_{OUT} = 2.7\text{ V}$ ,  $I_{OUT} = 40\text{ mA}$ ,  $F_{SW} = 400\text{ kHz}$ ,  $L_{BOOST} = 10\text{ }\mu\text{H}$ ,  $C_{BOOST} = 22\text{ }\mu\text{F} \parallel 0.1\text{ }\mu\text{F}$ .

### DC SpecificationsBoostConv\_datasheet.doc

Parameter	Description	Conditions	Min	Typ	Max	Units
$V_{BAT}$	Input voltage, includes startup voltage	$T = -35\text{ }^{\circ}\text{C}$ to $+65\text{ }^{\circ}\text{C}$	0.5	–	3.6	V
		Over entire temperature range	0.68	–	3.6	V
$I_{OUT}$	Load current , steady	Boost active mode, $V_{BAT} = 1.6\text{--}3.6\text{ V}$ , $V_{OUT} = 1.6\text{--}3.6\text{ V}$ , internal diode	–	–	75	mA



Parameter	Description	Conditions	Min	Typ	Max	Units
	state <sup>[1, 2]</sup>	Boost active mode, $V_{BAT} = 1.6\text{--}3.6\text{ V}$ , $V_{OUT} = 3.6\text{--}5.0\text{ V}$ , external diode	–	–	50	mA
		Boost active mode, $V_{BAT} = 0.8\text{--}1.6\text{ V}$ , $V_{OUT} = 1.6\text{--}3.6\text{ V}$ , internal diode	–	–	30	mA
		Boost active mode, $V_{BAT} = 0.8\text{--}1.6\text{ V}$ , $V_{OUT} = 3.6\text{--}5.0\text{ V}$ , external diode	–	–	20	mA
		Boost active mode, $V_{BAT} = 0.5\text{--}0.8\text{ V}$ , $V_{OUT} = 1.6\text{--}3.6\text{ V}$ , internal diode	–	–	15	mA
$I_{LPK}$	Inductor peak current		–	–	700	mA
$I_Q$	Quiescent current	Boost active mode	–	250	–	μA
		Boost standby mode, $I_{OUT} < 1\text{ μA}$	–	25	–	μA
$V_{OUT}$	Boost output voltage range <sup>[3, 4]</sup>					
	1.8 V		1.71	1.80	1.89	V
	1.9 V		1.81	1.90	2.00	V
	2.0 V		1.90	2.00	2.10	V
	2.4 V		2.28	2.40	2.52	V
	2.7 V		2.57	2.70	2.84	V
	3.0 V		2.85	3.00	3.15	V
	3.3 V		3.14	3.30	3.47	V
	3.6 V		3.42	3.60	3.78	V
	5.0 V	External diode required	4.75	5.00	5.25	V
$Reg_{LOAD}$	Load regulation		–	–	3.8	%
$Reg_{LINE}$	Line regulation		–	–	4.1	%

1. For output voltages above 3.6 V, an external diode is required.
2. Maximum output current applies for output voltages  $\leq 4\times$  input voltage.
3. Based on device characterization (Not production tested).
4. At boost frequency at 400 kHz,  $V_{OUT}$  is limited to  $4 \times V_{BAT}$ .

## AC SpecificationsBoostConv\_datasheet.doc

Parameter	Description	Conditions	Min	Typ	Max	Units
$V_{RIPPLE}$	Ripple voltage	$0.5\text{ V} < V_{BAT} < 1.7\text{ V}$ , $V_{OUT} = 1.8\text{ V}$ ,	–	–	100	mV



	(peak-to-peak)	$F_{SW} = 400 \text{ kHz}$ , $I_{OUT} = 10 \text{ mA}$				
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## Recommended External Components for Boost Circuit

Parameter	Description	Conditions	Min	Typ	Max	Units
$L_{BOOST}$	Boost inductor		–	10	–	$\mu\text{H}$
$C_{BOOST}$	Filter capacitor <sup>[5]</sup>		10	22	47	$\mu\text{F}$
$I_F$	External Schottky diode average forward current	External Schottky diode is required for $V_{OUT} > 3.6 \text{ V}$	1	–	–	A
$V_R$			20	–	–	V

## PSoC 5LP DC and AC Electrical Characteristics

Unless otherwise specified in the tables below, operating conditions are:  $L_{BOOST} = 10 \mu\text{H}$ ,  $C_{BOOST} = 22 \mu\text{F} \parallel 0.1 \mu\text{F}$ ,  $2 < V_{bat}:V_{out} \leq 4$ .

### DC SpecificationsBoostConv\_datasheet.doc

Parameter	Description	Conditions	Min	Typ	Max	Units
$V_{BAT}$	Input voltage, includes startup voltage <sup>[6]</sup>	$I_{out} < 7.5 \text{ mA}$ , $V_{out} = 1.8\text{V}$ nominal	0.5	–	0.6	V
		External Diode Required if $V_{bat} < 0.9 \text{ V}$	0.6	–	3.6	V
$I_{OUT}$	Load current, steady state <sup>[6, 7]</sup>	$V_{BAT} = 1.6\text{--}3.6 \text{ V}$ , $V_{OUT} = 1.6\text{--}3.6 \text{ V}$	–	–	75	mA
		$V_{BAT} = 1.6\text{--}3.6 \text{ V}$ , $V_{OUT} = 3.6\text{--}5.0 \text{ V}$ , external diode	–	–	50	mA
		$V_{BAT} = 0.5\text{--}1.6 \text{ V}$ , $V_{OUT} = 1.6\text{--}3.6 \text{ V}$	–	–	15	mA
		$V_{BAT} = 0.5\text{--}1.6 \text{ V}$ , $V_{OUT} = 3.6\text{--}5.0 \text{ V}$ , external diode	–	–	15	mA
$I_{LPK}$	Inductor peak current		–	–	700	mA
$I_Q$	Quiescent current	Boost active mode	–	250	–	$\mu\text{A}$
		Boost sleep mode, $I_{OUT} < 1 \text{ A}$	–	25	–	$\mu\text{A}$

5. Based on device characterization (Not production tested).

6. For  $V_{BAT} \leq 0.9 \text{ V}$  or  $V_{OUT} \geq 3.6 \text{ V}$ , an external diode is required.

7. If powering the PSoC from boost with  $V_{bat} = 0.5 \text{ V}$ , the IMO must be 3 MHz at startup.



Parameter	Description	Conditions	Min	Typ	Max	Units
V <sub>OUT</sub>	Boost output voltage range <sup>[8]</sup>	1.8 V nominal	1.71	1.80	1.89	V
		1.9 V nominal	1.81	1.90	2.00	V
		2.0 V nominal	1.90	2.00	2.10	V
		2.4 V nominal	2.28	2.40	2.52	V
		2.7 V nominal	2.57	2.70	2.84	V
		3.0 V nominal	2.85	3.00	3.15	V
		3.3 V nominal	3.14	3.30	3.47	V
		3.6 V nominal, External diode required	3.42	3.60	3.78	V
		5.0 V nominal, External diode required	4.75	5.00	5.25	V
V <sub>OUT</sub> : V <sub>BAT</sub>	Ratio of V <sub>OUT</sub> to V <sub>BAT</sub>		–	–	4	ratio
Reg <sub>LOAD</sub>	Load regulation		–	–	5	%
Reg <sub>LINE</sub>	Line regulation		–	–	5	%

8. Based on device characterization (Not production tested).

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Parameter	Description	Conditions	Min	Typ	Max	Units
V <sub>RIPPLE</sub>	Ripple voltage (peak-to-peak)	L <sub>BOOST</sub> = 10 µH, C <sub>BOOST</sub> = 22 µF    0.1 µF, 2 < Vbat:Vout <= 4, Iout = 10 mA	–	–	100	mV

## Recommended External Components for Boost Circuit

Parameter	Description	Conditions	Min	Typ	Max	Units
L <sub>BOOST</sub>	Boost inductor		4.7	10	22	µH
C <sub>BOOST</sub>	Filter capacitor <sup>[9]</sup>	Lboost = 4.7 uH	–	10	–	uF
		Lboost = 10 uH	–	22	–	uF
		Lboost = 22 uH	–	22	–	uF

9. Based on device characterization (Not production tested).



$I_F$	External Schottky diode average forward current		1	–	–	A
$V_R$			20	–	–	V

## Component Errata

This section lists known problems with the component.

Cypress ID	Component Version	Problem	Workaround
207556	All	The boost converter logic requires that the Vbat to Vout be specific values. Ratios outside of these values may not meet characterized performance. The external inductor must be chosen based on the Vbat and Vout ranges.	Refer to the Design guide in the Functional Description section or the boost section of the device datasheet.

## Component Changes

This section lists the major changes in the component from the previous version.

Version	Description of Changes	Reason for Changes / Impact
5.0.a	Added Component Errata section.	Silicon characterization limitations were discovered. The component datasheet is updated to give guidelines on choosing the correct operating values.
5.0	Removed PSoC 5 support.	
	Added setting of internal 400KHz frequency for Active mode to BoostConv_SetMode() function.	Vboost doesn't reach configured value when boost mode changes from STANDBY to ACTIVE.
	Disabled selecting the value of "Switching frequency" parameter to 32KHz for PSoC 3 and PSoC 5LP devices.	Boost always starts operation in Active mode. Only 400KHz switching frequency should be used with Active mode.
4.0	Added MISRA Compliance section.	The component does not have any specific deviations.
	Added Sleep mode for PSoC 5LP. Updated API functions description.	Added Sleep mode for PSoC 5LP in silicon.
	Updated Characterization data for PSoC 5LP.	To keep Characterization data up-to-date.
3.0	Updated portion of code for Disabling AUTO_BATTERY in BoostConv_Init() function.	To fix incorrect configuration of "eqoff" bit.
	Updated BoostConv_SelfFreq() API. Updated defines	Removed the unsupported switching



Version	Description of Changes	Reason for Changes / Impact
	generation for switching frequencies. Updated debug window file.	frequencies 100 KHz and 2MHz.
	Removed external reference bit selection in BoostConv_Init() API.	This is handled by Creator generated code that sets this bit at device startup and resets before the device goes to sleep.
	Updated Characterization tables for PSoC 3. Added Characterization tables for PSoC 5LP.	To keep Characterization data up-to-date.
	Updated BoostConv_Init(), BoostConv_Start(), BoostConv_Stop(), BoostConv_Enable(), BoostConv_Disable(), BoostConv_SetMode(), BoostConv_SelVoltage(), BoostConv_SelfFreq(), BoostConv_EnableAutoThump(), BoostConv_DisableAutoThump(), BoostConv_ManualThump(), BoostConv_ReadStatus() functions to have two consecutive reads for registers that reside in the Boost hardware.	Accessing the Boost hardware registers in read mode has speed limitations and requires two consecutive reads of any register that resides in the Boost hardware to guarantee the correct result is returned.
2.10	Added PSoC 5LP device support.	
	Added all BoostConv APIs with CYREENTRANT keyword when they are included in the .cyre file.	Not all APIs are truly reentrant. Comments in the component API source files indicate which functions are candidates.  This change is required to eliminate compiler warnings for functions that are not reentrant and used in a safe way: protected from concurrent calls by flags or Critical Sections.
2.0.a	Datasheet corrections.	
2.0	Removed reference to PSoC 5 support.	Component is not supported by PSoC 5.
	Updated BoostConv_Start() and BoostConv_Stop() functions with set/restore mode and voltage.	An expected use case is using Boost to power off-chip devices, so firmware is expected to start/stop the boost.
	Added new parameter "Disable auto battery connect to output when $V_{in} = V_{sel}$ ." Updated BoostConv_Init() function with disabling auto battery connect to output when $V_{in} = V_{SEL}$ .	To allow the user to configure whether the output voltage should track the input voltage when $V_{BAT} > V_{BOOST}$ , or only track after the diode is forward biased (so the output tracks the input - diode drop).
1.50.a	Added autothump support note to Features in datasheet.	Silicon bug in PSoC 3 ES2 and PSoC 5.
	Added information to the component that advertizes its compatibility with silicon revisions.	The tool reports an error/warning if the component is used on incompatible silicon. If this happens, update to a revision that supports your target device.
	Added characterization data to datasheet.	
	Removed reference to sleep mode from datasheet.	Component doesn't support sleep mode.



Version	Description of Changes	Reason for Changes / Impact
	Minor datasheet edits and updates.	
1.50	Added support of PSoC 3 Production silicon. Three API functions have been added: void BoostConv_EnableInt(void); void BoostConv_DisableInt(void); uint8 BoostConv_ReadIntStatus(void);	Boost Converter supports generation of undervoltage signal.
	API function has been added: void BoostConv_SelExtClk(uint8);	To support selection of external switching clock sources of Boost Converter: ILO or ECO.
	Added BoostConv_Init() function.	To comply with corporate standard and provide an API to initialize/restore the component without starting it.

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