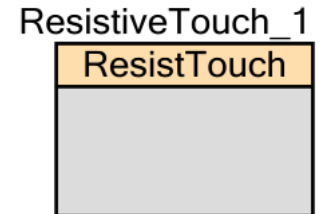


Resistive Touch (ResistiveTouch)

2.0

Features

- Supports 4-wire resistive touchscreen interface
- Supports the Delta Sigma Converter for both the PSoC 3 and PSoC 5LP devices
- Supports the ADC Successive Approximation Register for PSoC 5LP devices



General Description

This resistive touchscreen component is used to interface with a 4-wire resistive touch screen. The component provides a method to integrate and configure the resistive touch elements of a touchscreen with the emWin Graphics library. It integrates hardware-dependent functions that are called by the touchscreen driver supplied with emWin when polling the touch panel.

This component is designed to work with the SEGGER emWin graphics library. This graphics library is provided by Cypress to use with Cypress devices and is available on the Cypress website at www.cypress.com/go/comp_emWin. This graphics library provides a full-featured set of graphics functions for drawing and rendering text and images.

When to Use a ResistiveTouch

Use a ResistiveTouch component where low cost and simple interface electronics are required.

Input/Output Connections

This section describes the various input and output connections for the ResistiveTouch.

xm – Digital Input / Output

Signal x– from axis X of the resistive touch panel (active low).

xp – Analog / Digital Output

Signal x+ from axis X of the resistive touch panel (active high).

Resistive Touch (ResistiveTouch)

ym – Digital Input / Output

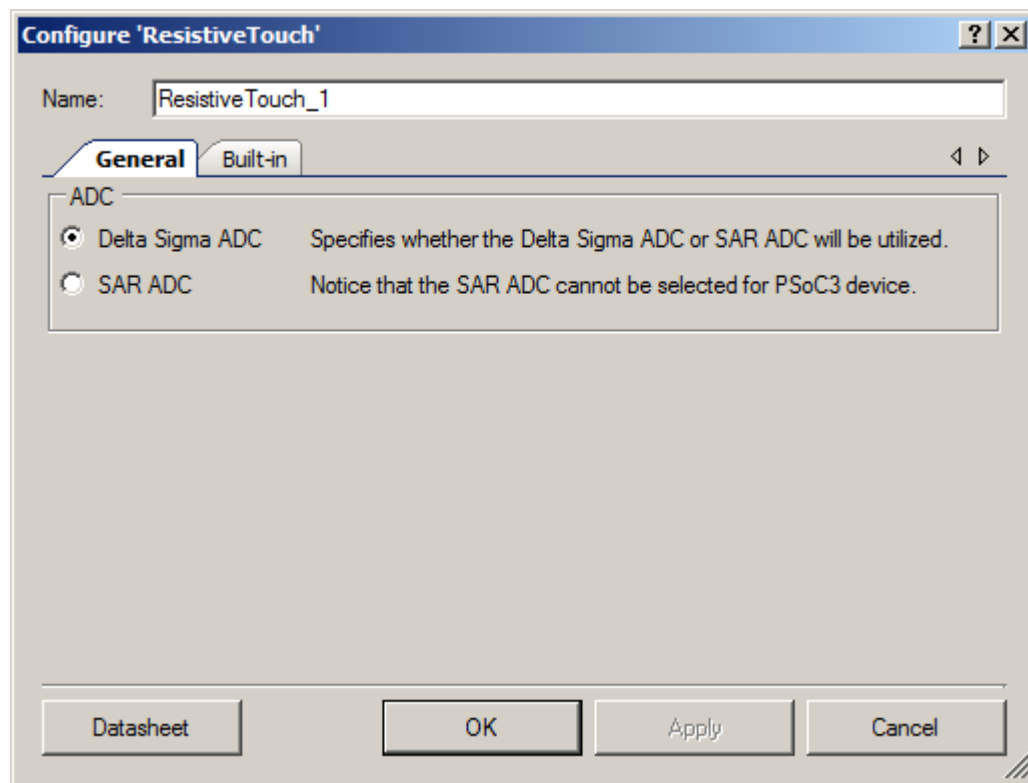
Signal y– from axis Y of the resistive touch panel (active low).

yp – Analog / Digital Output

Signal y+ from axis Y of the resistive touch panel (active high).

Component Parameters

Drag a ResistiveTouch component onto your design and double click it to open the **Configure** dialog.



ADC

The **ADC** parameter determines which ADC to use. For PSoC 3 devices, select the **Delta Sigma ADC** option.

Application Programming Interface

Application Programming Interface (API) routines allow you to configure the component using software. The following table lists and describes the interface to each function. The subsequent sections cover each function in more detail.

By default, PSoC Creator assigns the instance name “ResistiveTouch_1” to the first instance of a component in a given design. You can rename it to any unique value that follows the syntactic rules for identifiers. The instance name becomes the prefix of every global function name, variable, and constant symbol. For readability, the instance name used in the following table is “ResistiveTouch”.

Functions

Function	Description
ResistiveTouch_Start()	Calls the ResistiveTouch_Init() and ResistiveTouch_Enable() APIs.
ResistiveTouch_Stop()	Stops the ADC and the AMux component.
ResistiveTouch_Init()	Calls the Init() functions of the ADC and AMux components.
ResistiveTouch_Enable()	Enables the component.
ResistiveTouch_ActivateY()	Configures the pins to measure the Y-axis.
ResistiveTouch_ActivateX()	Configures the pins to measure the X-axis.
ResistiveTouch_TouchDetect()	Detects a touch on the screen.
ResistiveTouch_Measure()	Returns the result of the ADC conversion.
ResistiveTouch_SaveConfig()	Saves the configuration of the ADC.
ResistiveTouch_Sleep()	Prepares the component for entering the low power mode.
ResistiveTouch_RestoreConfig()	Restores the configuration of the ADC.
ResistiveTouch_Wakeup()	Restores the component after waking up from the low power mode.

void ResistiveTouch_Start(void)

Description: Sets the ResistiveTouch_initVar variable, calls the ResistiveTouch_Init() function, and then calls the ResistiveTouch_Enable() function.

Parameters: None

Return Value: None

Side Effects: None



void ResistiveTouch_Stop(void)

Description: Calls the Stop() functions of the ADC and the AMux components.

Parameters: None

Return Value: None

Side Effects: None

void ResistiveTouch_Init(void)

Description: Calls the Init() functions of the ADC and AMux components.

Parameters: None

Return Value: None

Side Effects: None

void ResistiveTouch_Enable(void)

Description: Calls the Enable() function of the ADC component.

Parameters: None

Return Value: None

Side Effects: None

void ResistiveTouch_ActivateX(void)

Description: Configures the pins to measure the X-axis.

Parameters: None

Return Value: None

Side Effects: None

void ResistiveTouch_ActivateY(void)

Description: Configures the pins to measure the Y-axis.

Parameters: None

Return Value: None

Side Effects: None

int16 ResistiveTouch_Measure(void)**Description:** Returns the result of the ADC conversion.**Parameters:** None**Return Value:** int16: The result of the ADC conversion**Side Effects:** None**uint8 ResistiveTouch_TouchDetect(void)****Description:** Detects a touch on the screen.**Parameters:** None**Return Value:** uint8: The touch state
0 – untouched
1 – touched**Side Effects:** None**void ResistiveTouch_SaveConfig(void)****Description:** Saves the configuration of the ADC.**Parameters:** None**Return Value:** None**Side Effects:** None**void ResistiveTouch_RestoreConfig(void)****Description:** Restores the configuration of the ADC.**Parameters:** None**Return Value:** None**Side Effects:** None**void ResistiveTouch_Sleep(void)****Description:** Prepares the component for entering the low power mode.**Parameters:** None**Return Value:** None**Side Effects:** None

void ResistiveTouch Wakeup(void)

Description: Restores the component after waking up from the low power mode.

Parameters: None

Return Value: None

Side Effects: None

Global Variables

Variable	Description
ResistiveTouch_initVar	<p>Indicates whether the ResistiveTouch has been initialized. The variable is initialized to 0 and set to 1 the first time ResistiveTouch_Start() is called. This allows the component to restart without reinitialization after the first call to the ResistiveTouch_Start() routine.</p> <p>If reinitialization of the component is required, then the ResistiveTouch_Init() function can be called before the ResistiveTouch_Start() or ResistiveTouch_Enable() function.</p>

Sample Firmware Source Code

PSoC Creator provides many example projects that include schematics and example code in the Find Example Project dialog. For component-specific examples, open the dialog from the Component Catalog or an instance of the component in a schematic. For general examples, open the dialog from the Start Page or **File** menu. As needed, use the **Filter Options** in the dialog to narrow the list of projects available to select.

Refer to the “Find Example Project” topic in the PSoC Creator Help for more information.

MISRA Compliance

This section describes the MISRA-C:2004 compliance and deviations for the component. There are two types of deviations defined:

- project deviations – deviations that are applicable for all PSoC Creator components
- specific deviations – deviations that are applicable only for this component

This section provides information on component-specific deviations. Project deviations are described in the MISRA Compliance section of the *System Reference Guide* along with information on the MISRA compliance verification environment.

The Resistive Touch component does not have any specific deviations.

This component has the following embedded components: Pins, Delta Sigma ADC, SAR ADC.

Refer to the corresponding component datasheets for information on their MISRA compliance and specific deviations.

API Memory Usage

The component memory usage varies significantly, depending on the compiler, device, number of APIs used and component configuration. The following table provides the memory usage for all APIs available in the given component configuration.

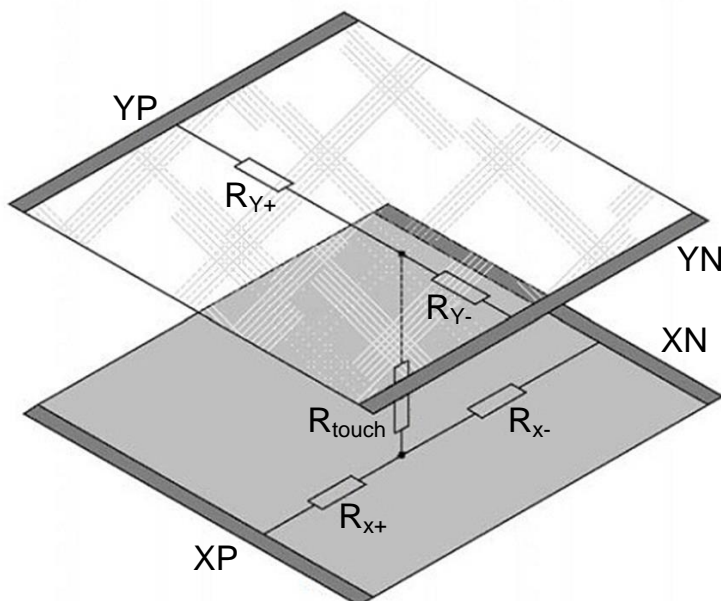
The measurements have been done with the associated compiler configured in Release mode with optimization set for Size. For a specific design the map file generated by the compiler can be analyzed to determine the memory usage.

Configuration	PSoC 3 (Keil_PK51)		PSoC 5LP (GCC)	
	Flash Bytes	SRAM Bytes	Flash Bytes	SRAM Bytes
Delta Sigma ADC	2341	22	2052	24
SAR ADC	N/A	N/A	982	15

Functional Description

This component provides a 4-wire resistive touch screen interface to read touchscreen coordinates and measure screen resistance. This component provides access to the touchscreen functionality of the SEGGER emWin graphics library for translation of resistance to screen coordinates.

The diagram that follows shows the schematic of a 4-wire touchscreen when pressure is applied.



The point of contact “divides” each layer in a series resistor network with two resistors (see the diagram), and a connecting resistor between the two layers. By measuring the voltage at this point, you get information about the position of the contact point orthogonal to the voltage gradient. To get a complete set of coordinates, you must apply the voltage gradient once in the vertical and then in the horizontal direction. First, you must apply a supply voltage applied to one layer and perform a measurement of the voltage across the other layer; next connect the supply to the other layer and measure the opposite layer voltage. When in touch mode, one of the lines is connected to detect touch activity. The following table defines the configuration of the pins while measuring the coordinates or touch.

	XP	XM	YP	YM
touch	Res Pullup	Analog Hi-Z	Analog Hi-Z	Strong Drive
X-Coordinate	Strong Drive	Strong Drive	Analog Hi-Z	Analog Hi-Z
Y-Coordinate	Analog Hi-Z	Analog Hi-Z	Strong Drive	Strong Drive

When a ResistiveTouch component is placed onto the project schematic, it is important that I/O port designations be set for xm, xp, ym, yp pins. Designation of these pins is not done on the symbol or schematic; instead, it is done in the Pins tab of the Design-Wide Resources window.

Resources

Depending on the configuration, the ResistiveTouch component uses the Delta Sigma ADC or SAR ADC component, and four pins.

DC and AC Electrical Characteristics

Depending on configuration the resistive touch component uses the Delta Sigma ADC or SAR ADC components. Therefore the resistive touch component is limited by the performance of these components. Refer to the Characteristics section of the associated ADC component datasheet for details.

References

Refer also to the Delta Sigma ADC and SAR ADC component datasheets.

Component Changes

This section lists the major changes in the component from the previous version.

Version	Description of Changes	Reason for Changes / Impact
2.0.b	Minor datasheet edits.	
2.0.a	Updated this datasheet change section to note that the internal clock configuration was modified in the Resistive Touch 2.0 component.	If the SAR ADC is chosen as the clock for the Resistive Touch component, any project that uses the maximum number of digital clocks will fail to build after updating the Resistive Touch component from v1.30 to v2.0. This is because the ResistiveTouch_ADC_SAR_theACLK internal clock uses the Digital domain by default. To resolve this problem, change the clock domain of the ResistiveTouch_ADC_SAR_theACLK internal clock from Digital to Analog in the Design-Wide Resources (<project>.cydwr) file Clocks tab.
2.0	Updated versions of the embedded ADC and pins components to the most current versions.	Out of date components may contain defects or incompatibilities.
	Updated MISRA compliance section.	The component was verified for MISRA compliance.
	Datasheet edits and corrections.	
1.30	Corrected the component changes made in PSoC Creator 3.0 SP1.	Correction of the Component Errata item – Cypress ID 191257.
1.20	Added MISRA Compliance section.	The component was not verified for MISRA compliance.
	Updated for compatibility with PSoC Creator v2.2	
1.10.a	Minor edits and updates to datasheet	
1.10	Added PSoC 5LP support	

© Cypress Semiconductor Corporation, 2015-2017. This document is the property of Cypress Semiconductor Corporation and its subsidiaries, including Spansion LLC ("Cypress"). This document, including any software or firmware included or referenced in this document ("Software"), is owned by Cypress under the intellectual property laws and treaties of the United States and other countries worldwide. Cypress reserves all rights under such laws and treaties and does not, except as specifically stated in this paragraph, grant any license under its patents, copyrights, trademarks, or other intellectual property rights. If the Software is not accompanied by a license agreement and you do not otherwise have a written agreement with Cypress governing the use of the Software, then Cypress hereby grants you a personal, non-exclusive, nontransferable license (without the right to sublicense) (1) under its copyright rights in the Software (a) for Software provided in source code form, to modify and reproduce the Software solely for use with Cypress hardware products, only internally within your organization, and (b) to distribute the Software in binary code form externally to end users (either directly or indirectly through resellers and distributors), solely for use on Cypress hardware product units, and (2) under those claims of Cypress's patents that are infringed by the Software (as provided by Cypress, unmodified) to make, use, distribute, and import the Software solely for use with Cypress hardware products. Any other use, reproduction, modification, translation, or compilation of the Software is prohibited.

TO THE EXTENT PERMITTED BY APPLICABLE LAW, CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS DOCUMENT OR ANY SOFTWARE OR ACCOMPANYING HARDWARE, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. To the extent permitted by applicable law, Cypress reserves the right to make changes to this document without further notice. Cypress does not assume any liability arising out of the application or use of any product or circuit described in this document. Any information provided in this document, including any sample design information or programming code, is provided only for reference purposes. It is the responsibility of the user of this document to properly design, program, and test the functionality and safety of any application made of this information and any resulting product. Cypress products are not designed, intended, or authorized for use as critical components in systems designed or intended for the operation of weapons, weapons systems, nuclear installations, life-support devices or systems, other medical devices or systems (including resuscitation equipment and surgical implants), pollution control or hazardous substances management, or other uses where the failure of the device or system could cause personal injury, death, or property damage ("Unintended Uses"). A critical component is any component of a device or system whose failure to perform can be reasonably expected to cause the failure of the device or system, or to affect its safety or effectiveness. Cypress is not liable, in whole or in part, and you shall and hereby do release Cypress from any claim, damage, or other liability arising from or related to all Unintended Uses of Cypress products. You shall indemnify and hold Cypress harmless from and against all claims, costs, damages, and other liabilities, including claims for personal injury or death, arising from or related to any Unintended Uses of Cypress products.

Cypress, the Cypress logo, Spansion, the Spansion logo, and combinations thereof, WICED, PSoC, CapSense, EZ-USB, F-RAM, and Traveo are trademarks or registered trademarks of Cypress in the United States and other countries. For a more complete list of Cypress trademarks, visit cypress.com. Other names and brands may be claimed as property of their respective owners.

