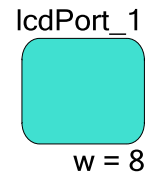


LCD Port

1.50

Features

- Common and segment selection
- Variable widths



General Description

The LCD port provides configuration as either common or segment for the appropriate I/O(s). The port allows for the creation of per-pin aliases, which may be viewed in the PSoC Creator Pin Editor and used in the generated port APIs.

When to use a Port

Use a port when a design needs to generate or access an off-device signal. Use an appropriate port for the type of signal being accessed. LCD ports are optimized for their specific application.

Input/Output Connections

The LCD port does not have any connections.

Component Parameters

Drag a Port onto your design and double-click it to open the Configure dialog.

Basic Tab

Configure 'cy_lcd_port'

Name:

Basic Pins Built-in

Parameter	Value
LCDDrive	PortLCDDrive_Common
PortAliasGroup	
PortAliasRequired	false
PowerOnResetState	PortPORState_InDisabledOutHiZ
SWControl	false
Width	8

Parameter Information

Data Sheet OK Apply Cancel

LCDDrive

This parameter configures all pins in the port as either common or segment drive pins.

PortAliasGroup

Allows pins from ports with PortAliasRequired enabled to be combined in a physical port. Ports for which PortAliasRequired is enabled may only be combined in a physical port if their PortAliasGroup parameters are identical. If empty, the instance name of the port will be used. To avoid name conflicts, the PortAliasGroup should begin with the instance name of the component that owns the ports.

PortAliasRequired

Specifies that the pins of the LCD port should reserve exclusive use of the PS_ALIAS/DR_ALIAS feature of the physical port(s) that contain the pins. When enabled, pins from this port cannot be in the physical port as pins from another port that has PortAliasRequired enabled unless the other port has the same PortAliasGroup.

PowerOnResetState

This parameter specifies the power on reset state of the port. Legal values include:

- **InDisabledOutHiZ** (default) – the input disabled the output is Hi-Z
- InEnabledOut1 – input enabled and the output is a logic one
- InEnabledOut0 – input enabled and the output is a logic zero
- InEnabledOutHiZ – input enabled and the output is Hi-Z

SWControl

Specifies whether the LCD Port will be controlled via software or the built-in hardware of the device.

Width

Specifies the width in bits of the logical port (default is 8).

Pins Tab

The screenshot shows the 'Configure 'cy_lcd_port'' dialog box with the 'Pins' tab selected. The 'Name' field contains 'lcdPort_1'. Below the tabs, there is a table with two columns: 'Pin Number' and 'Alias'. The 'Pin Number' column lists values from 0 to 7. The 'Alias' column is empty. At the bottom of the dialog, there are four buttons: 'Data Sheet', 'OK', 'Apply', and 'Cancel'.

Pin Number	Alias
0	
1	
2	
3	
4	
5	
6	
7	

Alias

This parameter assigns an alias for each pin in the port. The alias is presented in the pin editor and in the generated APIs for the port.

Resources

All ports consume one physical pin, per bit of their width parameter.

Application Programming Interface

Not applicable.

DC and AC Electrical Characteristics

Not applicable.

Component Changes

This section lists the major changes in the component from the previous version.

Version	Description of Changes	Reason for Changes / Impact
1.50.d	Minor datasheet edits and updates	
1.50.c	Minor datasheet edits and updates	
1.50.b	Minor datasheet edits and updates	
1.50.a	Minor datasheet edits and updates	

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