

The image displays several circuit diagrams for the USB-C Receptacle USB2.0 module, organized into functional blocks:

- USB-C Receptacle USB2.0:** Shows the physical connector (J5) with pins A1, A2, B1, B2, B3, B4, B5, B6, B7, B8, and B9. It includes a shield and ground connections. The diagram shows the internal wiring for USB-C signals (USBC-, USBC+, USBC0) and USB2.0 signals (SBU1, SBU2).
- Power Regulation:**
  - CM\_VBAT:** A simple voltage divider circuit using a 5V source and a 100k resistor (R7) to provide a 3.3V output (CM\_VBAT).
  - CM\_3V3:** A voltage divider circuit using a 5V source and a 100k resistor (R7) to provide a 3.3V output (CM\_3V3).
  - CM\_1V8:** A voltage divider circuit using a 5V source and a 100k resistor (R7) to provide a 1.8V output (CM\_1V8).
- Voltage Converter:** A circuit using a PAM2306AYPKE (U2) voltage converter to convert a 5V input to a 3.3V output. It includes a 100k resistor (R7), a 100nF capacitor (C7), and a 100nF capacitor (C8/9).
- Signal Processing:**
  - U1:** An AP2331W-7 (U1) signal processor circuit. It takes a 5V input and provides a 5V output. It includes a 100nF capacitor (C1), a 100nF capacitor (C2), and a 100nF capacitor (C3).
  - U8:** A TPS2561 (U8) USB-to-UART bridge circuit. It takes a 5V input and provides a 5V output. It includes a 100nF capacitor (C9), a 100nF capacitor (C10), and a 100nF capacitor (C11).

[illegible]

The image contains three separate schematic diagrams:

- CPU Fan:** A 3-pin connector symbol. The top pin is labeled '5V' with a voltage source 'XXX-00000'. The bottom pin is labeled 'GND'. The middle pin is labeled 'CPU\_FAN' and '1C1'. A label 'M1 Fan\_3pin' is next to the symbol.
- Mounting Holes:** Four circular symbols representing mounting holes, labeled 'H1 MountingHole', 'H2 MountingHole', 'H3 MountingHole', and 'H4 MountingHole'.
- Power LED:** A vertical circuit. At the top is a '3.3V' source. Below it is a resistor 'R3' with value '4700HM'. Below the resistor is an LED symbol labeled 'D2 LED-RED0603'. The bottom is connected to 'GND'.

The schematic diagram illustrates the HDMI interface circuit for the J3 connector. Key components and connections include:

- Transistors:** Two DMG1012T-7 transistors, Q1 and Q2, are used for signal level shifting. Q1's emitter is connected to GND, and its base is connected to the HDML\_CEC pin. Q2's emitter is connected to GND, and its base is connected to the HDML\_HPDP pin.
- Voltage Detector:** A PDS3V3LSUY (U3) is connected to the HDML\_SDA pin. Its pin 1 is connected to +5V, pin 2 to GND, and pins 3, 4, and 5 to the SDA- pin of the J3 connector.
- Resistors:**
  - R1 and R2 are 100k-0402 resistors connected between +5V and the HDML\_SCL and HDML\_SDA+ pins, respectively.
  - R3 is a 100k-0402 resistor connected between +5V and the HDML\_D0+ pin.
  - R4 is a 100k-0402 resistor connected between the HDML\_CK+ pin and the HDML\_CK- pin.
  - R5 is a 100k-0402 resistor connected between the 1.8V source and the HDML\_HPDP pin.
- Capacitors:** Two capacitors, C1 and C2, are connected between the HDML\_CEC and HDML\_HPDP pins and GND, respectively.
- Connector J3:** The J3 connector has 19 pins. The top 12 pins (D2+, D2-, D1+, D1-, D0+, D0-, HDML\_D0+, HDML\_CK+, HDML\_CK-, CEC, SCL+, SDA-) are connected to the HDMI port. The bottom 7 pins (UTILITY+/HEAC+, HPD+/HEAC-, CS, D0S, D1S, CS, D0S, D1S, GND, GND) are connected to the J3 connector.

Jumper Positions:  
 1-2 = USB BOOT ENABLED  
 2-3 = USB BOOT DISABLED

3.3V

R6 470-0402

EMMC\_DISABLE

DMG1012T-7 Q3

D1 LED-GREEN0603

1.8V

R9 100K-0402

EMMC\_ENABLE

DMG1012T-7 Q4

R8 47K-0603

VUSB\_B

J4 CONN\_03

GND

Figure 1: Pin connections of the ARJ1P11A-MASA-B-A-EMU2 module. The diagram shows the module's pinout on the left, with pins numbered 1 to 32. The right side shows the internal circuitry, including a transformer, diodes, and resistors. The module is connected to a 5V supply, a 1000HM-0603 resistor, and a LAN514 module. The module is also connected to a 5V supply, a 1000HM-0603 resistor, and a LAN514 module. The module is also connected to a 5V supply, a 1000HM-0603 resistor, and a LAN514 module.

Size: A2	Date: 2023-07-11	Rev: 1.1
KiCad E.D.A. kicad 7.0.6		Id: 1/1