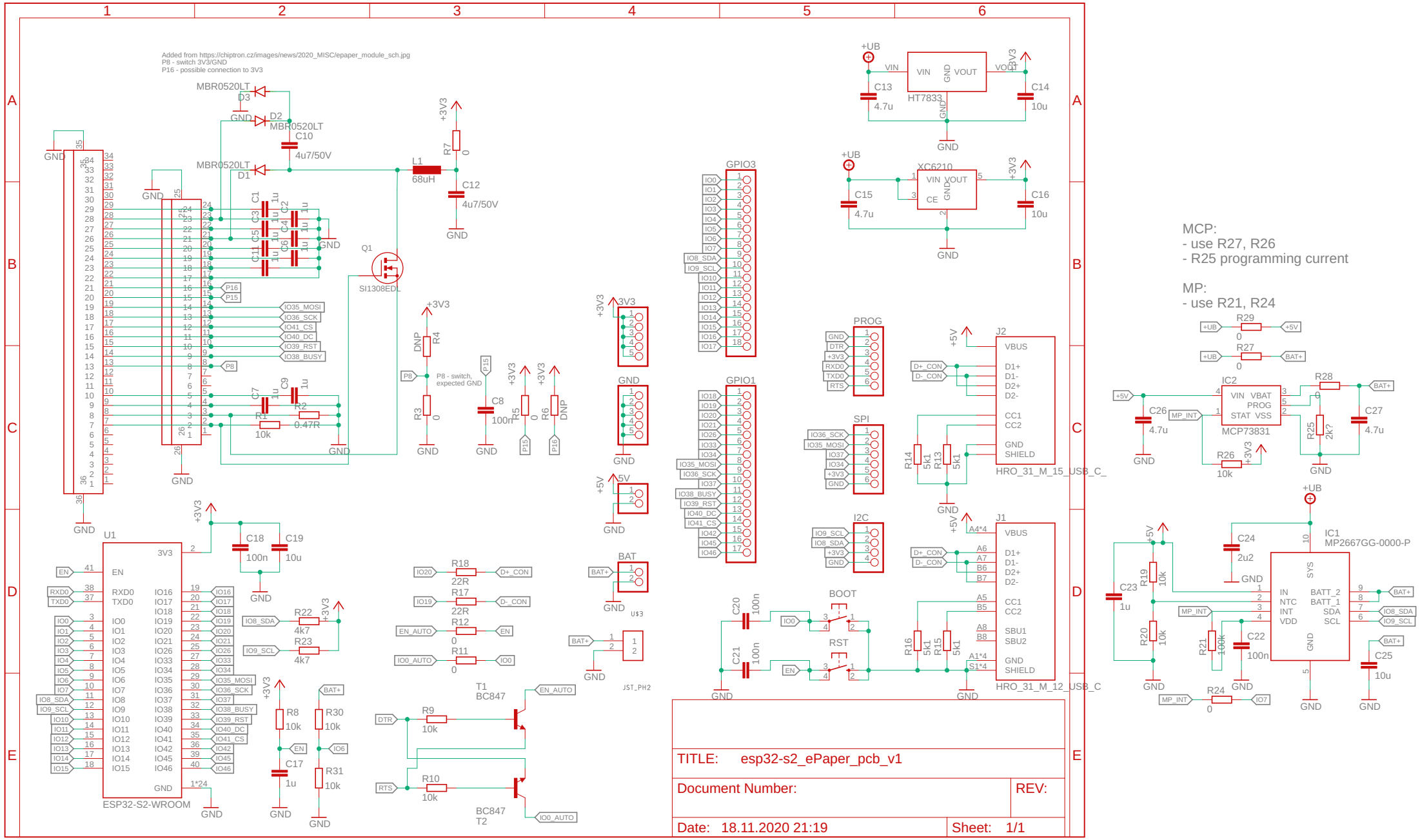
Added from https://chiptron.cz/images/news/2020_MISC/epaper_module_sch.jpg
P8 - switch 3V3/GND
P16 - possible connection to 3V3
MBR0520LT
D3
D2
D1
C10
4u7/50V
L1
68uH
R7
C12
4u7/50V
Q1
SI1308EDL
P8 - switch, expected GND
DNP
R4
R3
C8
100n
R5
R6
U1
ESP32-S2-WROOM
C18
100n
C19
10u
R22
4k7
R23
4k7
R8
10k
R30
10k
R31
10k
C17
1u
R18
22R
R17
22R
R12
0
R11
0
T1
BC847
T2
BC847
R9
10k
R10
10k
BAT
JST_PH2
GPIO3
GPIO1
PROG
SPI
I2C
BOOT
RST
C20
100n
C21
100n
R16
5k1
R15
5k1
R14
5k1
R13
5k1
J2
HRO_31_M_15_USB_C_
J1
HRO_31_M_12_USB_C
+UB
VIN
VOUT
HT7833
C13
4.7u
C14
10u
XC6210
CE
C15
4.7u
C16
10u
MCP:
- use R27, R26
- R25 programming current
MP:
- use R21, R24
R29
0
R27
0
IC2
MCP73831
R28
R25
2k7
R26
10k
C26
4.7u
C27
4.7u
IC1
MP2667GG-0000-P
C24
2u2
C23
1u
R19
10k
R20
10k
R21
100k
C22
100n
C25
10u
R24
0
TITLE: esp32-s2_ePaper_pcb_v1
Document Number:
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Date: 18.11.2020 21:19
Sheet: 1/1