Theory Quiz-2 and Lab Evaluation (November 2022)

Syllabus for Theory Quiz-2

Memory Management: Basic Hardware, Address Binding, Logical and Physical Address, Dynamic linking and loading, Shared Libraries, Swapping, Contiguous Memory Allocation, Segmentation, Paging, Structure of the Page Table.	**CHAPTER -8 Memory Management Strategies** Complete (Excluding Sec 8.6.4, 8.7, 8.8)
Virtual Memory Management: Demand Paging, Copy-on-Write, Page Replacement, Allocation of Frames, Thrashing, Allocating Kernel Memory.	**CHAPTER-9 Virtual Memory Management** Complete (Excluding Sec 9.6.2, Sec 9.6.3, Sec 9.6.4, Sec 9.7, Sec 9.8, Sec 9.9, Sec 9.10)
Concurrency: The Critical-Section Problem, Peterson's Solution, Synchronization Hardware, Mutex Locks, Semaphores (counting and binary), Classic Problems of Synchronization (reader writer, producer consumer, dining philosopher), Monitors.	**CHAPTER-06/05 Process Synchronization** (Include Only 6.1, 6.2, 6.3, 6.4, 6.5, 6.6, 6.7) Please note that if you are following the e-book of 9th Edition; then for this specific chapter please follow CHAPTER 5 (from e-book) named Process Synchronization.

Lab Evaluation (week 16-17, as per academic calendar floated by DoAA) - CLO2

05 marks = Viva from the topics **listed in assignment** ***[for conducting viva this is mandatory to submit the assignment on the mentioned link before the deadline]***

05 marks = **Programs** - CPU Scheduling, Banker's Algorithm, Memory Allocation Strategies (Best, First, Worst), Disk Scheduling ***(student need to submit soft copy of the program to lab instructor)***