

[illegible]

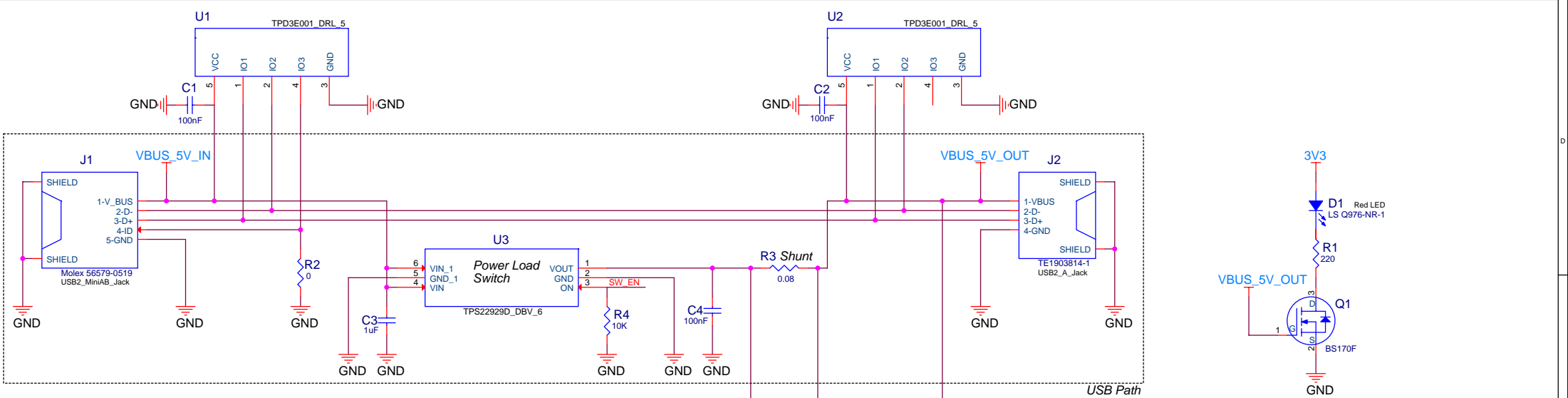
1. Schematics Creation
2. Version for layout start
3. INA I2C address decoding modified to align INA and EEPROM addresses

POWER_PROBE_USB Project

RevB

[illegible]

INGENIOS	BAYLIBRE SAS			
	POWER_PROBE_USB Project			
	Size A3	CAGE Code RFQ20150209-01A	DWG NO Front Page	Rev B
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INA226 I2C Address

A1	A0	SLAVE ADDRESS
0	0	1000000
0	1	1000001
0	SDA	1000010
0	SCL	1000011
1	0	1000100
1	1	1000101
1	SDA	1000110
1	SCL	1000111
SDA	0	1001000
SDA	1	1001001
SDA	SDA	1001010
SDA	SCL	1001011
SCL	0	1001100
SCL	1	1001101
SCL	SDA	1001110
SCL	SCL	1001111

EEPROM I2C Address

A2	A1	A0	EEPROM SLAVE ADDRESS	SERIAL NUMBER SLAVE ADDRESS
0	0	0	1010000	1011000
0	0	1	1010001	1011001
0	1	0	1010010	1011010
0	1	1	1010011	1011011
1	0	0	1010100	1011100
1	0	1	1010101	1011101
1	1	0	1010110	1011110
1	1	1	1010111	1011111

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