Version	Date	Author	Notes	Ind	Verif	Date Verif
0.1	29 Jan 2015	amarcos	1	D01	amarcos	TBD
0.2	27 March 2015	amarcos	2	D01	amarcos	27 March 2015
0.3	15 April 2015	amarcos	3	D01	amarcos	15 April 2015
0.4	24 April 2015	amarcos	4	D01	amarcos	24 April 2015
0.5	08 June 2015	amarcos	5	D01	amarcos	08 June 2016

- 1. Schematics Creation
- 2. Version for layout start
- 3. PCA9554 replaced by PCA9534 (no internal pull-up)
  3 external 10K pull-up added on I2C\_SW\_SEL, EEPROM\_WP and CONX\_WP signals
- 4. Probes connectors numbering modified as per Baylibre request GPIO Expander PCA9535's signals swapped to align layout with new Probes connectors numbering I2C Hub's signals swapped to align layout with new Probes connectors numbering Visualization connector's signals swapped to simplied layout
- 5. AT24C256 EEPROM replaced by AT24CS64 EEPROM.
  New EEPROM supports Unique Serial Number Register for board identification

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