Version	Date	Author	Notes	Ind	Verif	Date Verif
0.1	29 Jan 2015	amarcos	1	D01	amarcos	TBD
0.2	27 March 2015	amarcos	2	D01	amarcos	27 March 2015
0.3	24 April 2015	amarcos	3	D01	amarcos	24 April 2015

1. Schematics Creation
2. Version for layout start
3. INA I2C address decoding modified to align INA and EEPOM addresses

POWER_PROBE_JACK Project

RevB

Page Number	Page Title
1	Front Page
2	POWER_PROBE_JACK

INGENIOS	BAYLIBRE SAS			
	POWER_PROBE_JACK Project			
	Size A3	CAGE Code RFQ20150209-01A	DWG NO Front Page	Rev B
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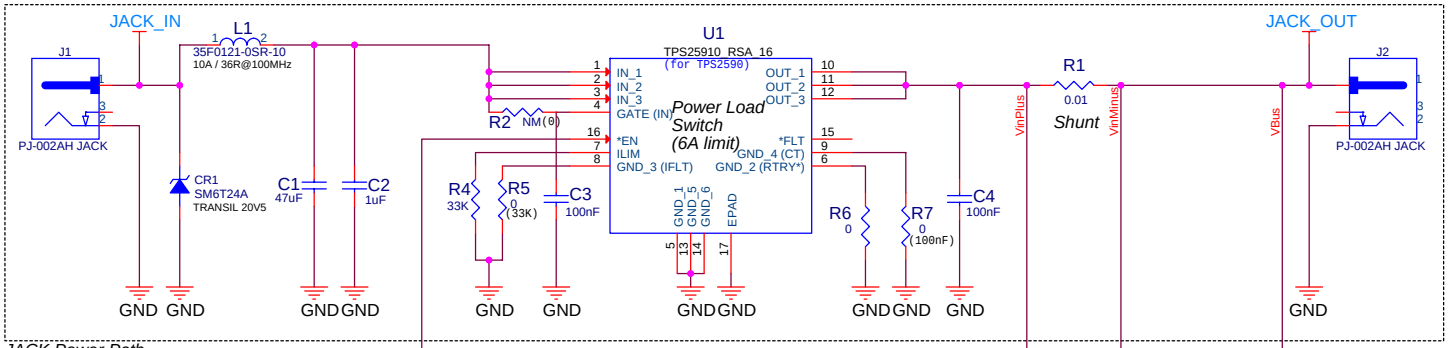
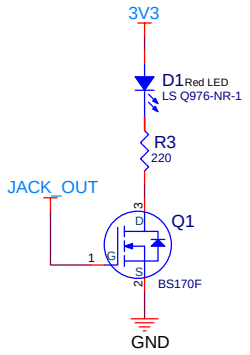
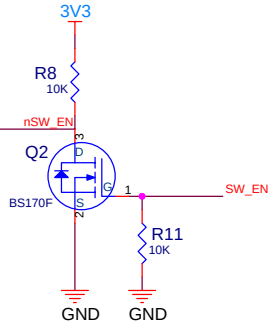
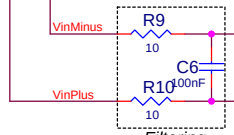


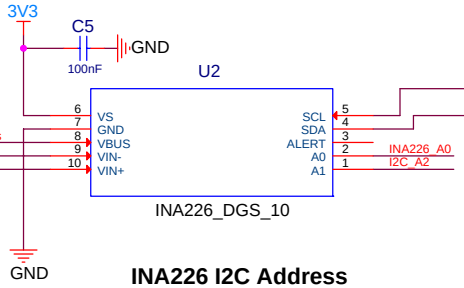
JACK Power Path

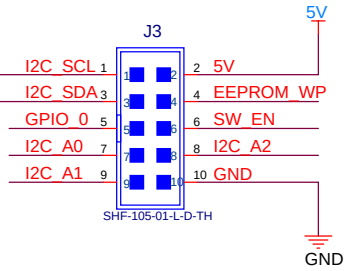
To support TPS2590 in place of TPS25910:
Mount R2 0ohm resistor
Remove C3
Replace R5 0ohm by 33Kohms
Replace R7 0ohm by 100nF 0603 capacitor

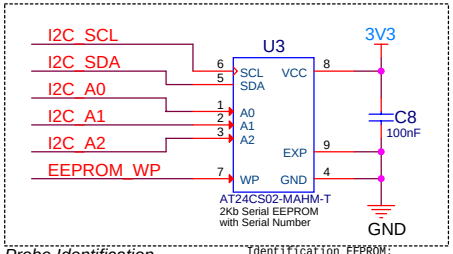






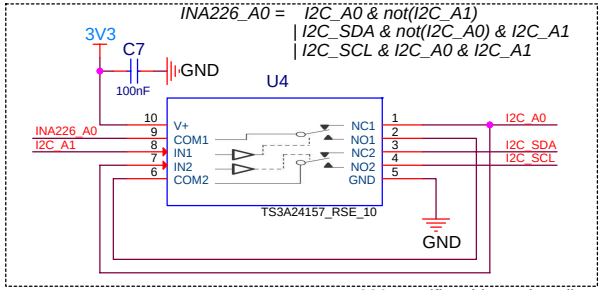






Probe Identification

```
Identification EEPROM:
# Probe Type
# Probe w/ or w/o load switch
# Probe shunt value
```



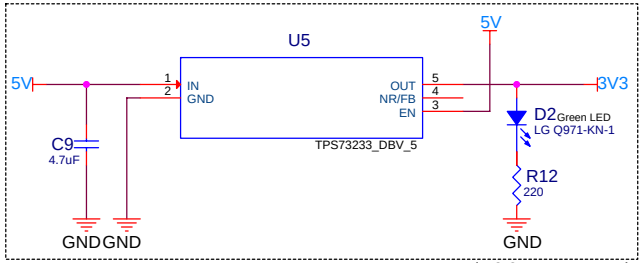
INA226 specific address decoding

INA226 I2C Address

A1	A0	SLAVE ADDRESS
0	0	1000000
0	1	1000001
0	SDA	1000010
0	SCL	1000011
1	0	1000100
1	1	1000101
1	SDA	1000110
1	SCL	1000111
~~SDA~~	~~0~~	~~1001000~~
~~SDA~~	~~1~~	~~1001001~~
~~SDA~~	~~SDA~~	~~1001010~~
~~SDA~~	~~SCL~~	~~1001011~~
~~SCL~~	~~0~~	~~1001100~~
~~SCL~~	~~1~~	~~1001101~~
~~SCL~~	~~SDA~~	~~1001110~~
~~SCL~~	~~SCL~~	~~1001111~~

EEPROM I2C Address

A2	A1	A0	EEPROM SLAVE ADDRESS	SERIAL NUMBER SLAVE ADDRESS
0	0	0	1010000	1011000
0	0	1	1010001	1011001
0	1	0	1010010	1011010
0	1	1	1010011	1011011
1	0	0	1010100	1011100
1	0	1	1010101	1011101
1	1	0	1010110	1011110
1	1	1	1010111	1011111



Main 3.3V power supply

INGENIOS

BAYLIBRE SAS

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Scale

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