

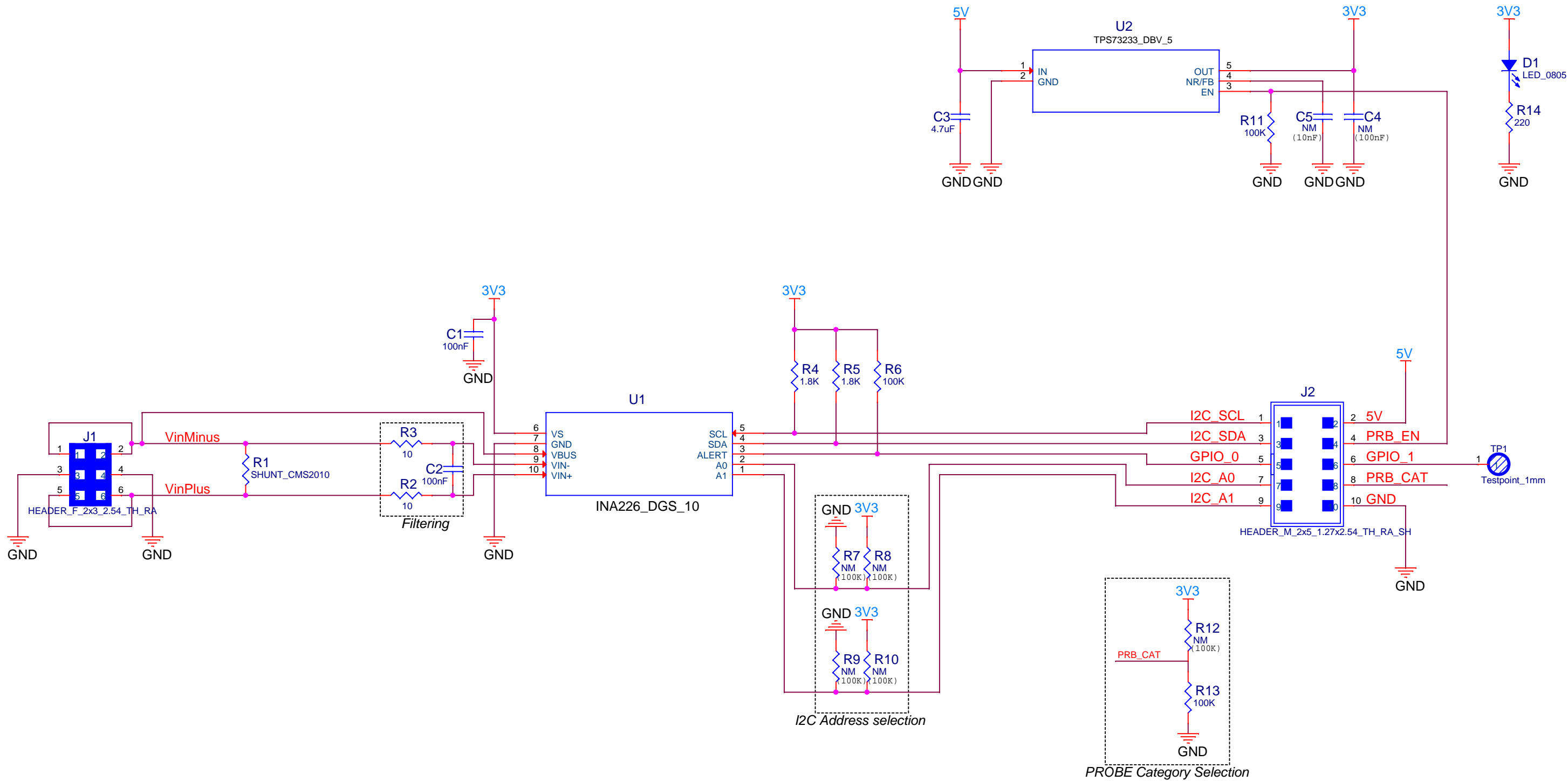
Version	Date	Author	Notes	Ind	Verif	Date Verif
0.1	23 June 2014	amarcos	1	D01	amarcos	TBD
0.2	01 July 2014	amarcos	2	D02	amarcos	01 July 2014
0.3	07 July 2014	amarcos	3	D03	amarcos	07 July 2014

1. Schematics Creation
2. R6 is mounted by default
GPIO_2 replaced by PRB_CAT signal allowing probe category selection + resistors selection implementation
3. Passive components footprint changed from 0805 to 0603
I2C Address selection connections to SCL and SDA signals removed to minimize PCB size. I2C address limited to 4 instead of 8.

POWER_PROBE_HE10 Project

RevA

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INGENIOS	BAYLIBRE SAS			
	POWER_PROBE_HE10 Project			
	Size A3	CAGE Code RFQ20140526-01A	DWG NO Power Probe HE10	Rev A
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