

Bayyapu Neelima

Post Doctoral Associate
MCS-Division
3.E.16, Building 240
Argonne National Laboratory
S. Cass Avenue, Lemont
Illinois, USA, 60439.

Office-Phone : +1-630-252-2016
Mail : nbayyapu@anl.gov
Mail2 : reddy_neelima@yahoo.com
Webpage : <http://www.mcs.anl.gov/~neelima/>
Github : <https://github.com/Bayyapu>

Education

- 07/2009-02/2015** (5.7 yrs) PH. D. IN INFORMATION TECHNOLOGY, NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL, INDIA.
Thesis : Communication and Computation Optimization of Concurrent Kernels and Sparse Matrix Vector Multiplication on GPU
▷ Thesis Supervisors : Prof. G. Ram Mohana Reddy (NITK) and Dr. Prakash S. Raghavendra (AMD, India).
- 01/2006-06/2011** (5.6 yrs) M. B. A. IN HUMAN RESOURCE MANAGEMENT, INDIRA GANDHI NATIONAL OPEN UNIVERSITY (IGNOU), INDIA..
Thesis : Analysis of Job satisfaction in IT Industry
▷ Thesis Supervisor : Dr. K. Suprabha (NITK- Surathkal).
- 08/2006-06/2008** (1.10 yrs) M. TECH. IN COMPUTER SCIENCE AND ENGINEERING, VISVESWARAYA TECHNOLOGICAL UNIVERSITY (VTU), BELGUAM, KARNATAKA.
Thesis : Analysing and Threading Applications for Performance Improvements on Multi core Platform
▷ Thesis Supervisor : Dr. Niranjana N. Chiplunkar, Principal, NMAMIT, Nitte..
- 08/1997-06/2001** (3.10 yrs) B. TECH. IN ELECTRONICS AND COMMUNICATION ENGINEERING, SREE NIDHI INSTITUTE OF TECHNOLOGY (SNIST), GHATKESAR, TELANGANA, INDIA .
Affiliated to Jawaharlal Nehru Technological University (JNTU), Hyderabad
▷ Thesis : Data Simulator for Cartosat Satellite.

Professional Experience

- 05/2017-Till Date** **Post-Doctoral Researcher**, Mathematics and Computer Science Division, Argonne National Laboratory (ANL), IL, USA.
- 07/2016-05/2017** (0.10 yrs) **Professor and Head**, Dept. of Information Science and Engineering, NMAM Institute of Technology, Nitte, Udupi District, Karnataka, India.
- 07/2015-06/2016** (1yr) **Professor**, Dept. of Computer Science and Engineering, NMAM Institute of Technology, Nitte, Udupi District, Karnataka, India.
- 10/2014-06/2015** (0.8 yrs) **Associate Professor**, Dept. of Computer Science and Engineering, NMAM Institute of Technology, Nitte, Udupi District, Karnataka, India.
- 03/2010-09/2014** (4.6 yrs) **Assistant Professor**, Dept. of Computer Science and Engineering, NMAM Institute of Technology, Nitte, Udupi District, Karnataka, India.
- 07/2009-02/2015** (5.7 yrs) **Ph. D. Scholar**, Dept. of Information Technology, National Institute of Technology Karnataka, Surathkal, India.
- 08/2006-03/2010** (3.7 yrs) **Senior Lecturer**, Dept. of Computer Science and Engineering, NMAM Institute of Technology, Nitte, Udupi District, Karnataka, India.
- 09/2006-05/2008** (1.8 yrs) **Graduate Student**, Dept. of Computer Science and Engineering, Visveswaraya Technological University (VTU), Belguam, Karnataka (Deputed)
- 08/2004-08/2006** (2 yrs) **Lecturer**, Dept. of Electronics and Communication Engineering, P.A. College of Engineering, Mangalore, Karnataka, India.
- 08/2003-08/2004** (1 yr) **Project Trainee**, Apollo Macro Systems, Hyderabad, Telangana, India.
- 08/2001-08/2003** (2 yrs) **Lecturer**, Vathsalya Institute of Science and Technology, Bhongir, Nalgonda, Telangana, India.

Research Interests

Research Interests High Performance Computing; Performance Analysis and Optimizations of Scientific Applications on Multi-cores and GPUs; Cyber Security, Computing and Analyzing using Graphs; Distributed Computing and Big Data Analysis; Bio-Informatics; Machine Learning and Deep Neural Networks

Research Projects

- 12/2009-01/2012** (2 Yrs) Software Infrastructure and Programming Model to Enhance the Performance of Applications on Multi-core Platform (Status : Completed)
▷ *Funding Agency Name : Department of Science and Technology, Govt. of India. Reference Number : DST/TDD/03 Amount sanctioned : Rs.15.98 Lakhs Awarded by : DST, Govt of India.*

Industry Grants

- 06/2012-12/2016** (4.6 Yrs) **Principal Investigator** of Professor Partner Ship Program with NVIDIA for GPU Education Center (GEC) (Formerly NVIDIA CUDA Teaching Centre)
08/2012-08/2013 (1 Yr) **Principal Investigator** for AMD University Relations Program
08/2007-08/2010 (3 Yrs) **Single Point of Contact (SPOC)** for Intel (Obtained 100 class room Software License from Intel for Intel VTune Analyzer)

Awards and Honors

- 12/2016** **Best Poster Award (GTCx-India)** Customized Face Classification to Reduce the Training time : A Proposal. 6th Dec., 2016. GPU Technology Conference (GTCx) India.
- 12/2015** **Best Poster Award (HiPC-SRS-15)** SWIFT : A Fast Enhanced String Matching Algorithm for Heterogeneous Architectures. 22nd IEEE-HiPC-SRS held at Bangalore, India from 16th to 19th December, 2015.
- 01/2015** **Best Psoter Award - PoPL-Student Posters-2015** String Sorting on GPU using Hybrid Algorithms. 42nd ACM SIGPLAN-SIGACT-PoPL-Student Posters held in Mumbai, India during 12th to 18th January, 2015.
- 05/2014** **IEEE-TCPP (Technical Committee on Parallel Processing) Travel Grant** for attending 28th IEEE - IPDPS held at Arizona, USA during 19th to 23rd May, 2014.
- 06/2014** **International Travel Grant** from Department of Science and Technology, Govt. of India to attend a 13th ISPDC. Held in France during 24th to 27th of June, 2014.
- 06/2013** **Women Who CUDA**, Invited by CUDA in Action Team of NVIDIA. Details @ <http://www.nvidia.com/object/women-and-cuda.html>
- 04/2013** **Best Paper Award** Graphics Processing Unit (GPU) Implementation Methodology of AERMOD model. 2nd International Conference on Recent Advances in Sciences and Engineering-ICRASE-2013. 29th to 30th April, 2013.
- 04/2013** **Best Project of the Year Award** Application Performance Acceleration using Multi-processors, Final Year Students' Project Exhibition and Competition-EXPRO-2013, NMAM Institute of Technology, Nitte, Karnataka, India.
- 12/2012** **Best CUDA University Relations Coordinator Award** Given by NVIDIA during NVIDIA day co-located with the 19th annual IEEE International Conference on High Performance Computing (HiPC 2012) held in Pune, India, December 18-21, 2012.
- 04/2012** **Best Project of the Year Award** GPU device optimization tool, Final Year Students' Project Exhibition and Competition-EXPRO-2012, NMAM Institute of Technology, Nitte, Karnataka, India.
- 01/2008** **Best Paper Award** Analysis of Threaded Applications on Multi-core Platform, National Conference on Ubiquitous and Pervasive Computing, NMAMIT, Nitte, 24th and 25th Jan- 2008.

Publications

Research papers in refereed journals

- JPDC-2017** **B. Neelima.** "High Performance Computing Education in an Indian Engineering Institute". Journal of Parallel and Distributed Computing by Elsevier. Vol. 105 : PP : 73-82. 2017.
- CCPE-2017** **B. Neelima,** Bharath B. Shamsundar, Anjjan S. Narayan, Rithesh R. Prabhu, Crystal Gomes. (2016). Performance Acceleration using Dynamic Parallelism of Kepler GPUs for Recursive Sorting Algorithm. Concurrency and Computation : Practice and Experience. Vol. 29, Issue 4. PP : 1-11. 2017.
- CCPE-2015** **B. Neelima,** Prakash S. Raghavendra and G. Ram Mohana Reddy. (2015) Communication and computation optimization of concurrent kernels using kernel coalesce on a GPU. Concurrency and Computation : Practice and Experience. Vol. 27, Issue 1, PP : 47-68. Jan, 2015. DOI : 10.1002/cpe.3194.
- SCPE-2012** **B. Neelima,**Prakash S. Raghavendra and G. Ram Mohana Reddy.(2012). "New Sparse Matrix Storage Format to Improve the Performance of Total SPMV Time". Scalable Computing : Practice and Experience. Vol. 13(2). (ISSN 1895-1767). Pp : 159-171.

Research posters

- SRS@HiPC-16**
(Hyderabad-India) Raghurama, Gururaja Rao P., Rajesh Poojary, Gajendra S Patagara and **B. Neelima.** "Customized Face Classification to Reduce the Training Time : A Proposal". Poster Acceptance at 9th Student Research Symposium (SRS) co-located with 23rd Annual IEEE International Conference on High Performance Computing, Data and Analytics (HiPC-2016) to be held during 19th to 22nd December, 2016 at Hyderabad, India.
- SRS@HiPC-16**
(Hyderabad-India) Sharath Yaji and **B. Neelima.** "Data Cube 2.0 : Data Dodecahedron (DRON) Operator using Euclid's Model for OLAPing BIGDATA Aggregation". Poster Acceptance at 9th Student Research Symposium (SRS) co-located with 23rd Annual IEEE International Conference on High Performance Computing, Data and Analytics (HiPC-2016) to be held during 19th to 22nd December, 2016 at Hyderabad, India.
- SRS@HiPC-15**
(Bangalore-India) Sourabh S. Shenoy, Ms. Supriya Nayak U. and **B. Neelima.** "SWIFT : A Fast Enhanced String Matching Algorithm for Heterogeneous Architectures". Student Research Symposium co-located with IEEE International Conference on High Performance Computing (HiPC-15) held at Bangalore, India from 16th to 19th December, 2015.
- SRS@HiPC-15**
(Bangalore-India) Bharath B.S., Amoolya U. Shetty, Ananya Rao, Supreetha Shetty, and **B. Neelima.** "Performance Improvement for Multi-Key Quick Sort using Kepler GPUs. Student research Symposium co-located with IEEE International Conference on High Performance Computing (HiPC-15) held at Bangalore, India from 16th to 19th December, 2015.
- SRS@PoPL-15**
(Mumbai-India) Anjjan S. Narayanan, Rithesh R. Prabbhu and **B. Neelima.** String Sorting on GPU using Hybrid Algorithms. 42nd ACM SIGPLAN-SIGACT-PoPL-Student Posters held in Mumbai, India during 12th to 18th January, 2015.
- SRS@HiPC-11**
(Bangalore-India) **B. Neelima,** Prakash S. Raghavendra and Jayavanth U. Shenoy.(2011). "BLSI : Bit Level Single Indexing of Sparse Matrix for GPU Architectures". International Conference on High Performance Computing (HIPC-SRS-2011-Poster), Bangalore. 18th to 21st December-2011.
- SRS@HiPC-11**
(Bangalore-India) **B. Neelima,** Prakash S. Raghavendra, Rashmi Mahima, Akshyaya L. Bhat and Ashik Kumar. (2011). "C2O : Communicaiton and Computation Optimizer for the Graphics Processor Architecture". International Conference on High Performance Computing (HIPC-SRS-2011-Poster), Bangalore. 18th to 21st December-2011.
- SRS@HiPC-11**
(Bangalore-India) **B. Neelima,** Adarsh Konchady, Shashikiran, Brenda Olivia Martis and Rayan Victor D'Souza. (2011) Code Optimizations in LLVM Compiler. International Conference on High Performance Computing (HIPC-SRS-2011-Poster) Bangalore. 18th to 21st December-2011.

Research papers in refereed conferences

- IES-2017**
(Kumamoto-Japan)
B. Neelima, Gururaja Rao P., Rajesh Poojary, Gajendra S Patagara, **B. Neelima**. "Face Classification using Convolution Neural Networks" Proceedings of 6th International Engineering Symposium -2017 (IES-2017), to be held from 1st to 3rd March, 2017 at 100th Anniversary Hall, Kumamoto University, Japan.
- ISPDC-2016**
(FuZhou-China)
Sourabh S. Shenoy, Supriya Nayak U., **B. Neelima**. "SWIFT : A Performance Accelerated Optimized String Matching Algorithm for Nvidia GPUs". 15th International Symposium on Parallel and Distributed Computing (ISPDC-16), 8th to 10th July, 2016 at FuZhou, China.
- ICHPCA-2014**
(Bhubaneswar-India)
B. Neelima, Anjjan S. Narayan and Rithesh G. Prabhu (2014). String Sorting on Multi and Many Threaded Architectures : A Comparative Study. 2014 International Conference on High Performance Computing and Applications. (ICHPCA-2014), Bhubaneswar, India. 22nd to 24th December, 2014. ISBN : 978-1-4799-5957-0.
- ICHPCA-2014**
(Bhubaneswar-India)
B. Neelima, Rithesh G. Prabhu and Anjjan S. Narayan (2014). Yet Another Proposal for All Pair Shortest Path on GPU. 2014 International Conference on High Performance Computing and Applications. (ICHPCA-2014), Bhubaneswar, India. 22nd to 24th December, 2014. ISBN : 978-1-4799-5957-0.
- ISPDC-2014**
(Porquerolles-France)
B. Neelima, G. Ram Mohana Reddy and Prakash S. Raghavendra (2014). A GPU Framework for Sparse Matrix Vector Multiplication. 13th International Symposium on Parallel and Distributed Computing (ISPDC-14), 24th to 27th June, 2014 at Porquerolles Golden Island, France. PP : 1427-1436.
- PDCTA-2011**
(Tirunelveli-India)
B. Neelima and Prakash S. Raghavendra. (2011). "CSPR : Column only Sparse Matrix Representation for Performance Improvement on GPU Architecture", International Conference on Parallel, Distributed Computing Technologies and Applications (PDCTA-2011) in conjunction with CCSEIT-2011, Tirunelveli, Tamil Nadu, India. Organized by AIRCC (Academy and Industry Research Collaboration Center) at Manonmaniam Sundharanar University, 23rd to 25th, September-2011. Series : Communication in Computer and information Science (CCIS). Title : Advances in Parallel, Distributed Computing. Volume Num : 203. 1st Edition, 2011, XVIII, 694p. ISBN 978-3-642-24036-2. p.581.
- ICCRD-2011**
(Shanghai-China)
Niranjan N. Chiplunkar, **B. Neelima** and Deepak Rao M. (2011). "Multithreaded Programming Framework Development for gcc Infrastructure". 3rd International conference on computer research and Development (ICCRD 2011), Shanghai, China. 11th-13th March 2011 54 - 57pp.
- ICCISE-2011**
(Duabi-UAE)
B. Neelima, Prakash S. Raghavendra, and Niranjan N Chiplunkar, "Framework for Interactive and Semi-Automatic Program Optimization Tool for Multi Core Architectures", International Conference on Computer and Information Science and Engineering (ICCISE)-2011, Duabi, Organized by World Academy of Science, Engineering and Technology (WASET), 25-27, January-2011.
- ICIIS-2010**
(Surathkal-India)
B. Neelima and Prakash S. Raghavendra. (2010). "Recent Trends in Software and Hardware for GPGPU Computing : A Comprehensive Survey", Fifth International Conference on Industrial and Information Systems (ICIIS)-2010, at National Institute of Technology Karnataka, Surathkal, 29th July to 1st August-2010. Published in IEEE Explore Conference Proceedings.

Research papers in refereed workshops

- EduHPC@SC-15**
(Texas-USA)
B. Neelima and Jiajia Li. Introducing High Performance Computing Concepts into Engineering Undergraduate Curriculum : A Success Story. Proceedings on Workshop on Education for High Performance Computing (EduHPC-15) co-located with SC-15, organized by ACM and IEEE Computer Society from 15th to 20th Nov. 2015, Austin, Texas, USA.
- MTAAP@IPDPS-15**
(Arizona-USA)
B. Neelima, G. Ram Mohana Reddy and Prakash S. Raghavendra (2014). Predicting an Optimal Sparse Matrix Format for SpMV Computation on GPU. Workshop on Multithreaded Architectures and Applications (MTAAP-14), co-located with 28th IEEE International Parallel and Distributed Processing Symposium (IPDPS-14), 19th to 23rd May, 2014 at Arizona, USA. PP : 51-58.

Invited Talks and workshops

04/2016	Two-day Workshop , Conducted a Two day Hands-on workshop, as Resource Person, on "Parallel computing using CUDA" at Dept. of CSE, Manipal Institute of Technology (MIT), Manipal on 11th and 12th of April, 2016.
05/2015	Invited Talk , Gave a talk on "Experiences in Introducing Research at Undergraduate Level", during One Day Faculty Conclave on Engineering Education Research held at NMAMIT, Nitte on 6th May, 2015
03/2015	Resource Person , during National Workshop on "Computing Systems Research" for faculty from March 25th to 28th, 2015 organized at NMAMIT, Nitte and Sponsored by TEQIP-II.
08/2014	Resource Person , to deliver a talk during "National Workshop on Multi-core Architecture Multithreading and Programming" at Acharya Institute of Technology, Bangalore on 21st August, 2014.
11/2013	Resource Person , for a National Workshop on Massively Parallel Programming held at B. V. B. C. E. T, Hubli on 21st November, 2013.
08/2009	Invited Talk , on "Device Drivers and Interrupt Mechanism in Embedded Systems" at "Embedded Systems" workshop, from 27th to 29th August, 2009, Organized by Department of Computer Science and Engineering, NMAMIT, Nitte.
08/2009	Invited Talk , on "Effective Teaching of VLSI Design" at "IUCEE Follow-up" workshop, from 24th to 25th, August, 2009, Organized by Department of Computer Science and Engineering, NMAMIT, Nitte.

Invited Panel

06/2016	Moderator , for a panel on "Achieving Gender Parity", during Women's Day organized on 8th March, 2016 at NMAMIT, Nitte, Karnataka, India. The panel members are Dr Niranjana N. Chiplunkar, Dr Asha Hegde, Prof Shalini K Sharma, Dr Sarojadevi Hande, Dr Sharada Prabhu, Prof Rekha Bhandary, and Dr Uma Shankaran.
08/2014	Panelist , for "Handling Career Breaks for Women" during Women in Computing Academic BoF session co-located with the 21st annual IEEE International Conference on High Performance Computing (HiPC 2014) held at the Hotel Cidade De Goa in Goa, India, during 17-20, December, 2014.

Professional Services and Activities

Journal Referee

TPDS	Reviewer , Transactions on Parallel and Distributed Systems, Published by IEEE. 2017, 2018
TCC	Reviewer , Transactions on Cloud Computing, Published by IEEE. 2017
JPDC	Reviewer , Journal of Parallel and Distributed Computing, Published by Elsevier. 2017, 2018
PARCO	Reviewer , Parallel Computing Systems and Applications, Published by Elsevier. 2017
CCPE	Reviewer , Concurrency and computation : Practice and Experience, published from John Wiley. 2015, 2016, 2017
CLUSTER	Reviewer , Cluster Computing, The journal of Networks, Software Tools and Applications (CLUSTER), Springer publications. 2015, 2016.

Chair/ Co-chair - Conferences / Workshops

- 2017** **Chair** 6-day Faculty Development Program (FDP) on "Machine Learning", jointly organized by Dept. of Information Science and Engineering, NMAM Institute of Technology, Nitte and E & ICT Academy, National Institute of Technology, Warangal (NIT-W) from 23rd to 28th January, 2017, at NMAM Institute of Technology, Nitte, Karnataka, India, Sponsored by Ministry of Electronics and Information Technology (Meity), Govt. of India.
- 2016** **Chair** 5-day Faculty Development Program on "Recent Trends in Information and Network Security", Organized by Dept. of Information Science and Engineering, NMAM Institute of Technology, Nitte from 28th November to 2nd December, 2016, sponsored by TEQIP-II.
- 2016** **Chair** NMAMIT ACM-W Student Chapter and GPU Education Center, NMAMIT in association with GPU Center of Excellence at Indian Institute of Technology, Bombay (IITB) has organized a one day free hands-on workshop on "Parallel Computing using MATLAB, CUDA and Embedded Systems" on 16th March, 2016.
- 2016** **Chair** one-day Women's day celebrations at NMAM Institute of Technology, Nitte on March 8- 2016 at NMAMIT organized by NMAMIT ACM-W Student Chapter, sponsored by TEQIP-II.
- 2016** **Chair** A hands-on "Program Verification and Logic Model Checking" by Dr. Devi Prasad, Professor at the School of Information Sciences of Manipal University, Manipal was arranged on 22nd Feb. 2016 at NMAMIT, Nitte, Karnataka, India.
- 2015** **Chair** One day workshop is sponsored and organized by Intel and Wipro respectively at NMAMIT, Nitte on " HPC Code Modernization" on 1st October, 2015.
- 2015** **Registration Committee Chair** for 3-day workshop on Research Methodologies and Latex" organized by VTU, Belagavi and NMAMIT, Nitte, Karnataka, India, from 2nd to 4th September, 2015.
- 2015** **Chair** 4-day workshop on "Computing Systems Research" for faculty from March 25th to 28th, 2015 organized at NMAMIT, Nitte, Karnataka, India and Sponsored by TEQIP-II.
- 2009** **Chair** 3-day "National level workshop on Multi-core Technologies" from 12th-14th March, 2009, organized by Department of CSE, NMAMIT, Nitte, Karnataka, India.
- 2008** **Chair** "Training on IBM DB2 and Open Source Tools for UG Students" on 23rd October, 2008, at NMAMIT, Nitte, Karnataka, India.

Session Chair - Conferences / Workshops

- 2016** **Session Chair** 23rd IEEE International Conference on High Performance Computing (HiPC-2016) to be held at Hyderabad during 19th to 22nd December, 2016.
- 2016** **Session Chair** IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics (DISCOVER 2016), organized by Mangalore Sub-section to be held at National Institute of Technology Karnataka (NITK), Surathkal, Karnataka, India, from 13th to 14th August, 2016.
- 2016** **Session Co-Chair** International Conference on Emerging Trends in Engineering-ICETE-16. Organized by NMAMIT, Nitte, Karnataka, INDIA.
- 2011** **Session Chair** at CCSEIT-2011, Tirunelveli, Tamil Nadu, India. Organized by AIRCC (Academy and Industry Research Collaboration Center) at Manonmaniam Sundharanar University, 23-25, September-2011.

Committee Member - Conferences / Workshops

- 2018** **Technical Program Committee**, Tenth International Conference on Contemporary Computing (IC3-2018), Jointly organized by Jaypee Institute of Information Technology (JIIT) and University of Florida, USA, Organized at JIIT, Noida, India from 2nd to 4th August, 2018 and technically co-sponsored by TCPP and IEEE Computer Society.
- 2017** **Technical Program Committee**, Tenth International Conference on Contemporary Computing (IC3-2017), Jointly organized by Jaypee Institute of Information Technology (JIIT) and University of Florida, USA, Organized at JIIT, Noida, India from 10th to 12th August, 2017 and technically co-sponsored by TCPP and IEEE Computer Society.
- 2017** **Advisory Committee**, National Symposium on Computing, Analytics and Networks (CAN-2017) to be held on 15th April, 2017 at Chitkara University, Himachal Pradesh.
- 2016** **Organizing Committee**, 23rd IEEE International Conference on High Performance Computing (HiPC-2016) to be held at Hyderabad during 19th to 22nd December, 2016.
- 2016** **Technical Program Committee**, 15th IEEE International Symposium on Parallel and Distributed Computing (ISPDC-2016) to be held from 8th to 10th July, 2016 at FuZhou, China.
- 2016** **Organizing Committee**, IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics (DISCOVER 2016), organized by Mangalore Sub-section to be held at National Institute of Technology Karnataka (NITK), Surathkal, Karnataka, India, from 13th to 14th August, 2016.
- 2015** **Technical Program Committee**, 14th International Symposium on Parallel and Distributed Computing (ISPDC-2015), to be held at Limassol, Cyprus from 29th Jun to 2nd Jul, 2015.
- 2015** **Organizing Committee**, for the 29th IEEE International Parallel and Distributed Processing Symposium (IPDPS-15) to be held during May 25th to 29th, 2015 at Hyderabad, INDIA.
- 2015** **Organizing Committee**, for the 22nd annual IEEE International Conference on High Performance Computing (HiPC 2015) to be held Bangalore, India, during 16th to 19th, December, 2015.
- 2014** **Organizing Committee**, the 1st Indian Symposium on Computer Systems (IndoSys-14), to be held at C-DAC Knowledge Park Campus in Bangalore from June 28-29, 2014, in cooperation with ACM India and IEEE Computer Society Bangalore Chapter.
- 2014** **Organizing Committee**, for the 21st annual IEEE International Conference on High Performance Computing (HiPC 2014) held at the Hotel Cidade De Goa in Goa, India, during 17-20, December, 2014.
- 2013** **Organizing Committee**, for the 20th annual IEEE International Conference on High Performance Computing (HiPC 2013) held at Park Plaza Bengaluru in Bengaluru, India, December 18-21, 2013.
- 2012** **Organizing Committee**, for the 19th annual IEEE International Conference on High Performance Computing (HiPC 2012) held at the Le Meridien Hotel Hotel in Pune, India, December 18-21, 2012.
- 2012** **Organizing Committee**, NMAMIT Research Conclave NMRECON-12, organized by NMAMIT, Nitte, Karnataka, India.

Reviewer - Conferences / Workshops

2016	Reviewer , IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics (DISCOVER 2016), organized by Mangalore Sub-section to be held at National Institute of Technology Karnataka (NITK), Surathkal from 13th to 14th August, 2016.
2016	Reviewer , International Conference on Emerging Trends in Engineering-ICETE-16. Organized by NMAMIT, Nitte, Karnataka, INDIA.
2014	Reviewer , International Conference on Emerging Trends in Engineering-ICETE-14. Organized by NMAMIT, Nitte, Karnataka, INDIA.
2012	Reviewer , International Conference on Emerging Trends in Engineering-ICETE-12. Organized by NMAMIT, Nitte, Karnataka, INDIA.

Service to the Professional Societies

2017	Executive Committee Member , IEEE Computer Society Bangalore Section.
2017	Executive Committee Member , IEEE Mangalore Subsection
2015, 2016, 2017	Faculty Sponsor , NMAMIT ACM-W Student Chapter, associated with ACM. Formation Date : 12th November, 2015.
2016, 2017	Faculty Sponsor , NMAMIT IEEE Women-in-Engineering (WIE) Student Chapter. Approved on 22nd September, 2016.
2008	Branch Coordinator , NMAMIT IEEE Student branch

Professional Affiliations

2016, 2017, 2018	Senior Member , Association of Computing Machinery (ACM) and Association of Computing Machinery for Women (ACM-W)
2015, 2016, 2017, 2018	Senior Member , Institute of Electrical and Electronics Engineers (IEEE) and Member of Women in Engineering (WiE) since 11th November, 2015. M-41280921.
2014, 2015	Member , Association of Computing Machinery (ACM) and Association of Computing Machinery for Women (ACM-W)
Since 03/2016	Member , Intuition of Engineers (India) M-153712-6 .
2014	Student Member , Institute of Electrical and Electronics Engineers (IEEE) and Member of Women in Engineering (WiE)
2010, 2011, 2012, 2015	Member , Institute of Electrical and Electronics Engineers (IEEE) and Member of Women in Engineering (WiE)
11/2010	Life Member , Computer Society of India (1084166).
07/2009-02/2016	Associate Member , Intuition of Engineers (India) AM097533-0.
Since 2008	Life Member , International Association of Engineers (IAENG) (109397)
2006-2008	Student Member , Institute of Electrical and Electronics Engineers (IEEE) and Member of Women in Engineering (WiE)
Since 2005	Life Member , Indian Society for Technical Education (ISTE) (LM 45377)
2000-2001	Student Member , Institute of Electrical and Electronics Engineers (IEEE) and Member of Women in Engineering (WiE)