WHAT ?
WHY ?
HOW ?

{ MICROPROCESSOR. } → µp

µ → µm → size.

Process → Eg: Brushing Teeth

IC
PROCESS
LAPTOPS / DESKTOP
↑MOBILES.

Electrical? or Electronic?

Electronic → Semiconductor → IC → Integrated circuit.?

Chip

µp is an Electronic Device.

form → circuit.

Discrete.

Integrated.

nm.

15 → 14nm → Size of circuit. → transistor

WHY? Study µp?.

→ machine independent.

↳ High level lang. (C, C++, Java, Python)

Compiler / Interpreter.

↳ Object code, Source code, Binary code.

Processor understands lang. of 0s and 1s. (Binary).

Low level lang.

↓

Machine lang. ← 0101101101 0111100111

↓

architecture of processor.

Processor dependent.

i3 →

i7 →

i5 →

AMD R5 →

machine independent

a=c

High level.

Medium level. → Assembly lang. → pseudo codes.

Low level.

Assembler

↓

machine code.

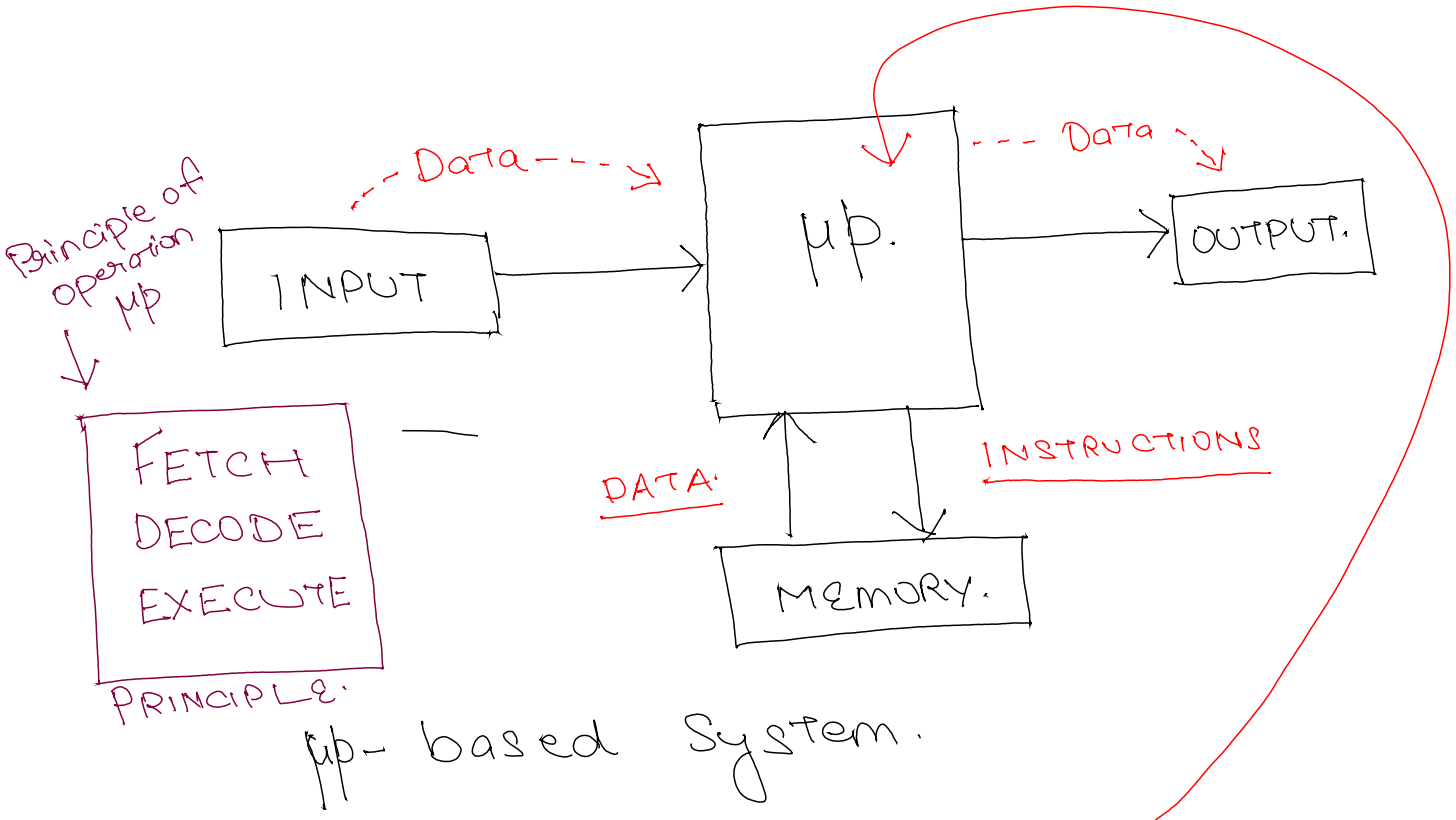
8085

8086 i3

i7

machine dependent.

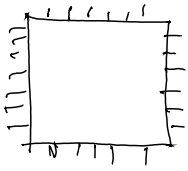
MOV R1, R2.



µp- based System.

Register.

a type of storage that is present in the µp IC itself.



MEMORY.

Primary

That interacts with µp directly.

→ RAM
→ CACHE

Secondary.

That doesnot interact with µp directly.

Hard-disk.
Pen-drive
SSD
CD
DVD.

CACHE
L1
L2