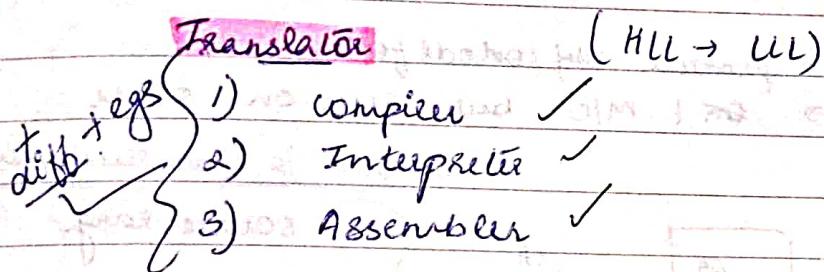


- Aho et al.

Compiler design

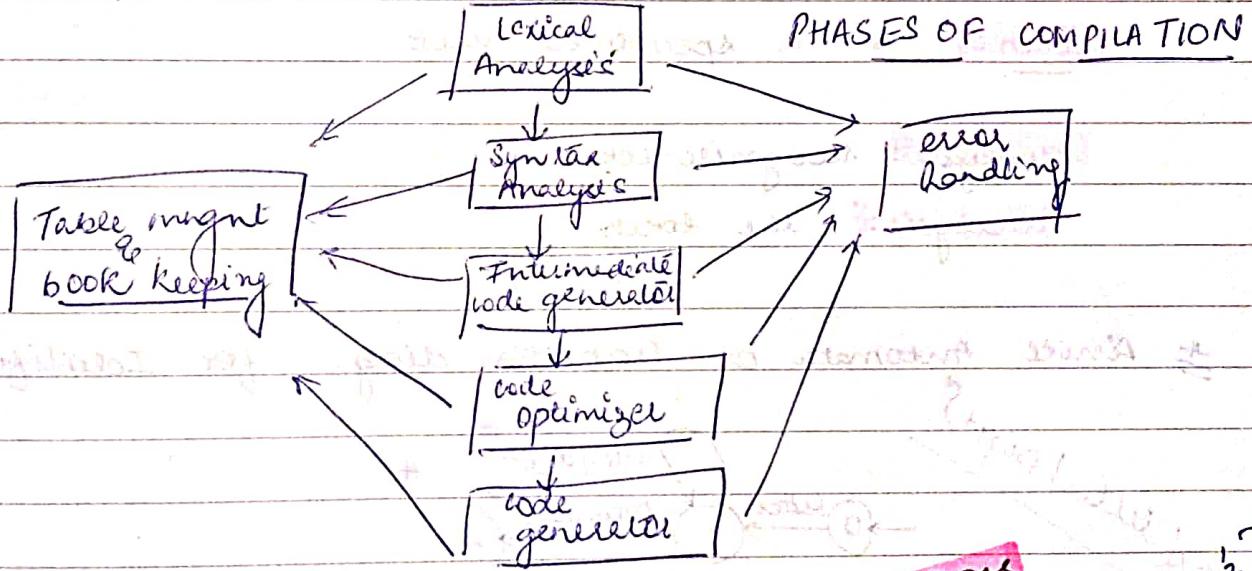


Q. Why do we need translator?

HLL \rightarrow LLL (ML)

Q. why C middle lang?

Assignment (I)



Passes →
Boot strapping

C → Multipass compiler
1st 3 stages are compiled.
 $\frac{1}{3}$ $\frac{2}{3}$ $\frac{3}{3}$

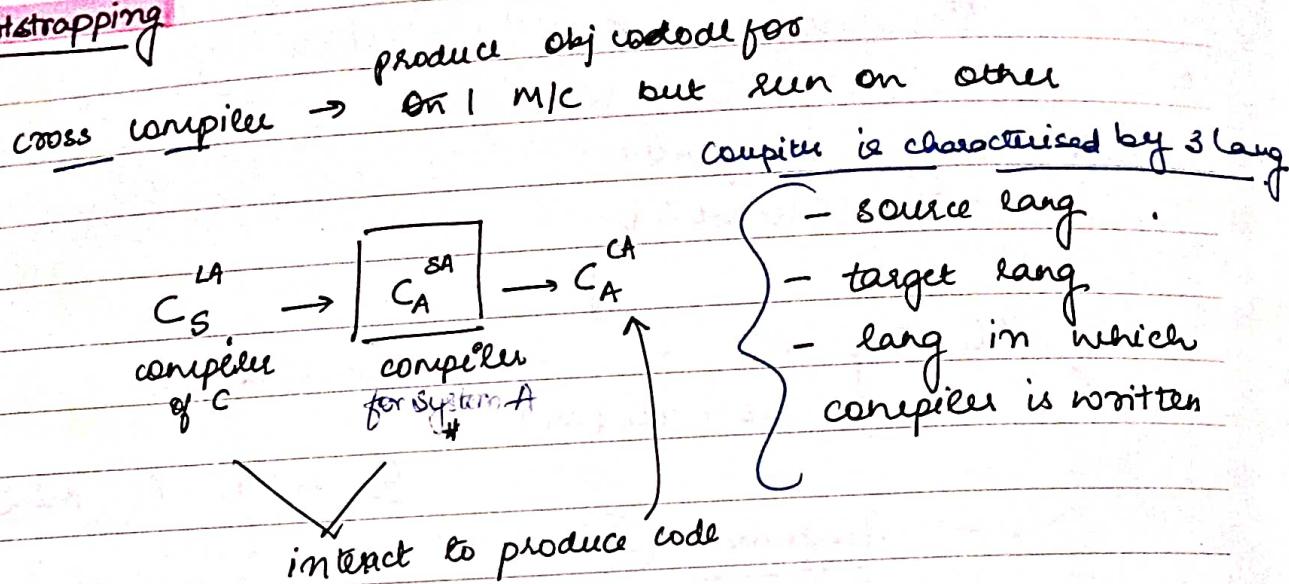
unix → singl pass compiler

C (flex) → C compiler
+ unix

pass in phase

(we pass in many phases).

Bootstrapping



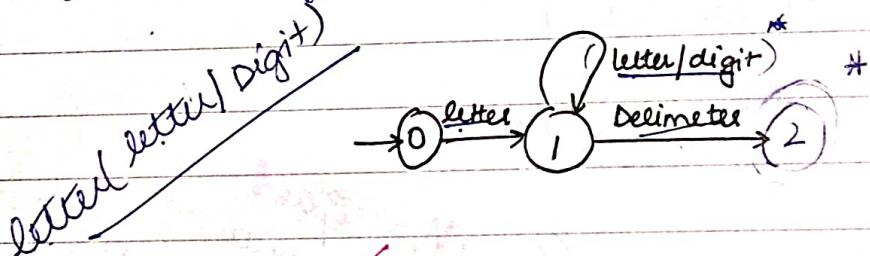
token: is a specialised unit

analysis: recognise tokens

analysis: the token

UNIT-2

\cong Finite Automata or Transition diag. for Identifiers:

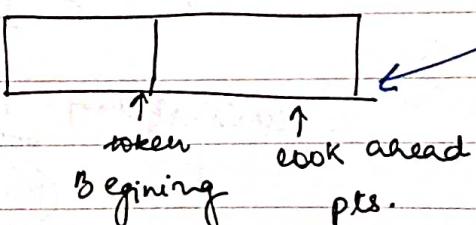


A B C D / 2 D 7

identifier
 \therefore retract
1 position
back.

need 4 LA's:

The purpose of splitting the analysis of the source prog into 2 phases, is to simplify the overall design of compiler.



many tokens may hv to be examined bt next token is determined :-

I/P buffer is used.

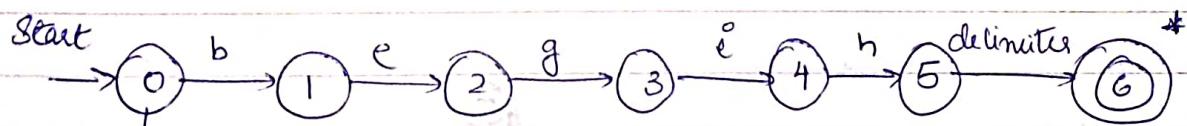
<u>Token</u>	<u>code</u>	<u>value</u>
begin	1	-
end	2	-
if	3	-
else	4	-
then	5	-
{ identifier }	6	Pointer to symbol table
constants	7	"
{ reserved words }		
<	8	1
<=	8	2
!=	8	3
>	8	4
<>	8	5
>=	8	6

RETRACT()

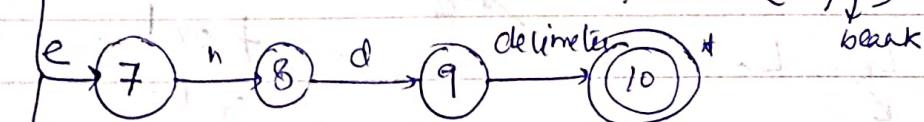
to identify
integer value
independently

return (id. INSTALL ())

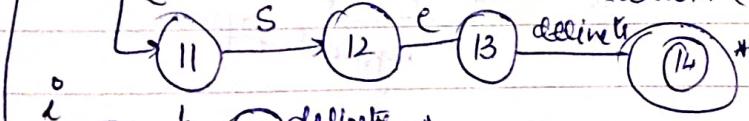
(0 - 127)



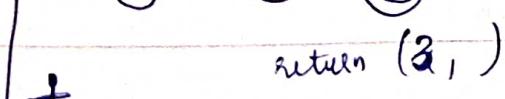
return (1,)



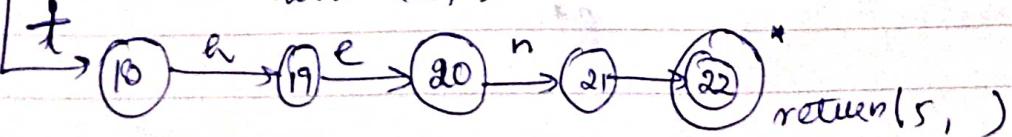
return (2,)



return (4,)

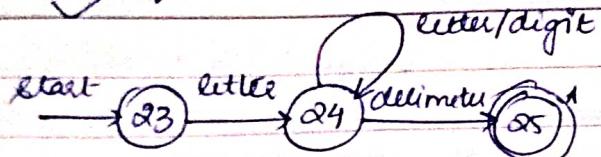


return (3,)



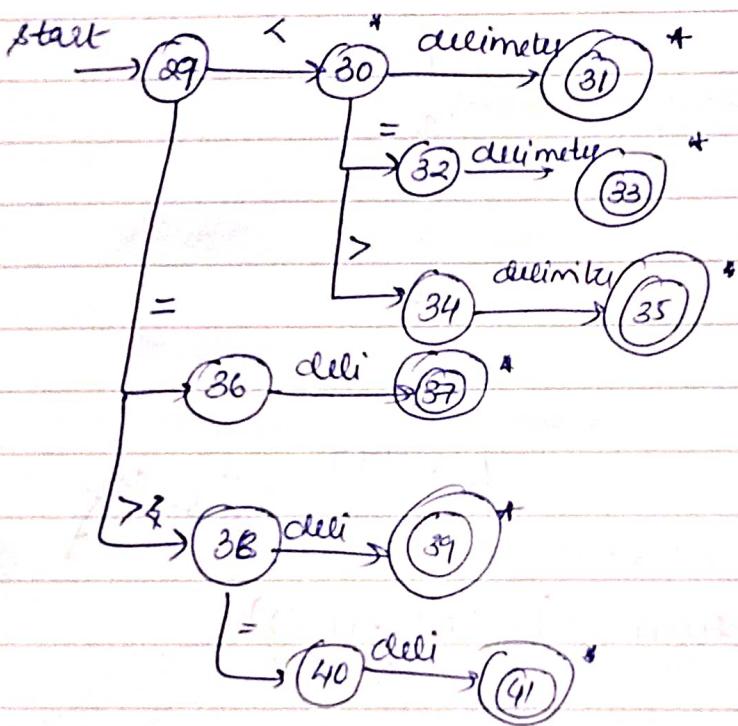
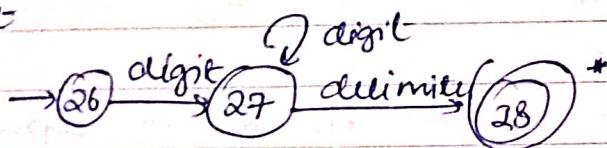
return(5,)

identifier



return (6, INSTALL())

constant



1 - $\in \rightarrow$

2 - $a \in L$

3 - $L_R \quad L_S$

Req. EXP to
belonging f.
DFA



+ or !
eg RIS

$L_R \cup L_S = \underbrace{R+S}_{\text{symbol. rep.}}$

symbol. rep.

$L_R \cdot L_S = L_S$

R^*

Keywords :-

BEGIN | END | IF | THEN | ELSE

17

Identifiers :-

LETTER (LETTER | DIGIT)

- equi of NDFA & DFA

- reg. exp to DFA

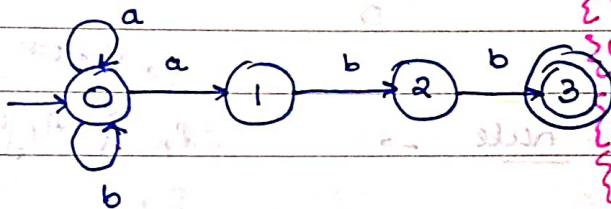
- DFA to reg. Exp.

- Min. of FA

- N states

DFA		a	b
0	$\{1, 0\}$	$\{0, 1\}$	
1			
2			
3			$\{1, 0\}$

Q Construct DFA equi to NDFA



a	b
$\{1, 0\}$	$\{0, 1\}$
$\{1, 0\}$	$\{0, 1\}$
$\{1, 0\}$	$\{0, 1\}$
$\{0\}$	$\{1, 0\}$
$\{1, 0\}$	$\{0\}$
$\{2\}$	$\{1, 0\}$
$\{3\}$	$\{0\}$

Q construct minimized DFA

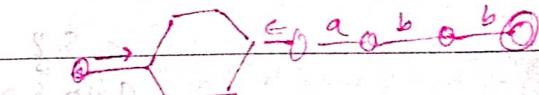
state	a	b
$\rightarrow A$	$\rightarrow B$	C
B	B	D
C	$\rightarrow B$	C
D	B	E
E	B	F

(fp)

$\pi_0 = \{E\} \{A, B, C, D\}$
 $\pi_1 = \{E\} \{A, C\} \{B, D\}$
 $\pi_2 = \{E\} \{A, C\} \{B\} \{D\}$

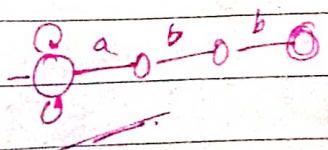
a	b
B	F
B	C
B	D
B	E

Q construct NFA for



R.E

$$R = (a|b)^* abb$$



LEX

~~Lexical Analyzer~~

Lang. need to specify lexical Analyzer.

it consists of

(1) Auxiliary define →

Distinct name

$D_1 = R_1 \rightarrow \text{regular exp}$

$D_n = R_n$

e.g.

IDENTIFIER = letter (letter | digit)*

D

R

Pattern

(2) Transiⁿ Rule →

$P_i \{ A_i \}$

$P_i \{ A_i \}$

e.g. Begin → { return(1, -) }

P

return A

Q. Consider the lex source prog.

AUX Def.

(none)

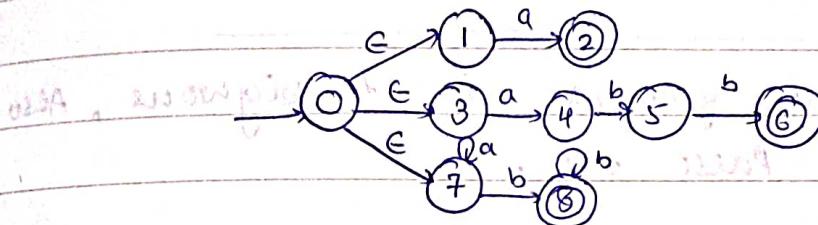
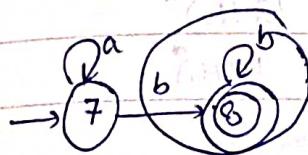
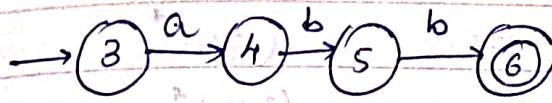
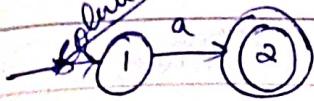
Transⁿ rule.

a { }

abb { } * Actions are omitted here *

a+ b+ { }

ddo (d|e)=2



Present state	a	b	Token formed
0137	247	8	0 - tokens

final 247 → 7 8 58 a

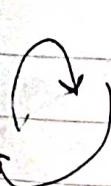
state 7 8 → 7 8 aabb a* b*

7 → 7 8 aabb a* b*

58 → - 68 a* b*

0137 → 68 → - 8 aabb a* b*

Q) Consider lex prog. given as:



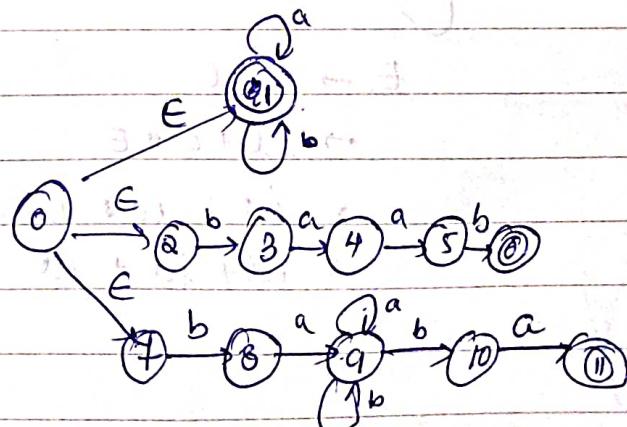
AD
(none)

TR

$(a+b)^*$??

baab ??

ba $(a+b)^*$ ba ??



Present state

a b

Token format

0127

1 138

138

149

1

$(a+b)^*$

149

159

$(a+b)^*$

159

19

1910

$(a+b)^*$

1910

1911

1910

"

UNIT:3

Q Show that the grammar is Ambiguous. Also construct Parse tree.

$$E \rightarrow E+E \mid E^* E \mid id.$$

Q what is CFG.

Q Find LMD, RMD and construct Parse tree for the given CFG

$$S \rightarrow icts$$

where i, t, e, S times for

$$S \rightarrow ic_t ses$$

it, then else

$$S \rightarrow a$$

LCD & S stand for

$$C \rightarrow b$$

"conditional" & "stmts".

$$E \rightarrow E+E$$

$$E \rightarrow E+E$$

$$\rightarrow id + E^* E$$

$$\rightarrow E + E^* E$$

$$\rightarrow id + id^* E$$

$$(d+0) \rightarrow id + id + id.$$

$$\rightarrow id + id + id$$

$$id + id + id$$

UNIT: 4

Parsing

start

- Top-down ↓
- bottom-up ↑

Predictive · Predictive

Recursive

Shift reduce Parsing. (consists of shifting of I/P symbols onto a stack until "side of production" appears)

operator precedence Parsing.

consider grammar (T)

abcede

$S \rightarrow aAcBe$

$A \rightarrow Ab|b$

$B \rightarrow d$

book 148

aA₁bcd₂e

a A₁c d₂e

a A₁c Be

↓
S.

a₁b₂c₃d₄e

book 157

right most derivation

RMD

Handle Pruning

bottom-up
conventional
reduce
regression
detained

SHIFT REDUCE PARSING

$E \rightarrow E + E | E * E | id$

Right Sentential form

$id_1 + id_2 * id_3$

Handle

id₁

Reducing Production

$E \rightarrow id$

$E + id_2 * id_3$
 $E + E * id_3$
 $E + E * E$

id₂

id₃

E * E

E * E

$E \rightarrow id$

$E \rightarrow id$

$E \rightarrow E * E$

$E \rightarrow E * E$

Handle
Pruning

Q) consider the following grammar for list & tree.

$$\left\{ \begin{array}{l} S \rightarrow a / \wedge / (T) \\ S \rightarrow T, S / S \end{array} \right. \quad \begin{array}{l} (T) \\ \equiv \\ \text{terminal non-terminal} \end{array}$$

right sent. form

$$\begin{aligned} & (a_1, (a_2, a_3)) \\ & (a_1, (a_2, S)) \\ & (a_1, (S, S)) \\ & (S, (S, S)) \\ & (S, (T, S)) \\ & (S, (T)) \\ & (S, S) \\ & (T, S) \\ & (T) \\ & S \\ & T, S \\ & (T) \\ & S \\ & T, S \\ & (T) \end{aligned}$$

handle

$$\begin{aligned} & a_3 \\ & a_2 \\ & a_1 \\ & S \\ & T, S \\ & (T) \\ & S \\ & T, S \\ & (T) \\ & S \\ & T, S \\ & (T) \end{aligned}$$

Reducing Prod

$$\begin{aligned} S & \rightarrow a_3 \\ S & \rightarrow a_2 \\ S & \rightarrow a_1 \\ T & \rightarrow S \\ T & \rightarrow T, S \\ S & \rightarrow (T) \\ T & \rightarrow S \\ T & \rightarrow T, S \\ S & \rightarrow (T) \\ T & \rightarrow S \\ T & \rightarrow T, S \\ S & \rightarrow (T) \end{aligned}$$

Jieme Badda

So Badda

a)

$$\begin{aligned} S & \rightarrow (T) \\ S & \rightarrow (T, S) \\ S & \rightarrow (S, (T)) \checkmark \\ S & \rightarrow (S, (T, S)) \\ S & \rightarrow (S, (S, S)) \\ S & \rightarrow (a, (S, S)) \\ S & \rightarrow (a, (a, a)) \end{aligned}$$

$$\begin{aligned} S & \rightarrow (T) \\ S & \rightarrow (T, S) \\ S & \rightarrow (S, S) \checkmark \\ S & \rightarrow ((T), S) \\ S & \rightarrow ((T, S), S) \\ S & \rightarrow ((S, S), S) \end{aligned}$$

LMD

$$\begin{aligned} S & \rightarrow ((T, S, S), S) \\ S & \rightarrow ((S, S, S), S) \\ S & \rightarrow (((T), S, S), S) \\ S & \rightarrow (((T, S), S, S), S) \\ S & \rightarrow (((S, S), S, S), S) \\ S & \rightarrow (((a, a), S, S), S) \\ S & \rightarrow (((a, a), (S, S)), S) \\ S & \rightarrow (((a, a), (a, a)), S) \end{aligned}$$

2)

$(a_4, (a_2, a_3))$	a_1	$s \rightarrow a_1$
$(s, (a_2, a_3))$	s	$T \rightarrow s$
$(T, (a_2, a_3))$	a_2	$s \rightarrow a_2$
$(T, (s, a_3))$	s	$T \rightarrow s$
$(T, (T, a_3))$	a_3	$s \rightarrow a_3$
$(T, (T, s))$	T, s	$T \rightarrow T, s$
$(T, (T))$	(T)	$s \rightarrow (T)$
(T, s)	T, s	$T \rightarrow T, s$
(T)	(T)	$s \rightarrow (T)$

RMD

Book 154

OPERATOR PRECEDENCE PARSING

$$1 - a = b$$

$$\alpha \sqsubseteq \beta$$

$\beta \rightarrow E$ or single NT

$$s \rightarrow i \ (\neq s_a)$$

$$C \rightarrow b \quad i \neq t$$

$$s \rightarrow i t s$$

$$i = t.$$

$$2 - <$$

$$\alpha A \beta$$

$$A \rightarrow \gamma b \delta$$

$\gamma \rightarrow E$ or Single NT

$$a < b$$

U CR CS

8. \rightarrow

$\alpha A b \beta$

$A \rightarrow \gamma a \delta$

$\delta \rightarrow E$ or single NR

$a \rightarrow b$

e.g.

$(T) \rightarrow E \rightarrow E \oplus T \mid T$

$T \rightarrow T \oplus F \mid P$

IF $\xrightarrow{\text{leading}}$ (E) $\xrightarrow{\text{mid}} \dots$ $\xleftarrow{\text{trailing}}$

NON TERMINAL | 1st terminal | last terminal

E. (, id, *, +) , id, *, +



T. (, id, *) , id *

F. (, id) , id

1st fixst:

+ T

+ < (, id, *, +)

id \rightarrow

& L.

T \rightarrow S

* F

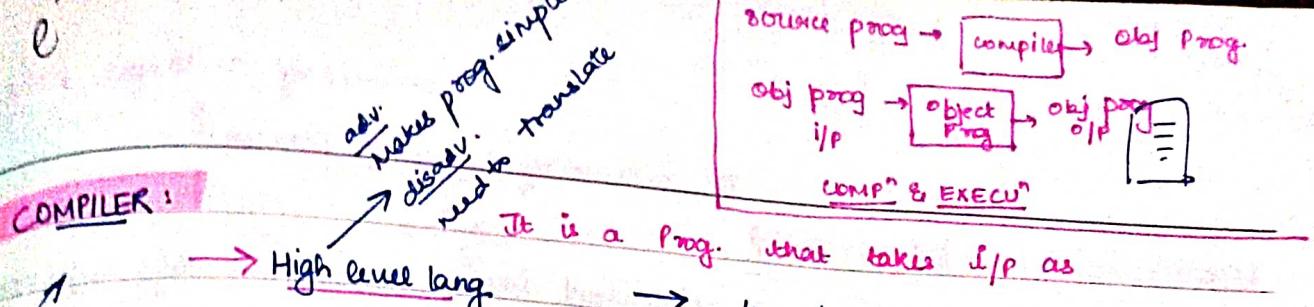
(E)

\rightarrow | E x
| F x
| E)

✓ HIGH LEVEL LANG: It makes the programming task easier, but it also introduces some prob.

i.e. There is a need to translate HLL into M/C L.

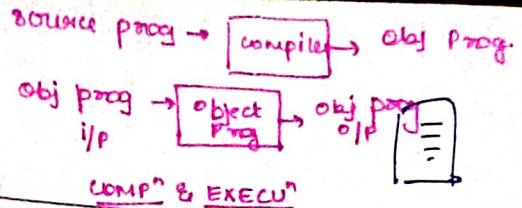
• It enables prog. to express algo. in more natural notations.



COMPILER:

It is a Prog. that takes I/p as High level lang.
eg. FORTRAN, COBOL etc.

adv: makes Prog. simple
disadv: need to translate



COMP" & EXECU"

low level lang.
eg. Assembly or M/C lang.

Execution of a prog. is done in 2 steps:
- compilation of source prog. into target prog.

- loading resulting Obj. prog. into M/M & executing.

smaller than compiler.
but slower execn

INTERPRETER:

Prog lang. → simplified lang. ≡
(Intermediate code)

- execution time is more (slower)
directly executed.
by INTERPRETER.

ASSEMBLER:

source target
Assembly lang. → M/C lang.

PREPROCESSORS:

High level lang of 1 form → high level lang of other form.

* NEED OF TRANSLATOR *

TRANSLATORS:

① coz M/C lang is in terms of bits, registers etc.

② complex algorithm is diff. to program

③ M/C lang Prog is cryptic & impossible to modify in convenient manner.

SYMBOLIC ASSEMBLY LANGUAGE:

In this a prog uses mnemonic names for both operation codes & data addresses. eg. ADD X, Y.

comp cannot execute

a prog in assembly lang

thus,

it is translated into M/C lang with the help of assembler.

(Text replacement capability)

MACROS:

can
translate into a sequence of assembly lang. stmts.

eg. MACRO DEFINITION

{ MACRO
ADD2
LOAD X
ADD Y
STORE X
END MACRO

give ADD2
name to macro
formal parameters.

now after defining we can use the macro ADD2 anywhere in Prog without defining its functionality.

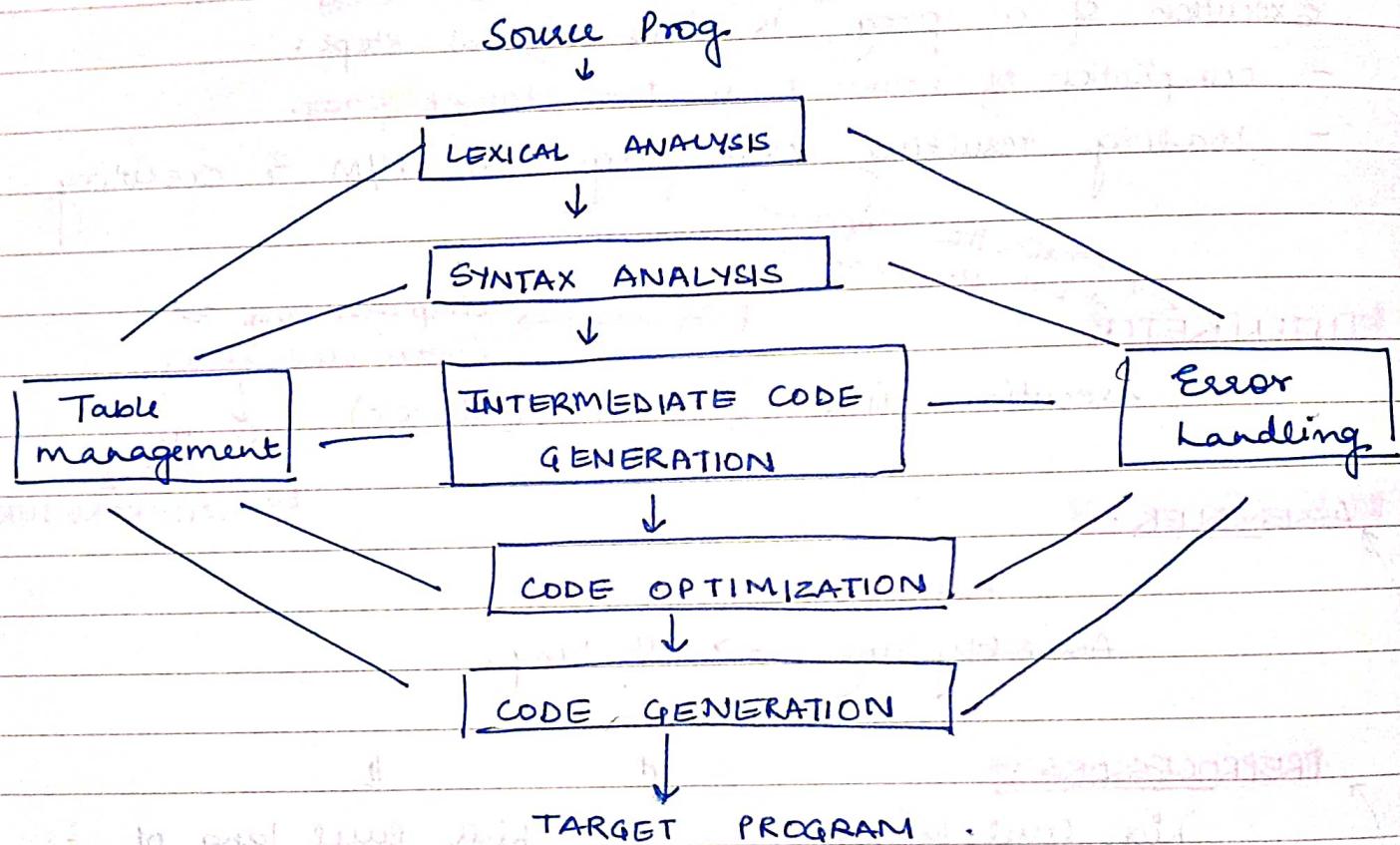
eg. ADD2 A,B → actual parameters ADD A
which will be translated into → STORE B

source prog \rightarrow target prog.

It is a very complex process. \uparrow

STRUCTURE OF A COMPILER:

compilation process is partitioned into a series of subprocesses called **PHASES**. (It is a logically cohesive operation, it takes 1 ip of source prog & produces o/p.)

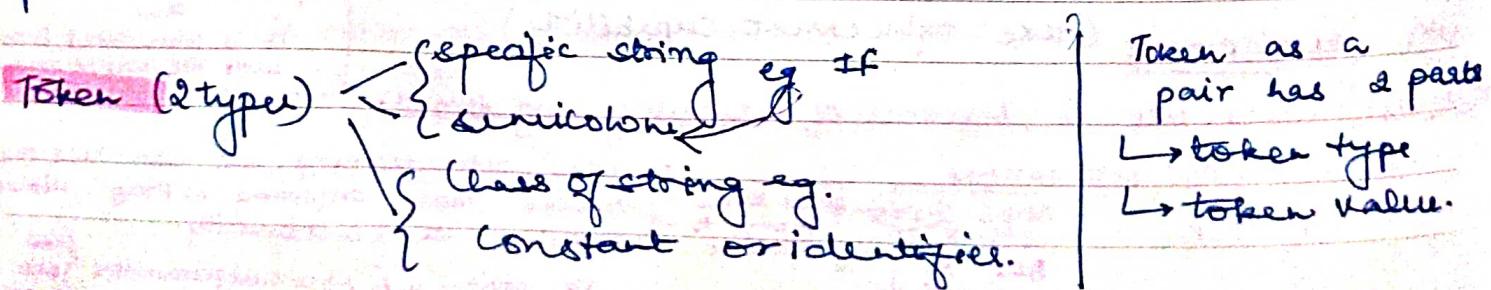


① LEXICAL ANALYSER:

Separates characters of source lang into groups that logically belong tog., these groups are called TOKENS.

The usual tokens are keywords eg. DO or IF.

'LA' is a stream of tokens, which is passed to next phase.



Finding token:

To find the next token the 'LA' examines successive characters in source prog.

② SYNTAX ANALYSIS:

It identifies which part of i/p stream should be grouped.
 Interpretations:
 → A/B is then C
 → or BAC is then A
 after this a parse tree is generated.

It groups tog. tokens into syntactic structures
 eg. 3 tokens representing $A + B$ might be grouped tog. into a structure called EXPRESSION. that

③ ICG:

may be combined to form STATEMENTS

It uses structure produced by the 'SA' to create a stream of simple instructions.

The diff. b/w IC & AC is that in IC we need not specify the registers.

3- Address code:

It is a popular type of intermediate lang.

A typical '3AC' statement is -

$\checkmark A := B \text{ op } C$

④ OPTIMIZATION: ~~# loop optimiza~~: move a computation that produces the same result each time around the loop to a point in prog. just b4 loop is entered.

It is an optional phase designed to improve the intermediate code so that the ultimate obj prog. runs faster or takes less space.

⑤ CODE GENERATION:

It converts intermediate code into a sequence of M/C instructions.

↳ selects registers in which computation will be done.

⑥ BOOKKEEPING: portion of computer keeps a names of used by the prog. & records essential info.
eg. types of integers, variables etc.

⑦ ERROR HANDLING: it is invoked when a flaw in the source prog. is detected.

PASS: reads o/p of previous pass → make transformation → write off into intermediate file.

COMPILER WRITING TOOLS:

eg. Scanner, parser generator etc.

Transition diag + finite automata

ways of designing token recognizers

Regular exp: suitable for describing tokens.

string: seq. of symbols.

any: set of strings.

uses of cross compiler: They are usually found in use for generating compilers for embedded system or multiple platforms.

It is a tool used for a platform where it is inconvenient to compile.

It is used in →

- Embedded comp
- compiling on same form
- compiling for multiple M/c
- Bootstrapping.

BOOTSTRAPPING:

A compiler is characterised by 3 lang:

- source lang
- obj lang
- lang. in which it is written.

These all languages are quite different.

CROSS COMPILERS: They run on one Mc to produce obj. code for another Mc.

Suppose, we have new lang 'L', which we want to make available on Mc 'A' & 'B'.

We write compiler C_S^{LA}

Produce obj.

in simple lang 'S'

It runs through

code for A.

↓
 C_A^{SA}

C_S^{LA}

C_A^{SA}

C_A^{LA}

Bootstrapping a compiler

C_L^{LB}

C_A^{LA}

C_A^{LB}

C_L^{LB}

C_A^{LB}

C_B^{LB}

Bootstrapping a compiler 2 Mcs

COMPILER CONSTRUCTION TOOLS:

A no. of tools have been developed specifically to help construct compilers.

They range from \leftarrow ^{Scanner generator}
 \leftarrow ^{parser generator}
to.
 \searrow complex systems.

The i/p specification of such tools contain:

- Lexical & syntactic structure of source lang.
- description about i/p.
- description of target M/C
- Parser generator: These produce syntax analyzers, parse trees, normally from the i/p is based on a context free grammar.
They utilize powerful algorithms, that are too complex to be carried out by hand.
- Scanner generator: They automatically generate, lexical Analyse normally, from the specification based on regular expression.
- Syntax-directed translation engine: They produce collection of routines that walk the parse tree generating intermediate code
- Automatic code generators: They take collection of rules for each operation of
Intermediate lang \rightarrow M/C lang.

PASS :

It is an implementation of a compiler.

It reads source prog → makes transformations specified
↓
writes out the output.

A no. of phases are grouped into a pass.

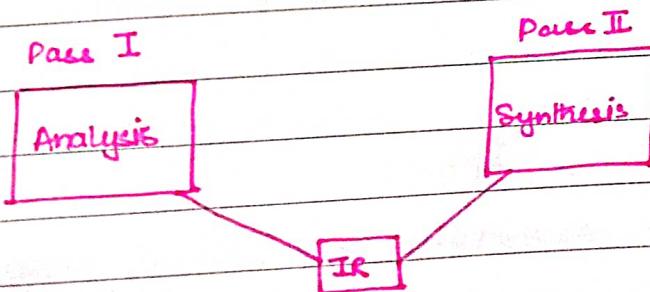
The environment in which the compiler operates can effect the no. of passes.

There are 2 passes:

- Single Pass
- Multipass.

A multipass compiler uses less space than single pass but.

MPC is faster than SPC as each file reads & writes an intermediate file.



MPC is better than SPC.

A one-pass assembler generates a skeleton of the M/c instruction the 1st time it sees it e.g. unit complete then append M/c address.

A two-pass assembler uses its 1st pass to enter into its symbol table a list of all identifiers together with M/c address.

e.g. c compiler 2nd pass replaces mnemonic, operation code eg GOTO by their M/c lang equivalent.

token

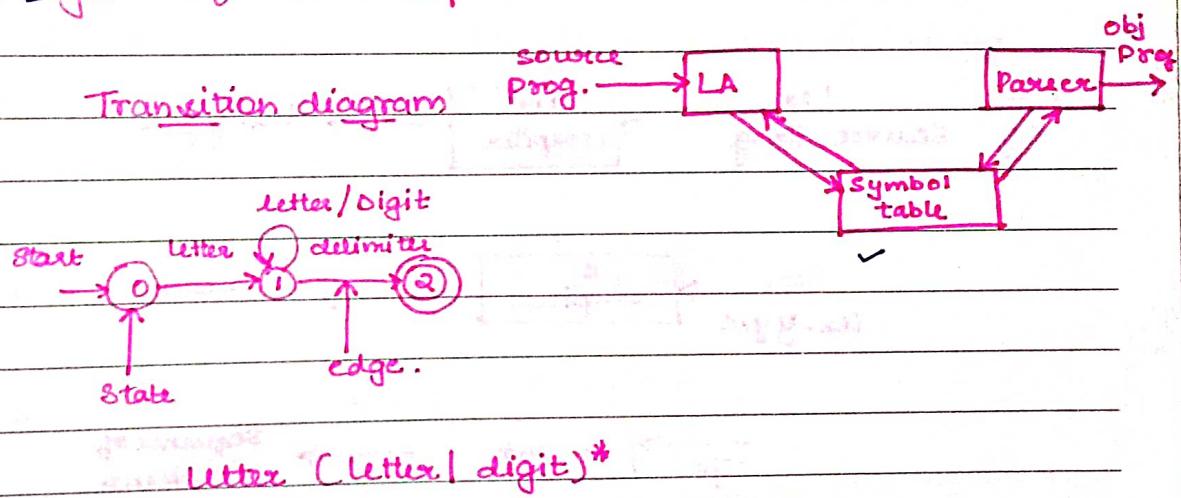
UNIT 2:

LEXICAL ANALYSER:

It could be a separate pass, placing its O/P on an intermediate file from which parser may then take its I/P.

It acts as a subroutine, which is called by the parser whenever it needs a token.

The purpose of splitting the analysis of the source prog. into two phases, ie lexical analysis & syntax analysis is to simplify the overall design of the compiler.

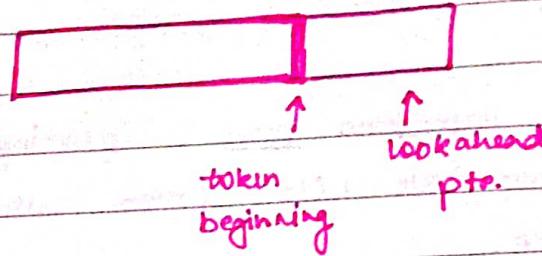


Input buffering:

The lexical analyser scans the characters of source prog. one at a time to discover tokens.

Often, characters beyond the next token may have to be examined before the next token can be determined.

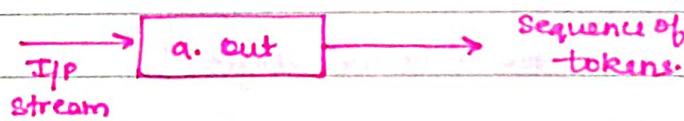
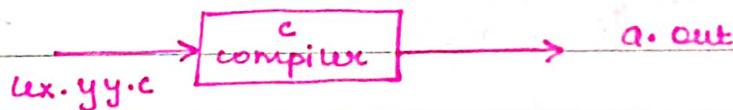
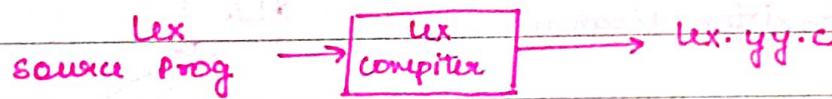
For this reason, it is desirable for LA to read its I/P from an I/P buffer.



LEX

It is a tool, that has been widely used to specify lexical analysers.

lex like specifications can be used even if lex compiler is not available.



It consists of:

1) Declarations

2) Auxiliary definition: $D_1 = R_1$

$$D_n = R_n$$

eg. A Identifier \rightarrow letter (Letter|Digit)*

3) Transition Rules:

$$P_1 \{ A_1 \}$$

$$P_n \{ A_n \}$$

e.g. Begin $\rightarrow \{ \text{return } (1, -) \}$

Lexem: It is I/P string or grammar.

NDFA

RE \rightarrow Recognise

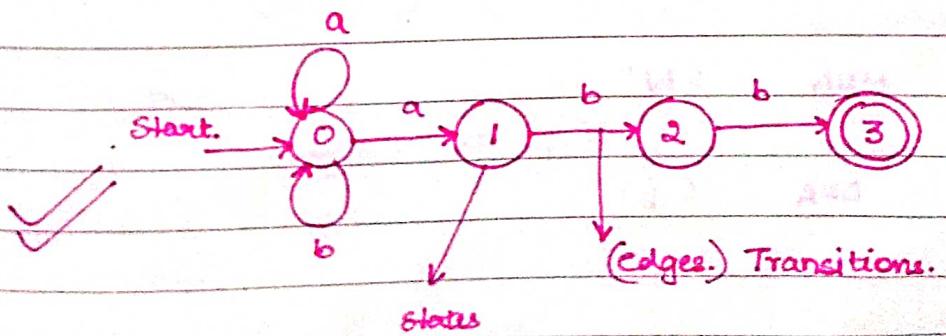
A better way to convert a regular expression to a recognizer is to construct a generalised transition diagram.

This diagram is called a NDFA.

A NDFA recognising lang $(a|b)^* abb$

transition table.

State	Input symbol	
	a	b
0	{0,1}	{0}
1	-	{2}
2	-	{3}



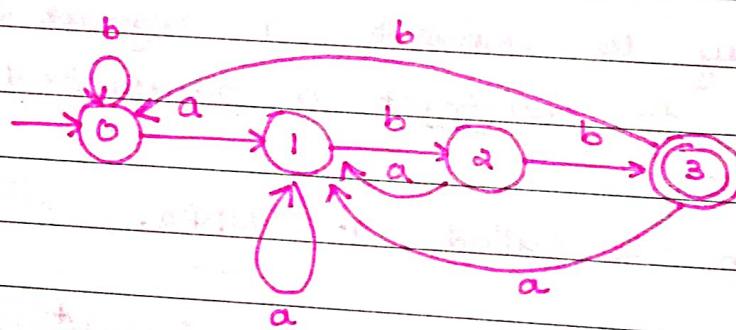
The lang defined by NFA is set of i/p strings it accepts.

DFA

In deterministic FA:

- There are no transitions on i/p ϵ
- For each state s and i/p symbol a , there is at most one edge labeled a leaving s.

DFA accepting $(a|b)^* abb$



DFA is easier to simulate than NFA.

States of DFA represent subsets of all sets of NFA.

NFA \rightarrow DFA.

I/P: An NFA ' N '

O/P: A DFA ' D '

METHOD:

E-CLOSURE

begin

push all state onto STACK

E-CLOSURE(T) := T ;

while STACK not empty

do

begin

pop 's' → tapelement from stack

for each

edge from s to t labeled $\epsilon \in Q_0 \rightarrow$

do

if t is not in E-CLOSURE(T)

do

begin

add t to E-CLOSURE(T);

push t onto STACK DFA on i/p string

end

end

end.

DFA is a 5-tuple $(Q, \Sigma, \delta, q_0, F)$

$Q \rightarrow$ finite set of states

$\Sigma \rightarrow$ " " " i/p symbols

$\delta \rightarrow$ transition func

$q_0 \rightarrow$ a start state

$F \rightarrow$ a set of final states

$q_1 \rightarrow$ initial state

If q is accepting then return "yes"

for $j = 1$ to n do

Begin

$q_j = \square$ goto $[q, s_j]$ end

If q is accepting then return "yes"

End.

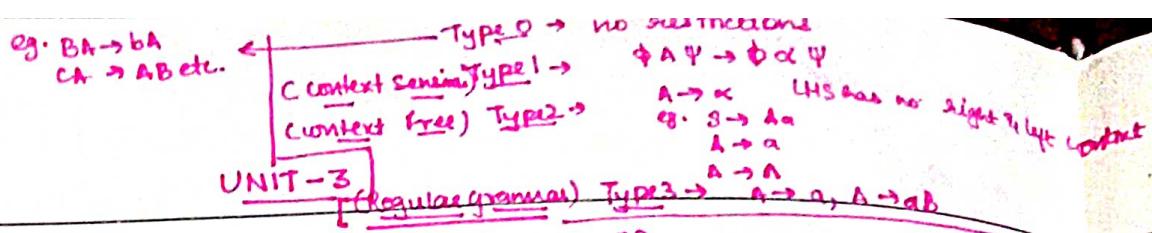
Return "NO"

End.

$$R = Q \times R^P$$

$$R^P = Q^P +$$

$$R$$



CFG:

For the syntactic specification of a progr. lang we use a notation called a CONTEXT-FREE GRAMMAR.

- It gives a precise, yet easy to understand, syntactic specification.
- An efficient parser can be constructed automatically.

A grammar imparts a structure to a progr. that is useful for its implementation.

- ↳ Translation into obj code.
- ↳ detection of errors.

A grammar involves:

- Terminals

they are basic symbols in a string.

They are $\rightarrow a, b, c, (,), +, -, \dots, 1, 2, \dots, id$ etc.

- Nonterminals

Special symbols that denote sets of strings.

They are \rightarrow expression, E, A, S etc.

- Start Symbol

- productions

Types

CFG

CNF

GNF

CNF
GNF

Chomsky Normal Form: A 'CFG' G is in CNF if every production is of form:

$$\begin{cases} A \rightarrow a & \text{or} \\ A \rightarrow BC & \text{and} \\ S \rightarrow \Lambda \end{cases}$$

Theorem:

1) Elimination of NULL and UNIT production.

$$\underline{A \rightarrow \Lambda}, \quad \underline{A \rightarrow B}$$

2) Elimination of terminals on RHS.

3) Restrict no. of variables on RHS.

eg. $S \rightarrow ABC$

$S_1 \rightarrow AB$

Final form: $S \rightarrow S_1 C$

Greibach Normal Form: A 'CFG' G is in GNF if

every prodn is of form:

$$\begin{cases} S \rightarrow \Lambda \\ S_i \rightarrow aBCD \dots \\ A \rightarrow a \end{cases}$$

Theorem:

1) Remove NULL and UNIT production.

2) Renaming Variables.

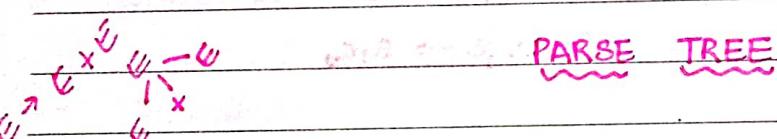
3) Substituting by looking at the index.

i.e. if LHS index $>$ RHS

4) Apply LEMMA THEOREM.

Regular expr & CFG:

- For RE' lexical rules are simple & we don't need powerful notations.
- 'RE' notation is easier to understand.
- It is easier to construct efficient recognisers from 'RE' than 'CFG'.
- Separating syntactic structure into
 - lexical part
 - non lexical part provides manageable-sized components.



It is a graphical representation for derivations.

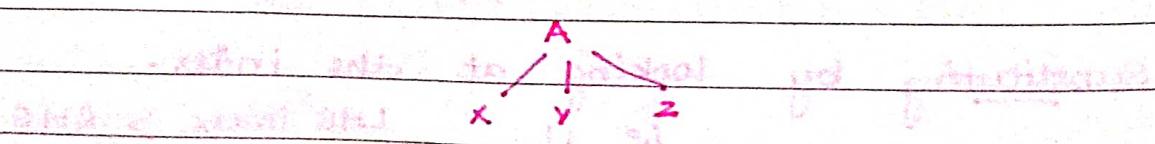
This representation is called a "parse tree".

It makes hierarchical syntactic structure of sentences.

Each interior node of parse tree is labeled by some nonterminal A. & ^{rest by the nodes} ~~symbols are~~ labeled from left to right by the symbols.

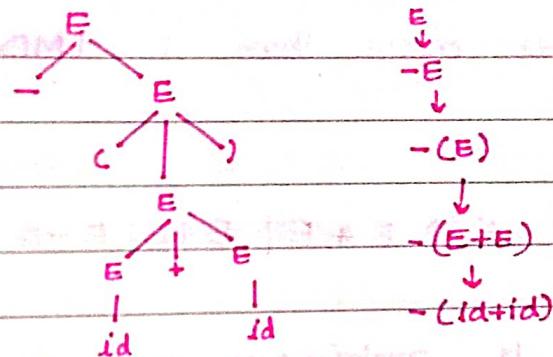
e.g.

$$A \rightarrow XYZ$$



The leaves of the tree are labeled by non-terminals or terminals, read from left to right.

e.g. Parse tree for $-(id + id)$



e.g.

statement: $id + id * id$

period has 2 LMD

id will stand =

$$E \rightarrow E+E$$

$$E \rightarrow E * E$$

$$E \rightarrow id + E$$

$$E \rightarrow E+E+E$$

$$E \rightarrow id + E * E$$

$$E \rightarrow id + E * E$$

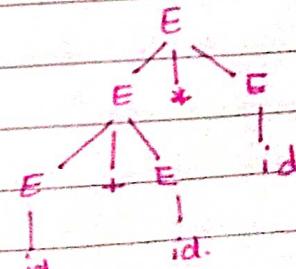
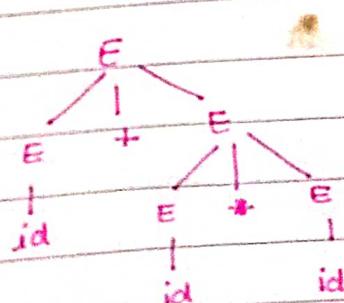
$$E \rightarrow id + id * E$$

$$E \rightarrow id + id * E$$

$$E \rightarrow id + id * id$$

$$E \rightarrow id + id * id$$

Their parse tree:



Ambiguity: A grammar that produces more than one parse tree for some sentence is said to be "ambiguous".

A grammar that produces more than one parse tree for some sentence is said to be "ambiguous".

It produces more than 1 LMD or RMD for same sentence.

e.g. $E \rightarrow E + E \mid E * E \mid E - E \mid E / E \mid E \uparrow E \mid (E) \mid -E \mid id$

This is ambiguous grammar. but we can



disambiguate

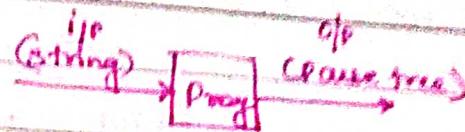
both these grammars by specifying

- Associativity
- Precedence of operators.

Ambiguous grammar fails to be LR.

UNIT-4

PARSERS:



A parser for a grammar is a prog that takes IP as a string & produces OP as a parse tree. If IP is a sentence or leaves an error msg.

Bottom up

build parse tree from bottom to top.

shifted

operator procedure

Top-down

It works with root & then moves to the leaves.

Predicive

Recursive

eg. $S \rightarrow iCtS$
 $S \rightarrow iCtSeS$

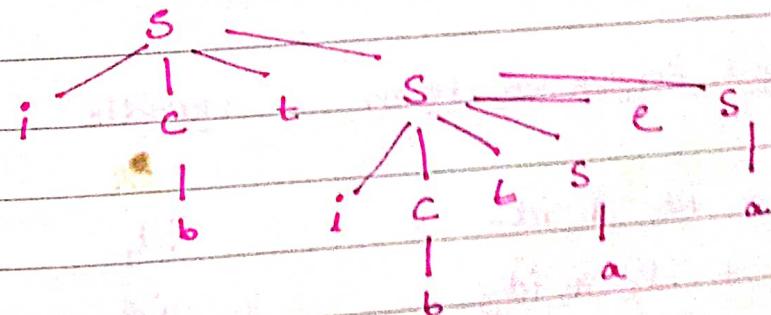
$a \rightarrow$
 $c \rightarrow b$

$\therefore S \Rightarrow ictS$

$\Rightarrow ictictses$

$\Rightarrow ibtibtæs$

$\Rightarrow ibtibtaea$



SHIFT REDUCE PARSING

This parsing method attempts to construct a parse tree for an i/p string beginning at the leaves and working up towards root.

- Each replacement of right side producⁿ by left side is called "Reduction".
It is the element ie reduced.
- A "handle" of right sentential form γ is a production $A \rightarrow p$ & a position of γ where string p may be found & replaced by A .
- If grammar is unambiguous then it has only 1 handle
- "Handle Pruning" : A RMD in reverse is called 'canonical reduction sequence' & is obtained by HP.

Q. i/p $\rightarrow id_1 + id_2 * id_3$
reduces to $\rightarrow E$

<u>Right Sentential Form</u>	<u>Handle</u>	<u>Reducing producⁿ</u>
------------------------------	---------------	------------------------------------

$id_1 + id_2 * id_3$	id_1	$E \rightarrow id$
----------------------	--------	--------------------

$E + id_2 * id_3$	id_2	$E \rightarrow id$
-------------------	--------	--------------------

$\rightarrow E + E * id_3$	id_3	$E \rightarrow id$
----------------------------	--------	--------------------

$E + E * E$	$E * E$	$E \rightarrow E * E$
-------------	---------	-----------------------

$E + E$	$E + E$	$E \rightarrow E + E$
---------	---------	-----------------------

E		
-----	--	--

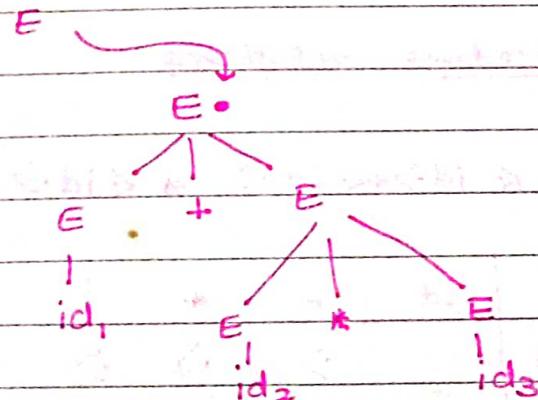
There are 2 problems:

- ① To locate handle in LR(0) FSR
- ② what product to choose if more than 1 product.

∴ we use 'stack'

stack	i/p	Action	Result
\$	id ₁ + id ₂ * id ₃ \$	shift	
\$ id ₁	+ id ₂ * id ₃ \$	reduce E → id	
\$ E	+ id ₂ * id ₃ \$	shift	
\$ E +	id ₂ * id ₃ \$	shift	
\$ E + id ₂	* id ₃ \$	reduce by E → id	
\$ E + E	* id ₃ \$	shift	
\$ E + E *	id ₃ \$	shift	
\$ E + E * id ₃	\$	reduce by E → id	
\$ E + E * E	\$	reduce by E → E * E	
\$ E + E	\$	reduce by E → E + E	
\$ E	\$	accept	

Parse tree



OPERATOR PRECEDENCE PARSING

* These grammars have property:

- no production Right side is $S \xrightarrow{+} G$
- no 2 adjacent non-terminals $\xrightarrow{+} AA$

* disadv of DP:

① hard to handle tokens eg (-) has 2 precedences.

② Only small class of grammars can be parsed.

* 3 disjoint 'precedence relations':

$a < b$ a yields precedence to B

$a > b$ a takes precedence over B

$a = b$ same precedence.

* Operator precedence relations

eg. ① $\$ < id > + < id > * < id > \$$

Diagram illustrating the construction of a parser for the expression $\$ < id > + < id > * < id > \$$. The table shows the grammar symbols and their precedence relations:

	id	$+$	$*$	$\$$
id	$>$	$>$	$>$	
$+$	$<$	$>$	$<$	$>$
$*$	$<$	$>$	$>$	$>$
$\$$	$<$	$<$	$<$	

The diagram also includes annotations for the Boom table, grammar symbols, and various precedence relations (e.g., $a < b$, $a > b$, $a = b$) indicated by arrows and numbers (1, 2, 3, 4, 5, 6).

OPR (Associativity + Precedence)

* Asso. + Prece. rules:

① If Θ_1 has higher precedence than Θ_2 make

$$\Theta_1 \rightarrow \Theta_2$$

$$\Theta_2 \leftarrow \Theta_1$$

eg. $(* \rightarrow +) \Theta_1 (+ \leftarrow *)$

② If Θ_1 & Θ_2 have equal precedence. make

$$\begin{array}{l} \Theta_1 \rightarrow \Theta_2 \\ \Theta_2 \rightarrow \Theta_1 \end{array}] \text{ left association}$$

or

$$\begin{array}{l} \Theta_1 \leftarrow \Theta_2 \\ \Theta_2 \leftarrow \Theta_1 \end{array}] \text{ right association.}$$

$$\begin{array}{l} \Theta \rightarrow \$ \\ \$ \leftarrow \$ \end{array}$$

$$E \rightarrow E+E \mid E-E \mid (E) \mid \text{id.}$$

$$\$ \leftarrow E \rightarrow + \leftarrow E \rightarrow \$$$

$$\text{precedence} \left[\begin{array}{c} \$ \leftarrow id + id - id \Rightarrow \$ \\ E \rightarrow E + E - E \end{array} \right] \text{id + id} \quad \begin{array}{c} (+) \\ (+-) \end{array}$$

(\approx)	$+ \rightarrow +$	$+ - id (\) \$$	$(-)$
$\$ \leftarrow (- \rightarrow -)$	$+$	$> > < \textcircled{<} > >$	$\leftarrow > +$
$) \rightarrow \$ + \rightarrow -$	$-$	$> > < \textcircled{<} > >$	$\leftarrow < +$
$\$ \leftarrow id - \rightarrow +$	id	$> > \textcircled{*} > >$	$\leftarrow < +$
$id \rightarrow \$$	$(\leftarrow < \textcircled{<} \leftarrow \textcircled{<} \mid \textcircled{=}$	$\textcircled{*}$	$\textcircled{-}$
$(\leftarrow (\textcircled{*}) \rightarrow)$	$\textcircled{>} \textcircled{>} \textcircled{*} \textcircled{>} \textcircled{>} \textcircled{>}$	$\textcircled{*}$	$\textcircled{-}$
$) \rightarrow \$$	$\$ \leftarrow < \textcircled{<} \textcircled{<} \textcircled{<} \textcircled{<} \textcircled{*}$	$\textcircled{*}$	$\textcircled{-}$
$(\leftarrow id id \rightarrow)$			

what is (α, β) if id ↑ → lowest.

repeat forever
if only ϵ on stack & on ip then
accept
and break
else

OPERATOR PRECEDENCE GRAMMAR :

$$\textcircled{1} \quad a \div b$$

if in $\alpha a \beta b \gamma$

ϵ or single non-terminal

$$\textcircled{2} \quad a < b$$

if in $\alpha a A \beta$

$A \Rightarrow r b s$



ϵ or single non-terminal

$$\textcircled{3}$$

$a > b$

if in $\alpha A b \beta$

$A \Rightarrow r a s$



ϵ or single non-terminal

LEADING :

I/P: A CFG e.g.

O/P:

Boolean Array L [A, a]

in which $A \cdot q_a$ is true

if & only if a is in LEADING [A]

Method:

INITIAL(A, a) \rightarrow It sets $L[A, a] = \text{true}$
if it was not true

Place (A, a) on STACK

procedure INSTALL (A, a);

if not $L[A, a]$ then

begin

$L[A, a] := \text{true};$

push (A, a) onto STACK

end.

Main Procedure:

begin.

1) for each non terminal A to terminal a

do

$L(A, a) := \text{False};$

2) For each product of form

$A \rightarrow a\alpha$ do

or $A \rightarrow B\alpha$ do

INSTALL (A, a);

3) while STACK not empty do

begin

3) Pop top pair (B, a) from STACK

4) for each product of form

$A \rightarrow B\alpha$ do

INSTALL (A, a);

end() until no more in stack

end.

→ Trailing is Analogous to this

COMPUTING OPERATOR PRECED^{^2} RELA[?]:

I/P: operator grammar ${}^e G$

O/P: The relations \prec, \doteq, \succ for G .

METHOD:

- Compute LEADING(A)
- TRAILING(A)

a) Set $\$ \prec a$ for all a in LEADING($\$$)

& $b \succ \$$ for all b in TRAILING($\$$)

for

$A \rightarrow X_1 X_2 \dots X_n$ do
for $i := 1$ to $n-1$ do

begin

if X_i & X_{i+1} are terminals then set $X_i \doteq X_{i+1}$

if X_i is terminal & X_{i+1} is nonterminal
then set for all leading(X_{i+1})
 $X_i \prec a$

if X_i is nonterminal & X_{i+1} is terminal
then set for all trailing(X_i)
 $a \succ X_{i+1}$

End.

	\leftarrow	\rightarrow	\leftarrow	\rightarrow
$E \rightarrow E + T \mid T$				
$T \rightarrow T * F \mid F$				
$F \rightarrow (E) \mid id$				
for $E \quad \textcircled{1} \leftarrow T \xrightarrow{\textcircled{2}} \textcircled{3} \leftarrow F$				

	$+$	$*$	$($	$)$	id	\leftarrow	\rightarrow
$+$	$>$	$<$	$<$	$>$	$<$	$>$	$>$
$*$	$>$	$>$	$<$	$>$	$<$	$>$	$>$
$($	$<$	$<$	$<$	$=$	$<$		
$)$	$>$	$>$		$>$			
id	$>$	$>$		$>$			
\leftarrow	$<$	$<$	$<$	$<$	$<$	$>$	
\rightarrow							

① (\doteq)

② FIRST:

\leftarrow

1) $+ T$

E

③ LAST:

ie $+ < \cdot *$ | $+ < \cdot ($ | $+ < \cdot id$

④ $E +$

2) $(* F)$ | (E) | id

$* > +$

ie $* < ($ | $* < id$

⑤ $T *$

3) (E)

$* > *$ | $) > *$ | $id > *$

$(< id$, $(< *$, $(< +$) $<$

⑥ E

$* >)$ | $+ >)$ | $) >)$ | $id >)$

we add

$* > \$$	$\$ < *$
$+ > \$$	$\$ < +$
$) > *$	$\$ < ($
$id > *$	$\$ < id$

SYMBOL TABLE

① A symbol table is merely a table with 2 fields, ie.

- Name
- information

② We need to be able to:

- determine whether a given name is in table.

- add new name

- access info. associated with given name

- add new info ^{for} ~~associated with~~ a name.

- delete a name.

③ The info. collected about a name includes:

- Type of characters (eg. integer)

- form (eg. a structure)

- location other attributes.

- offset describing position in storage.

④ Each time a name is encountered, the symbol table is searched to see whether that name has been seen previously.

If name is new, it is entered into symbol table.

Info. is entered into table during - lexical

- syntactic analysis.

- info in symbol table is used during several phases of compilation
- detect error & correct it.

④ A linear list → simple but slow

A hash table → fast but more complex.

Tree → intermediate performance.

SYMBOL TABLE RECORDS :

The simplest way to implement a symbol table is as linear array of RECORDS.

It consists of known no. of words of memory.

RECORD

dimple
attribute

identifier

information.

STORING IDENTIFIER NAME

In separate array

length of identifier

6

Sattribute2

Sattribute3

identifier

information.

R F D I M P L E

INDIRECTION: It can save space in representing identifiers.

The most sig. adv of this is when we have a type of info that is applicable to only a minority of entries.

④ A sensible way to allocate space is:

	extra space for arrays etc
	space for - simple names - ptr to array etc.
	fixed data eg. Return address.

REUSING SYMBOL TABLE SPACE:

The identifier used by programmer to denote a particular name in symbol table must be preserved.

It is essential as all uses of the identifier can be associated with the same symbol-table entry & hence same name.

Exception: External names.

When NAME is declared external to prog. its IDENTIFIER must be preserved.

A compiler when finds a name ie external,
↓

makes a copy of NAME in another small array
↓

Then POINTER to the identifier is made to point
the new copy of NAME.

→ we can reclaim the space used to store identifier.

e.g.

TWO-ARRAY SYMBOL TABLE

Identifier	Information
ai ↓ DIMPLE	si ↓ attributes of DIMPLE

DATA STRUCTURES:

① ARRAYS: Data struc. of same kinds.

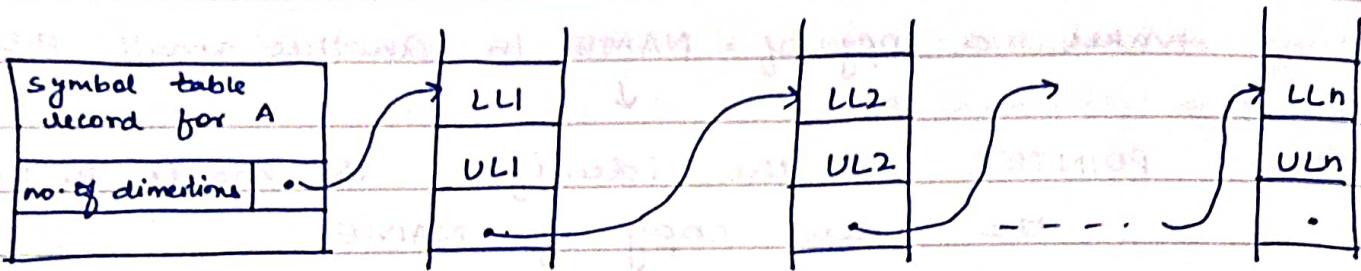
If a language places a limit on no. of dimensions
then all subscript info. is placed in
symbol table.

If an expression is constant its value can be
stored in symbol table.

We may store,

- no. of dimensions
- pointer to linked list
- element holding upper limit & lower limit.

as in, size of storage required depends upon the number of dimensions.



no. of limitation of dimension.

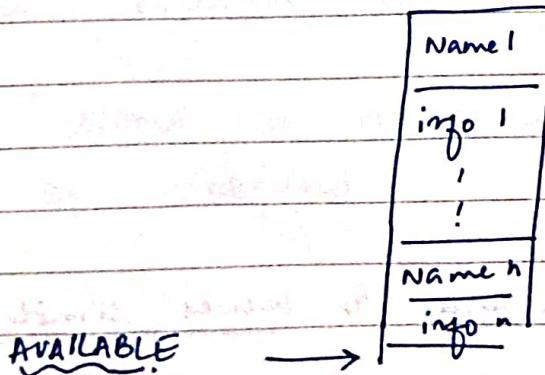
eg. FORTRAN limits to 3 dimensions.

FORTRAN		
Symbol Table Record	Dimension	Associated Data
	A	
UL1	UL2	
UL3	B	

symbol table record for FORTRAN

② LISTS: They are simpler & easiest to implement data struc. for symbol table.

we use single array or a group of arrays to store names & their associated info.



To retrieve info about a name:

we search from beginning of array till position marked by AVAILABLE.
(indicate beginning of empty portion of array)

When name is located, its associated info. can be found

but, if we reach AVAILABLE WITHOUT FINDING THE NAME we have a FAULT

To insert a name:

we scan down the list to be sure it ~~does~~ is not already there.

if it is there
then we hv a fault
of → multiply defined name

If not, then
we store new name
immediately after AVAILABLE
UPDATE the pointer.

To insert n names & m enquires

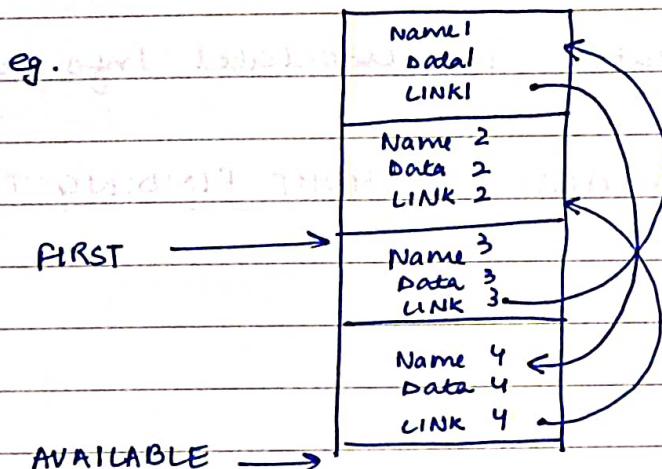
$$\text{Total work} = \underline{\underline{O(n+m)}}$$

constant

self organised lists

It saves fraction of time spent in searching the symbol table.

there is a LINK field in each record.



Order is 3, 1, 4, 2.

⑧ SEARCH TREES:

It is a more efficient approach to symbol table organisation.

It has 2 link fields:

LEFT RIGHT

to each record.

We use these fields to link the records in Search tree.

If names are encountered randomly → The avg. length of path will be proportional to $\log n$

Av length of $\log n$ where $n \rightarrow$ no. of names.

Since each search follows 1 path from root

TIME for n names \propto

m inquiries \propto

$$(n+m) \log n$$

① Take $P \rightarrow$ pointer, ..

(1) while $P \neq \text{NULL}$ do

(2) if $\text{NAME} = \text{NAME}(P)$

.. "Name found"

(3) else if $\text{NAME} < \text{NAME}(P)$

then

$P := \text{LEFT}(P)$

(4) else if

$\text{NAME}(P) < \text{NAME}$

then

$P := \text{RIGHT}(P)$

④ HASH TABLES: It is a superior method.

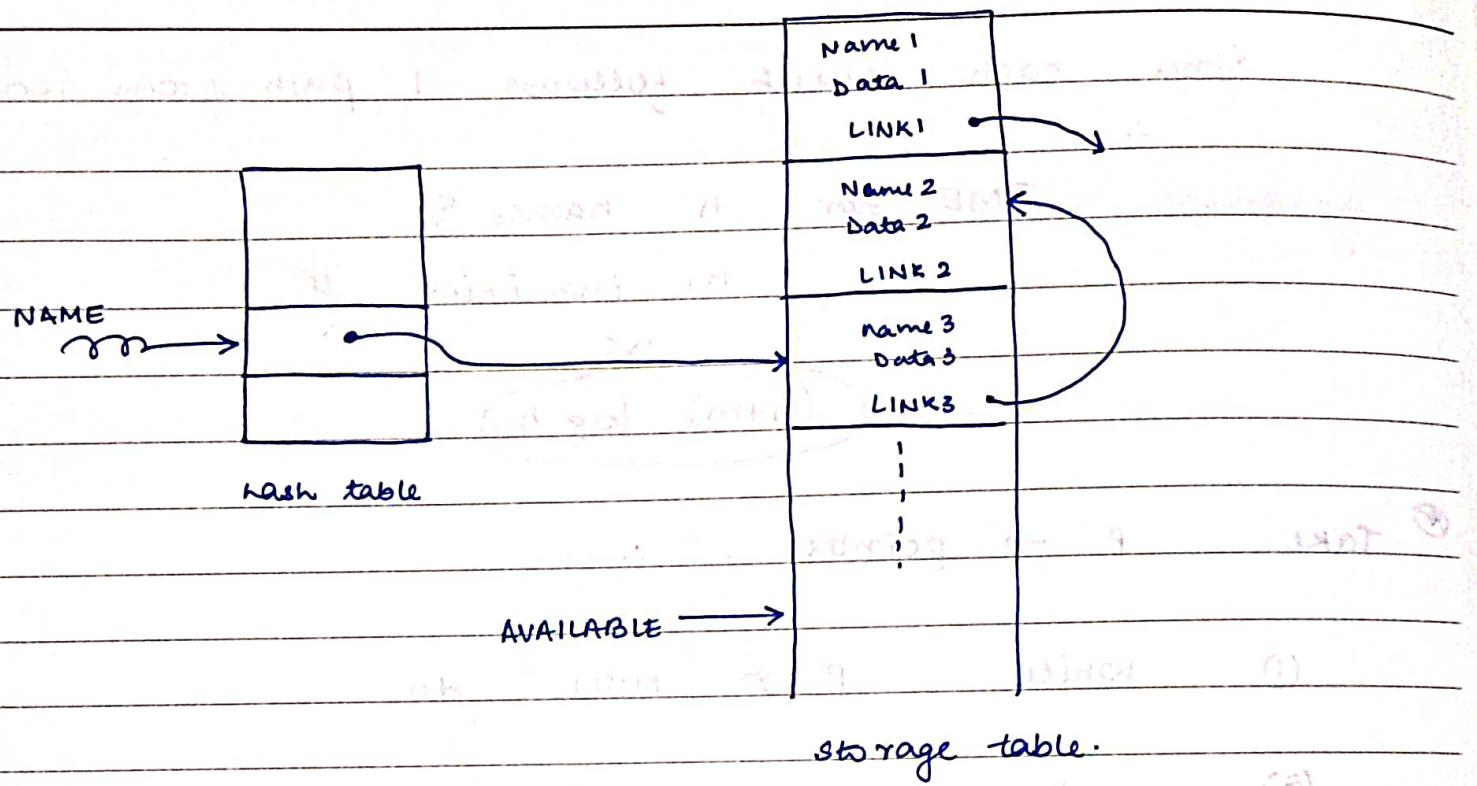
It gives us capability of performing
 $m \rightarrow$ accesses

$n \rightarrow$ names

in

Time $\propto \frac{n(n+m)}{K}$

$K \rightarrow$ constant.



It has 2 tables

Hash table consists of k words

$$0 \dots k-1$$

they are ptr into storage table.

① we apply name to Hash table

& apply hash function $h(\text{NAME})$

it is an integer between $0-k$

SEARCH

To inquire about NAME, one computes $h(\text{NAME})$

to search list

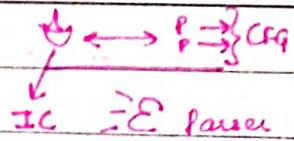
② Create a record in the available space in storage table

link record to beginning
of $h(\text{NAME})$ list.

ENTER

UNIT-6

SYNTAX DIRECTED TRANSLATION:



It allows subroutines to be attached to a product of a context-free grammar.

These subroutines generate intermediate code when called by a parser.

They are useful becoz it enables the compiler designer to express the generation of IC directly in terms of syntactic structure.

Semantic action:

A syntax directed translation scheme is merely a CFG in which an OUTPUT ACTION is associated with each product.

OUTPUT ACTION may involve:

- computation of values.
- generation of IC.

- Printing errors etc.

for building a product $E \rightarrow E^{(1)} + E^{(2)}$ Semantic action

$$E \rightarrow E^{(1)} + E^{(2)} \quad \{ E.\text{VAL} := E^{(1)}.\text{VAL} + E^{(2)}.\text{VAL} \}$$

Value associated with grammar symbol is TRANSLATION.

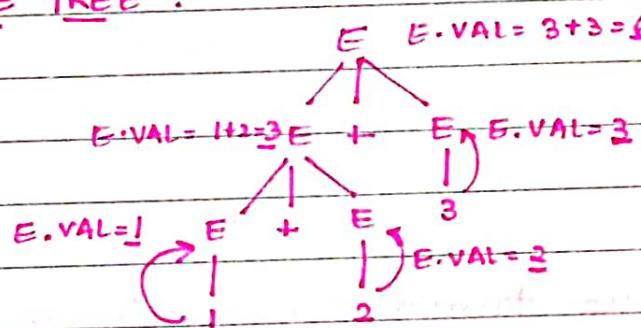
eg. $A \rightarrow xyz \{ y.\text{VAL} := 2 * A.\text{VAL} \}$

In this translation of non-terminal on right side is defined in terms of translation of non-terminal on left.

This is INHERITED TRANSLATION.

$E.\text{VAL} \rightarrow$ is integer valued translation

values of translations are determined by constructing a PARSE TREE.



Parse tree for $1+2+3$

eg. $E.\text{VAL} := E^{(1)}.\text{VAL} + E^{(2)}.\text{VAL}$

IMPLEMENTATION:

It is a convenient description of what we would like done.

The DPL defined is independent of kind of parser used.

It is easy to modify.

eg. specify 'Desk calculator' for i/p $23 * 5 + 4$
 e. o/p 119

The productions:

$$S \rightarrow E \$$$

$$E \rightarrow E + E$$

$$E \rightarrow E * E$$

$$E \rightarrow (E)$$

$$E \rightarrow I$$

$$I \rightarrow I \text{ digit}$$

$$I \rightarrow \text{digit}$$

SYNTAX DIRECTED Xⁿ SCHEME

Production Semantic Action

$$S \rightarrow E \$ \quad \{ \text{print } E.\text{VAL} \}$$

$$E \rightarrow E^{(1)} + E^{(2)} \quad \{ E.\text{VAL} := E^{(1)}.\text{VAL} + E^{(2)}.\text{VAL} \}$$

$$E \rightarrow E^{(1)} * E^{(2)} \quad \{ E.\text{VAL} := E^{(1)}.\text{VAL} * E^{(2)}.\text{VAL} \}$$

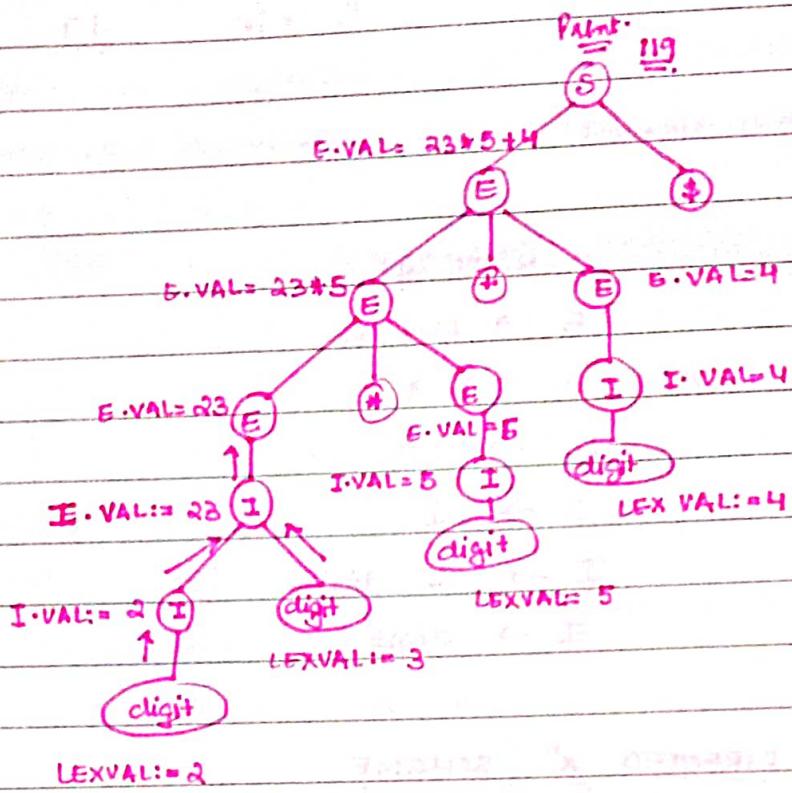
$$E \rightarrow (E^{(1)}) \quad \{ E.\text{VAL} := E^{(1)}.\text{VAL} \}$$

$$E \rightarrow I \quad \{ E.\text{VAL} := I.\text{VAL} \}$$

$$I \rightarrow I^{(1)} \text{ digit} \quad \{ I.\text{VAL} := 10 * I^{(1)}.\text{VAL} + \text{LEXVAL} \}$$

$$I \rightarrow \text{digit} \quad \{ I.\text{VAL} := \text{LEXVAL} \}$$

Parse tree



PARSE TREE AND SYNTAX TREE

graphical representation of source prog.

It is very useful sply in OPTIMIZATION PHASE

as in this the code needs to be extensively restructured.

It is a variant of Parse tree, in which:

each leaf * represents operand &

Each interior node rep. operator

E. VAL is a translation whose value is a pointer to a node * in syntax tree.

④ The function NODE (OP, LEFT, RIGHT)

take 3 arguments.

1st → name of operator.

2nd & 3rd → ptr's to root of subtree.

It creates a new node & makes 2nd &
3rd Arg. the left & right children.

eg:

$E \rightarrow E^{(1)} \text{ op } E^{(2)}$ $\{ E.\text{VAL} := \text{NODE} (\text{op}, E^{(1)}.VAL, E^{(2)}.VAL) \}$

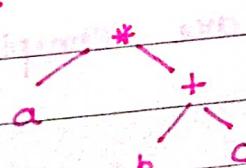
⑤ The function UNARY (OP, CHILD)

makes new node labeled OP & CHILD its
child.

⑥ The function LEAF (ID)

creates new node labeled ID & returns ptr
to that node.

eg. Syntax tree



THREE-ADDRESS CODE, QUADRUPLES AND TRIPLES:

① 3 add. code:

It is a sequence of statements, typically of general form

[A := B op C]

where A, B & C are either \rightarrow prog. defined names or temp names etc.

& op stands for operator ie \rightarrow fixed

\rightarrow arithmetic etc.

It is called "3-add code" as each statement usually contains

\rightarrow 3 addresses $\begin{cases} 2 \text{ for operands} \\ 1 \text{ for result} \end{cases}$

eg. "X + Y \rightarrow Z" yield

T₁ := Y + Z

T₂ := X + T₁

T₁ & T₂ are compiler generated Temp names.

② Some statements are:

① A := B op C

③ unconditional Jump ie goto L

② A := op B (unary op)

④ conditional jump ie

eg. Shift

if A relop B goto L

⑤ Index stmts

eg. A := B[I] &

Arr := B

QUADRUPLES:

It has 4 fields ie \rightarrow OP

ARG 1

ARG 2

RESULT

This representation of 3-add code is Quadruples.

e.g.

$$A := -B * (C + D)$$

$$\therefore T_1 := -B$$

$$T_2 := C + D$$

$$T_3 := T_1 * T_2$$

$$\text{result} \rightarrow A := T_3$$

	OP	ARG1	ARG2	RESULT
(0)	uminus	B	-	T ₁
(1)	+	C	D	T ₂
(2)	*	T ₁	T ₂	T ₃
(3)	\coloneqq	T ₃	-	A

TRIPLES:

It enables us to avoid entering temporary names.

It has 3 fields ie \rightarrow OP

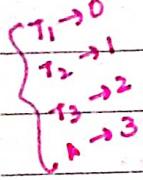
ARG1

ARG2

We use parenthesized nos to represent pointers.

eg.

	OP	ARG1	ARG2
(0)	uminus	B	-
(1)	+	C	D
(2)	*	(0)	(1)
(3)	:=	(A)	(2)



also for $A[i] := B$

	OP	ARG1	ARG2
(0)	[] =	A	I
(1)	-	B	-

INDIRECT TRIPLES:

It lists pointers to the triples rather than listing triples themselves.

	STATEMENT
A	(14) ← our choice.
(0)	(14)
(1)	(15)
(2)	(16)
(3)	(17)

	OP	ARG1	ARG2
(0)	uminus	B	-
(1)	+	C	D
(2)	*	(14)	(15)
(3)	:=	(A)	(16)

ERRORS

Repair vs Recovery:

① Error repair → repairing error that has occurred in the prog.

Error recovery → removing error from prog.

② Users of HLL are apt to make many errors.

Thus, various phases of a compiler communicate with error handler

error reporter

which are invoked when error are detected.

③ It is desirable that compilation of erroneous prog continue, so that user can clean several errors out of the source before they recombine.

④ Error can be detected at:

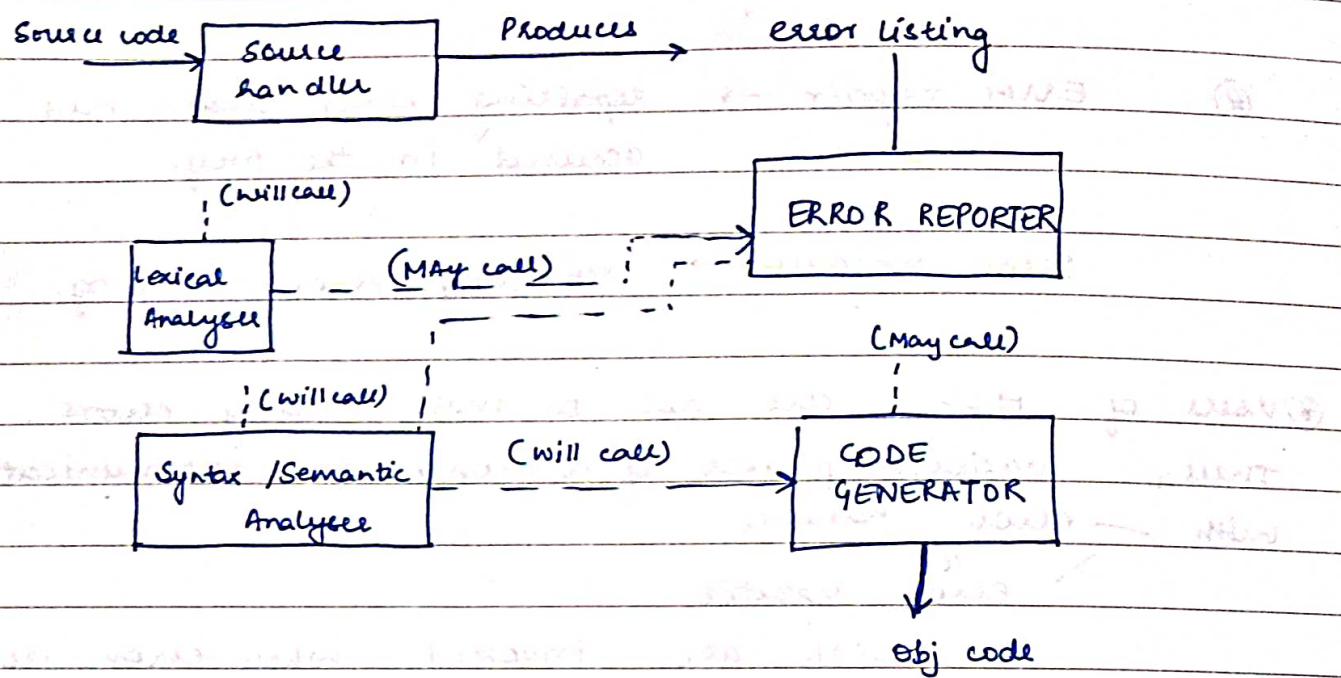
- compile time (in source code)

- run time (when executing obj prog)

⑤ Sometimes, mistakes are called:

errors, that show at COMPILE TIME

⑥ Exceptions at RUN TIME



① On an error :

A simple compiler may STOP all activities.

A complex compiler may REPAIR the error.

② Diff types of errors :

→ lexical analyzer may be unable to proceed bcoz token is misspelled.

→ Syntactic error.

→ ICG may detect error on operator whose operands have incompatible types.

etc

MINIMUM DIST. MATCHING:

There are a no. of situations eg. - misspelling, for which we may use MDM to correct spelling of tokens.

If we suspect 'x' i.e. misspelled bcoz no declaration for x is present in symbol table, we could try to find out an identifier in symbol table ie. ^{closest} ~~similar~~ to x.

The prob. of finding a word from a given collection that is closest to string 'x' has been studied in detail.

A no. of Algorithms are known:- ie-

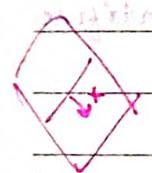
fewest no. of insertions
" " " deletions etc.

SYNTACTIC PHASE ERRORS :

Most of errors detection & correction in compiler is centred around syntax analysis.

From a grammar we can generate a parser that recognises exactly the lang. specified by the grammar.

Violation of this will be caught automatically by the parser.



A parser detects an error when it has no legal move from its current configuration.

To recover from the error, the parser should locate the position of error, correct it & revise its current configuration.

Time Detection :

LL(1) & LR(1) have valid prefix property:

they will announce errors as soon as a prefix of i/p as been seen for which there is no valid continuation.

The time taken by them is the shortest.

PANIC MODE

Effective systematic method of error recovery is called the panic mode.

In this mode, The parser discards i/p symbol until a synchronizing token eg. ; is encountered.

The parser delete the stack entries until it finds an entry such that it can continue passing.

ER in Operator Precedence parsing:

The problems are:-

- There is no precedence relation
- Handle is found but no product is there.

In handle match we check that :-

— terminals are same

— positions occupied by non-terminal is also the same.

ER during reduction:

Suppose abc is popped & there is no product right side consisting a, b & c tog. Then we can:

i) Delete one of a, b or c:

i.e. aACB & issue

ILLEGAL b ON LINE

a) Insert a terminal

i.e. $abAdc \in \text{issue}$

$\rightarrow \text{MISSING } d \text{ ON LINE}$

Handling Shift-reduce errors:

Suppose $a \in (b)$ are 2 top stack symbols & $c \& d$ are next symbols & there is no precedence reln b/w $c \& b$

↓ then:

e.g. if $a \leq c$ we pop 'b' from stack

or $b \leq d$ we delete 'c'.

ER in LR parsing:

It detects an error when it consults the parsing action table & finds an error entry.

Exhaustive method

It is to examine each error entry in the parsing table & decide the most likely error that would give rise to that error.

Systematic method

Scan down the stack until state 's' till non-terminal 'A' is found.

The parser stacks the state $\text{GOTO}[s, A]$ & resumes.

Adhoc: Design a handling routine for an LR parser.

ER for YACC:

we add "grammatical error production" of form
 $A \rightarrow \text{error } \alpha$

YACC encounters errors & finds underlying set of items + item of form $A \rightarrow \text{error } \alpha$.

It then shifts error

Then invokes semantic action associated with
 $A \rightarrow \text{error}$

It discards i/p symbols until it finds a symbol from where parsing can proceed.

ER in LL Parsing:

It has "PANIC MODE".

On finding error we skip i/p

we pop the stack until "synchro niza" token (X) is on top of stack

If (X) is not synch. token then X will be expanded & symbols will be popped till syn. token comes on top of stack.

SEMANTIC ERRORS:

① The 1st source of SE are:

- undeclared names

- type incompatibility.

② Recovery from undeclared name is straightforward.

1st time we encounter an undeclared name we make an entry for that name in the symbol table.

A flag in symbol table entry is set to indicate the entry was made in response to semantic error.

③ Each time a name is used incorrectly, we check to determine whether previously this erroneous usage was ^{detected} declared or not.

If detected → no new error msg is printed

If not → error msg printed & new erroneous usage is added to list.

④ eg. ARAY[A, B]

it is not a valid array name.

UNIT 5

LR(0)

LR(0) item of grammar 'G' is production of G with a dot at some point of right side.

eg. $A \rightarrow XYZ$

so $A \rightarrow \cdot XYZ$

$A \rightarrow X \cdot YZ$

$A \rightarrow X Y \cdot Z$ etc.

consider:

$G \rightarrow \text{Grammar}$

$G' \rightarrow \text{Augmented grammar}$

$S \rightarrow \text{Start symbol}$

$S' \rightarrow \text{new start symbol}$

$\therefore S' \rightarrow S.$

CLOSURE: Let 'I' be set of items for 'G'. Then closure is.

i) Every item in 'I' is in CLOSURE(I).

ii) If $A \rightarrow \alpha \cdot B \beta$ is in CLOSURE(I) & $B \rightarrow \gamma$ is a production then add it to I.

GOTO: $\text{GOTO}(I, x)$.

where ($I \rightarrow \text{set of items}$)

$x \rightarrow \text{grammar symbol.}$

if $A \rightarrow \alpha x \cdot \beta$ is in I

then add all productions

in which $x \cdot$ will be present.

SLR Parsing Table: | CLR Parsing Table.

I_P : canonical collection of sets of items for an augmented grammar G'

O_P : LR Parsing table containing → ACTION & GOTO functions.

METHOD: let

$$C = \{ I_0, \dots, I_n \}$$

the states of parser from 0 to n.

state i is being constructed from I_j

The parsing action is:

- 1) If $[A \rightarrow \alpha \cdot a \beta]$ is in I_i & $\text{GOTO}(I_i, a) = I_j$
set ACTION $[i, a]$ to 'shift j'
- 2) If $[A \rightarrow \alpha \cdot]$ is in I_i , set ACTION $[i, a]$ to 'reduce' for all a in FOLLOW(A)
- 3) If $[S^* \rightarrow S \cdot]$ is in I_i , set ACTION $[i, \$]$ to 'accept'
- 4) If $\text{GOTO}[I_i, a] = I_j$ - then $\text{GOTO}[i, A] = j$
- 5) All entries not defined by rules (2) & (3) are made 'ERROR'.
- 6) The initial state of parser is one constructed from set containing item
$$(S^* \rightarrow S, \$)$$

LR(1):

I/P: Grammar g

O/P: set of LR(1) items.

METHOD:

procedure CLOSURE (I);

begin

repeat

for $[A \rightarrow \alpha \cdot B \beta, a]$ in I ,

each product $B \rightarrow Y$ & each terminal b

such that $[B \rightarrow \cdot Y, b]$ is not in I

do add $[B \rightarrow \cdot Y, b]$ to I ;

return I

end;

Procedure GOTO (I, X)

begin

let J be set of Items $[A \rightarrow \alpha \cdot X \beta, a]$ such that
 $[A \rightarrow \alpha \cdot X \beta, a]$ is in I ;

return closure (J)

end;

begin

$C := \{ \text{CLOSURE } (\{ s^1 \rightarrow \cdot s, \$ \}) \}$

repeat

for each set of items I

& symbol X

such that $\text{GOTO } (I, X)$ is not empty

& not in C do

add $\text{GOTO } (I, X)$ to C

until no more sets of items can be added to C

end.

LALR Parsing table:

I/P: grammar & augmented by $S' \rightarrow S$

O/P: LALR Parsing table.

METHOD:

1) Construct $C = \{ I_0, \dots, I_n \}$

i.e. sets of LR(1) items.

2) Find all sets having core ϵ & replace these sets by their union.

3) Let $C' = \{ J_0, \dots, J_m \}$ be resulting LR(1) items.

4) GOTO table is constructed as follows.

if $J = I_1 \cup I_2 \cup \dots \cup I_m$ then

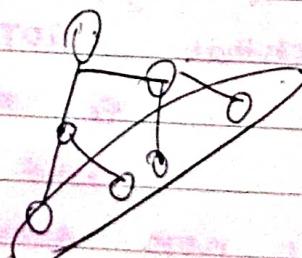
cores of $GOTO(I_1, x)$

$GOTO(I_2, x) \dots$

are same

since I_1, \dots, I_k have same cores.

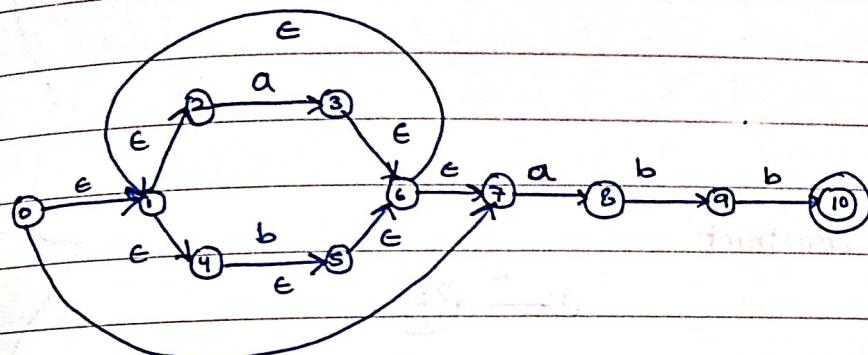
Hence $GOTO(J, x) = k$



UNIT:2

NFA:

$(a|b)^*$ abb



set of all items irrespective of going from 0

$$A = \{0, 1, 2, 4, 7\}$$

from 1 to all elements via (a)

$$B = \{1, 2, 3, 6, 7, 8, 4\}$$

from 1 to all elements via (b)

$$C = \{1, 4, 5, 6, 7, 2\}$$

now reach (b) on 9 & (b) on 10

$$\therefore D = \{1, 4, 5, 6, 7, 2, \underline{9}\}$$

$$E = \{1, 4, 5, 6, 7, 2, \underline{10}\}$$

now,

State	<u>IP</u>	
	a	b
$\rightarrow A$	B	C
B	B	D
C	B	C
D	B	E
(E)	B	C

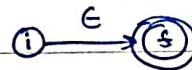
RE \rightarrow NFA

TIP: RE \Leftarrow R'

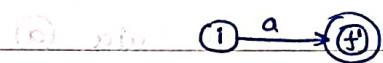
O/P: NFA \Leftarrow N'

METHOD:

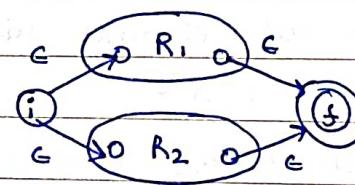
- 1) For ϵ we construct



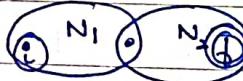
- 2) For a we construct



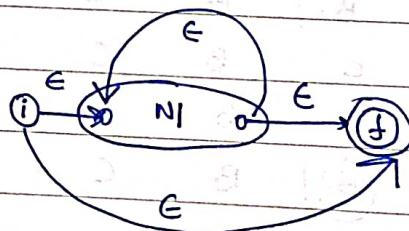
- 3) For $R_1 | R_2$ we construct



- 4) For $R_1 R_2$ we construct



- 5) For R_1^* we construct



Minimization of FA:

IP: A DFA 'M' with set of states 'S'

OP: DFA 'M' with reduced set of states.

METHOD:

eg.	Status	I/P	
		a	b
	A	B	C
	B	B	D
	C	B	C
	D	B	E
	(E)	B	C

$$\pi_0 = \{E\} \{A, B, C, D\} \leftarrow \text{subgroups}$$

$$\pi_1 = \{E\} \{A, C\} \{B\} \{D\}$$

$$\therefore \pi_f = \{E\} \{A, C\} \{B\} \{D\}$$

Status	I/P	
	a	b
A	B	A
B	B	D
C	B	E
(E)	B	A

we omit state C as C = A.

just fine

LR(0)

$$I_0 = E^1 \rightarrow \cdot E$$

$$E \rightarrow \cdot E F F$$

$$E \rightarrow \cdot T$$

$$T \rightarrow \cdot T * F$$

$$T \rightarrow \cdot F$$

$$F \rightarrow \cdot (E)$$

$$F \rightarrow \cdot id$$

$$I_0 = E \rightarrow E + T.$$

$$T \rightarrow T * F$$

$$I_{10} = T \rightarrow T * F.$$

$$I_{11} = (E) \cdot$$

now I₁

$$I_1 = E^1 \rightarrow E \cdot$$

$$E \rightarrow E \cdot + T \cdot$$

$$I_2 = E \rightarrow T \cdot$$

$$T \rightarrow T \cdot * F \cdot$$

$$I_3 = T \rightarrow F \cdot$$

$$I_4 = F \rightarrow (\cdot E)$$

$$E \rightarrow \cdot E + T$$

$$E \rightarrow \cdot T$$

$$T \rightarrow \cdot T * F$$

$$T \rightarrow \cdot F$$

$$F \rightarrow \cdot (E) | . id$$

$$I_5 = F \rightarrow id.$$

$$I_6 = E \rightarrow E + \cdot T$$

$$T \rightarrow \cdot T * F$$

$$T \rightarrow \cdot F$$

$$F \rightarrow \cdot (E) | . id$$

$$I_7 = T \rightarrow T * \cdot F$$

$$F \rightarrow \cdot (E) | . id$$

$$I_8 = F \rightarrow (E \cdot)$$

$$E \rightarrow E \cdot + T$$

sub. net as reduce shift conflict

$$\left\{ \begin{array}{l} S \rightarrow L = R \\ S \rightarrow R \\ L \rightarrow * R \\ L \rightarrow id \\ R \rightarrow L \end{array} \right.$$

$$I_0 = S^1 \rightarrow \cdot S \cdot$$

$$S \rightarrow \cdot L = R$$

$$S \rightarrow \cdot R$$

$$L \rightarrow \cdot id$$

$$R \rightarrow \cdot L$$

Table same as

LALR | CLR

but we don't

add \$ to production
we just calculate
follow & add \$

$$I_3 = S \rightarrow R \cdot$$

$$I_4 = L \rightarrow * \cdot R \cdot$$

$$\left[\begin{array}{l} R \rightarrow \cdot L \leftarrow \\ L \rightarrow \cdot id \\ L \rightarrow \cdot id \end{array} \right]$$

$$I_6 = S \rightarrow L = \cdot R$$

$$R \rightarrow \cdot L$$

$$L \rightarrow * R$$

$$L \rightarrow \cdot id$$

NOT Ambiguous

$$I_7 = L \rightarrow * R \cdot$$

several RD

LR(0)

$$I_8 = R \rightarrow L \cdot$$

$$I_9 = S \rightarrow L = R \cdot$$

$$\begin{array}{l} S \rightarrow CC \quad (1) \\ C \rightarrow cC \quad (2) \\ C \rightarrow d \quad (3) \end{array}$$

wecadd

$$I_0 = S' \rightarrow S, \$ \text{ with start symbol.}$$

$$S \rightarrow .CC, \$$$

$$C \rightarrow .cC, c/d \text{ all terminals}$$

$$C \rightarrow .d, cd$$

$$I_1 = S' \rightarrow S., \$$$

$$\begin{aligned} I_2 = & S \rightarrow C.C, \$ \text{ match.} \\ & C \rightarrow .CC, \$ \\ & C \rightarrow .d, \$ \end{aligned}$$

$$\begin{aligned} I_3 = & C \rightarrow c.C, c/d \\ & C \rightarrow .cC, c/d \\ & C \rightarrow .d, \$ c/d \end{aligned}$$

$$I_4 = C \rightarrow d., c/d$$

$$I_5 = S \rightarrow CC., \$$$

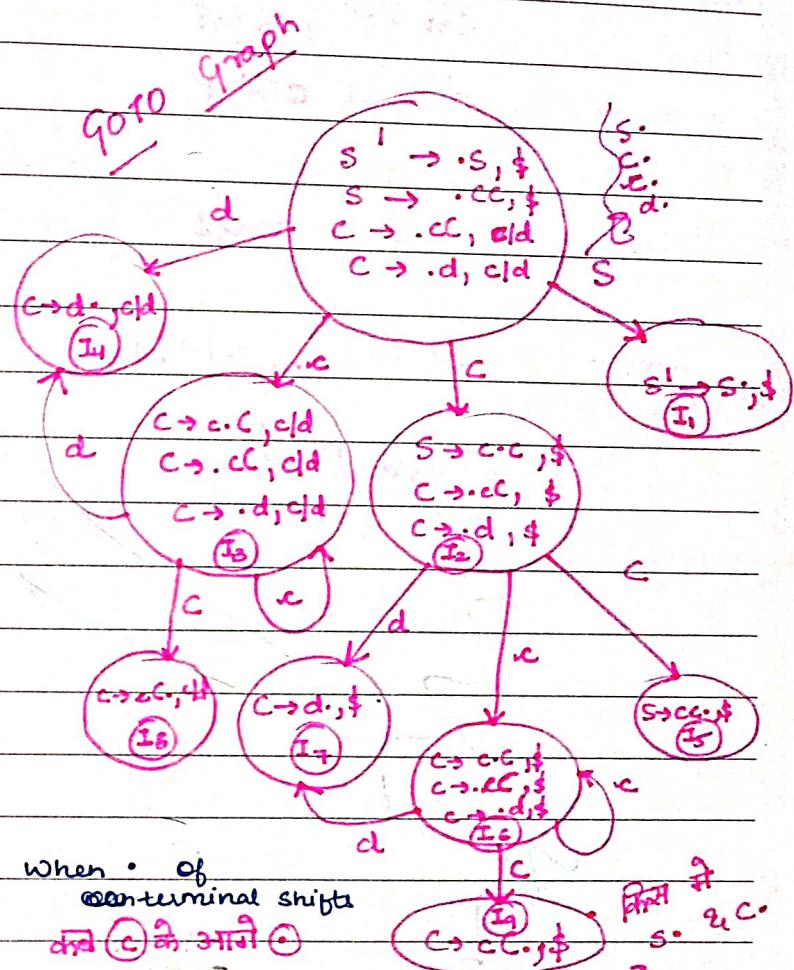
$$\begin{aligned} I_6 = & C \rightarrow c.C, \$ \text{ choose only } \$ \text{ we add.} \\ & C \rightarrow .CC, \$ \\ & C \rightarrow .d, \$ \end{aligned}$$

$$I_7 = C \rightarrow d., \$$$

$$I_8 = C \rightarrow CC., c/d$$

$$I_9 = C \rightarrow CC., \$$$

follow {S} \rightarrow



when . of non-terminal shifts

and (2) के अन्ति (1)

पर नहीं सूचित है

state	Action			goto
	c	d	\$	
0	S3	S4	acc	1 2
1				
2	S6	S7		5 8
3	S3	S4		
4	γ_3	γ_3		
5	γ_3	γ_3	γ_1	
6	S6	S7		
7				9
8	γ_2	γ_2		
9				

when . is at end with 0 at last (end)

NOVEMBER 2008							DECEMBER 2008						
Su	Mo	Tu	We	Th	Fr	Sa	Mo	Tu	We	Th	Fr	Sa	Su
1	2	3	4	5	6	7	8	9	10	11	12	13	14
15	16	17	18	19	20	21	22	23	24	25	26	27	28
29	30	31	*	*	*	*	*	*	*	*	*	*	*

- DFA \leftrightarrow NDFA
- Minimizaⁿ of FA

SATURDAY

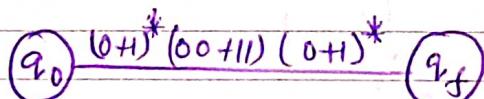
04

* {refer Pg. 141} not imp.

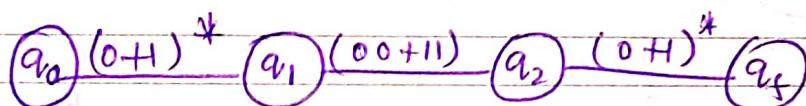
Regular Exp to FA:

eg. RE \rightarrow $(0+1)^*$ $(00+11)$ $(0+1)^*$

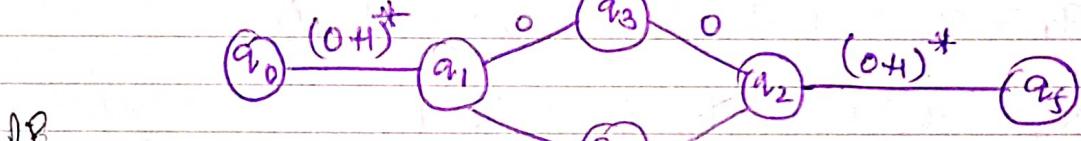
(1)



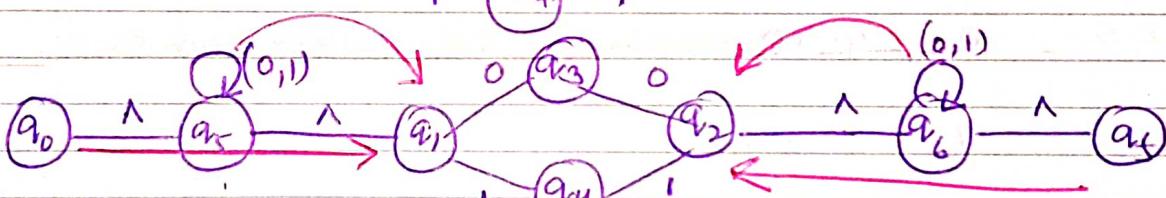
(2)



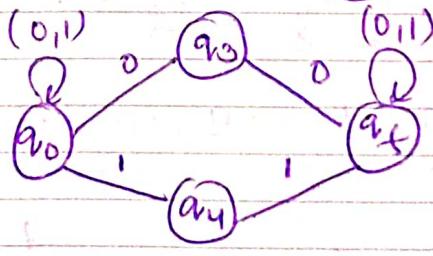
(3)



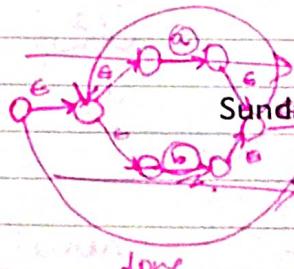
(4)



(5)

state $\rightarrow q_0$ $0 \over q_0, q_3$ $1 \over q_0, q_4$ q_1 q_2 q_3 q_4 q_5 q_6 q_7 $(a|b)^*abb$

Sunday 05



done

pg 156

★ Pg 156

The wise does at once what the fool does at last.

OCTOBER

2008

Week 41 * Day 280-086

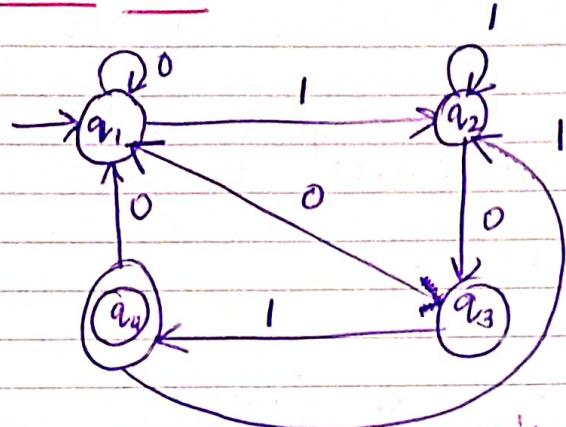
06

MONDAY

FA to RE

SEPTEMBER 2008							OCTOBER 2008						
Mo	Tu	We	Th	Fr	Sa	Su	Mo	Tu	We	Th	Fr	Sa	Su
36	1	2	3	4	5	6	7	40	1	2	3	4	5
37	8	9	10	11	12	13	14	41	6	7	8	9	10
38	15	16	17	18	19	20	21	42	13	14	15	16	17
39	22	23	24	25	26	27	28	43	20	21	22	23	24
40	29	30	*	*	*	*	*	44	27	28	29	30	31

Day 149



$q_1 \rightarrow$ jinse o delta hai write them.

$$q_1 = q_1 \cdot 0 + q_4 \cdot 0 + q_3 \cdot 0 + 1$$

$$q_2 = q_1 \cdot 1 + q_2 \cdot 1 + q_4 \cdot 1$$

$$q_3 = q_2 \cdot 0$$

$$q_4 = q_3 \cdot 1$$

now substitute $\therefore q_4 = q_2 \cdot 0 \cdot 1$

$$q_2 = q_1 \cdot 1 + q_2 \cdot 1 + q_4 \cdot 1$$

$$q_2 = \underbrace{q_1 \cdot 1}_R + \underbrace{q_2 \cdot 1}_Q + \underbrace{q_2 \cdot 0 \cdot 1 \cdot 1}_{R P} q_2(1+011)$$

$$R = Q + RP \\ R = QP^*$$

$$\therefore q_2 = q_1 \cdot 1 (1+011)^*$$

$$\text{now } q_1 = q_1 \cdot 0 + q_2 \cdot 0 \cdot 1 \cdot 0 + q_2 \cdot 0 \cdot 0 + 1$$

$$= q_1 \cdot 0 + q_1 \cdot 1 (1+011)^* \cdot 0 \cdot 1 \cdot 0 + q_1 \cdot 1 (1+011)^* \cdot 0 \cdot 0 + 1$$

$$q_1 = \underbrace{q_1 (0 + 1 (1+011)^* \cdot 0 \cdot 1 \cdot 0 + 1 (1+011)^* \cdot 0 \cdot 0)}_P + 1$$

$$q_1 = \{0 + 1 (1+011)^* \cdot 0 \cdot 1 \cdot 0 + 1 (1+011)^* \cdot 0 \cdot 0\}^*$$

$$q_1 = (0 + 1 (1+011)^* \cdot 0 \cdot 0 \cdot 1 + 1 (1+011)^* \cdot 0 \cdot 0)^*$$

Patience is a necessary ingredient of genius.

$$q_4 = q_2 \cdot 0 \cdot 0 = q_1 \cdot 1 (1+011)^* \cdot 0 \cdot 1 = (0 + 1 (1+011)^* \cdot 0 \cdot 0 \cdot 1 + 1 (1+011)^* \cdot 0 \cdot 0)^* (1+011)$$