8085 Stack Operation

- The Stack 18 a position of read/white memory set aside by the user for the purpose of storing in formation temporarily.
- -> When the information is written on stack the operation is called PUSH.
- -> When the information is stead from stack the operation is called POP.
- The Stack operates in First IN Last out (FILO) fashion. This means that the first information pushed on to the Stack is the last information popped off from the Stack.
 - TH 8085, Stack is implemented using STACK POINTER Aggister. SP is a 16-bit segister which gives the address of memory where the information 12 to be staded on to be sead.
 - The memory location consently pointed by Stack pointed is called top of Stack.
 - -> It 8085, there are 2 instructions that work with stack.
 - (D) PUSH ap (D) POP ap ap -> means 16-bit stegisted paid (BC, DE, HL)

PUSH 91 p o This in stauction decarements

Stock pointer by one and copies the higher byte of the aegister pair mentioned higher byte of the aegister pair mentioned in the instruction into the memory location in the instruction into the memory location panted by Stock pointer. It then decrements the stock pointer again by one and copies the stock pointer again by one and copies the lower byte of the register pair into memory socation pointed by Stock pointer.

Oberation: $SP \leftarrow SP-1$; $(SP) \leftarrow 9PH$; $SP \leftarrow SP-1$; $(SP) \leftarrow 9PL$

	_	2000
2000H	2000H	2,001
2001H	2001H	2.002
2002H	2002H SP->	91PL 2003
2003H	SP > 9/bH 2003H	2004
200414	20044	1
$SP \rightarrow L$	(0)	1 Panionent SP

(a) Initial Position

- (b) Decrements SP
 by 1 and store
 lower byte of
 Ab
- (c) Decrement 2P
 by 1 and Store
 higher byte
 of ap.

Example: PUSH B & Store BC 9p onto Stock

PUSH D & Store DE 9p onto Stock

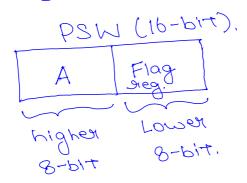
PUSH H & Store HL 9p onto Stock

PUSH PSW & Store PSW onto Stock.

PUSH PSW & Store PSW onto Stock.

POSH PSW & Store PSW onto Stock.

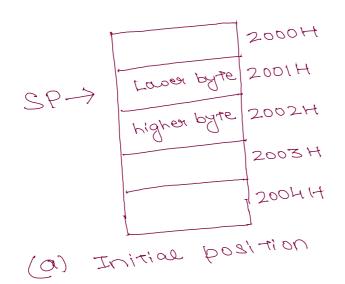
PSW: Parogram Status World 18 the Combination of accumulation and Flag register.

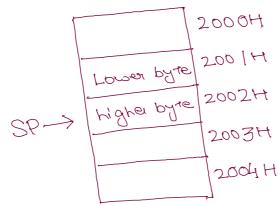


POP ap : This instruction copies the contents of memory location pointed by the Stock bointest into the lower byte of the Specified segister pair and increments the Stock pointer by one. Then it copies the Stock pointer by one higher byte of the Stock pointer into the higher byte of the Stock pointer into the higher byte of the Stock pointer into the higher byte of the Specified segister pair and increments the Specified segister pair and increments the

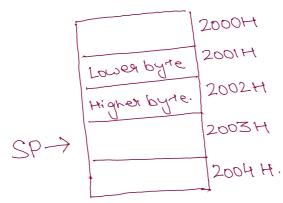
operation:
$$APL \leftarrow (SP)$$
 $SP \leftarrow SP+1$
 $APH \leftarrow (SP)$
 $SP \leftarrow SP+1$

Examples of POP B & sead into BC sip POP D & sead into DE sip POP H & sead into HL sip POP PSW & sead into PSW





(b) Read lower byte Porto siph then increment SP by 1



(C) Read higher byte into siph then in crement SP by 1. > Paragram to Swap the Contents of BC and BODE register pair using stock.

LXISP, 2004H & SP 18 initialized.

LXI B, 4444H 3 grandom values are LXI D, 5555H. Stored in BC, DE MP.

PUSH B & Contents of BC is stored on stock

PUSH DE CONTENTS of DEIS Stated on Stack

POP B < contents of stock lead to BC

POP D & corrents of Stock read to DE

HLT.