

Digital Logic Design (EE1005)

Date: February 25th, 2025

Course Instructor(s)

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Sessional-I Exam

Total Time: 1 Hour

Total Marks: 30

Total Questions: 02

Semester: SP-2025

Campus: Karachi

Dept: School of Computing

| Student Name | Roll No | Section | Student Signature |
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| CLO # 1 Number systems and logic | Estimated Time=20mins |
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Q1: Answer the following: [10]

- (a) What is the largest binary number that can be expressed with 14 bits? [1]

$2^{14} - 1 = 16383$ [1]

- (b) Calculate the binary equivalent of $\frac{2}{3}$ out to three places. [1]

$0.666 \times 2 = 1.33$ 1
 $0.333 \times 2 = 0.66$ 0
 $0.666 \times 2 = 1.33$ 1
 (0.101)₂ [1]

- (c) Perform the 8-bit signed addition of (using 2's Complement)

(i) [(+29) + (-49)]

(ii) [(-29) + (+49)]

+29 00011101
 +49 00110001
 -29 11100011
 -49 11001111

+29 00011101
 -49 11001111
 -20 11101100
 +20 00010100

-29 11100011
 +49 00110001
 +20 100010100

- (d) Find gray code for the following hexadecimal numbers: (E5)₁₆ [2]

E 5
 B 1110 0101
 6 1001 0111

- (e) Perform the following BCD addition and show the answer is correct. 1485 + 754 [2]

$$\begin{array}{r}
 0001 \quad 0100 \quad 1000 \quad 0101 \\
 0000 \quad 0111 \quad 0101 \quad 0100 \\
 \hline
 0001 \quad 1011 \quad 1101 \quad 1001 \\
 \hline
 0010 \quad 1010 \quad 0011 \quad 1001
 \end{array}$$

$$\begin{array}{r}
 1485 \\
 + 754 \\
 \hline
 2239
 \end{array}$$

- (f) Express each decimal number in binary as an 8-bit sign-magnitude number, the 1's complement form and the 2's complement form. [2]

(i) +67 (ii) -89

$$\begin{aligned}
 +67 &= 01000011 \\
 1^{\text{st}} \text{Comp} &= 01000011 \\
 2^{\text{nd}} \text{Comp} &= 01000011
 \end{aligned}$$

$$\begin{aligned}
 -89 &= 11011001 \\
 +89 &= 01011001 \\
 1^{\text{st}} \text{Comp} &= 10100110 \\
 2^{\text{nd}} \text{Comp} &= 10100111
 \end{aligned}$$

CLO # 2 Techniques to design logic circuits

Estimated Time=40mins

Q2: Answer the following:

[20]

- (a) Design a combinational circuit with four inputs (A, B, C, D) and one output (F) such that: $F=1$ when the number of 1's in the input is odd. Develop the truth table (Input variables are A, B, C, D and output F) and write down the SOP and POS expressions for the logic circuit. [5]

| A | B | C | D | X |
|---|---|---|---|---|
| 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 0 | 1 | 1 |
| 0 | 0 | 1 | 0 | 1 |
| 0 | 0 | 1 | 1 | 0 |
| 0 | 1 | 0 | 0 | 1 |
| 0 | 1 | 0 | 1 | 0 |
| 0 | 1 | 1 | 0 | 1 |
| 0 | 1 | 1 | 1 | 0 |
| 1 | 0 | 0 | 0 | 1 |
| 1 | 0 | 0 | 1 | 0 |
| 1 | 0 | 1 | 0 | 0 |
| 1 | 0 | 1 | 1 | 1 |
| 1 | 1 | 0 | 0 | 0 |
| 1 | 1 | 0 | 1 | 1 |
| 1 | 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 | 1 |

$$\begin{aligned}
 \Sigma (1, 2, 4, 7, 8, 11, 13, 14) \\
 X = \bar{A}\bar{B}\bar{C}D + \bar{A}\bar{B}C\bar{D} + \bar{A}B\bar{C}D \\
 + \bar{A}BC\bar{D} + A\bar{B}\bar{C}\bar{D} + A\bar{B}C\bar{D} \\
 + AB\bar{C}D + ABC\bar{D}
 \end{aligned}$$

$$\Pi (0, 3, 5, 6, 9, 10, 12, 15)$$

$$\begin{aligned}
 (A+B+C+D)(A+B+\bar{C}+\bar{D}) \\
 (A+\bar{B}+C+\bar{D})(A+\bar{B}+\bar{C}+D)(\bar{A}+B+C+\bar{D}) \\
 (\bar{A}+B+\bar{C}+D)(\bar{A}+\bar{B}+C+D) \\
 (\bar{A}+\bar{B}+\bar{C}+\bar{D})
 \end{aligned}$$

- (b) For the waveforms given in Fig-1 below, A and B are XORed to get an output F, then C and D are XNORed to get an output G. Finally C, F and G are ORed. Draw the net output waveform. [2]

$$\begin{array}{r}
 11101 \\
 11110
 \end{array}$$

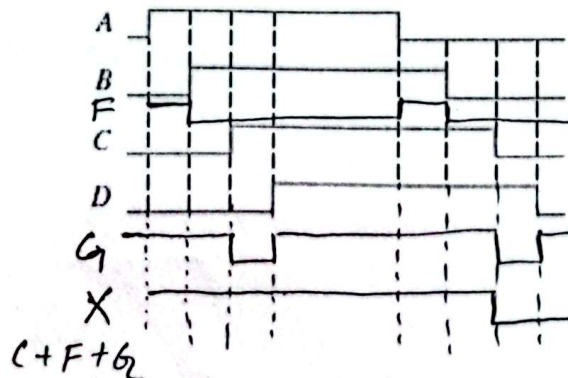


Fig-1

