

ChengHao_MEC733_W2022_Lab 2.ino	ChengHao_MEC733_W2022_La b2.h	ChengHao_MEC733_W2022_L ab2.s
<pre>Int main() {   uint8_t i,j;    port_D_init();   delay_ms(2000);    initUSART();   printString("We are doing lab 2!");    while(1)   {     i=port_D7_read();     printString("\n i=");     printBinaryByte(i);      j=port_D6_read();     printString("\n j=");     printBinaryByte(j);      if(i==0)     {port_D3_high();}     if(i!=0)     {port_D3_low();}      if(j==0)     {port_D2_high();}     if(j!=0)     {port_D2_low();}   } }</pre>	<pre>extern "C" void port_D_init(void);  extern "C" uint8_t port_D6_read(void);  extern "C" uint8_t port_D7_read(void);  extern "C" void port_D2_high(void);  extern "C" void port_D2_low(void);  extern "C" void port_D3_high(void);  extern "C" void port_D3_low(void);</pre>	<pre>port_D_init 0b00001100 =&gt; R16 =&gt; 0x0A 0b11000000 =&gt; R16 =&gt; 0x0B  port_D7_read 0x09 =&gt; R24 0b10000000 &amp; R24  port_D6_read 0x09 =&gt; R24 0b10000000 &amp; R24  port_D2_high 0b00000100 =&gt; 0x0B  port_D2_low 0b00000000 =&gt; 0x0B  port_D3_high 0b00001000 =&gt; 0x0B  port_D3_low 0b00000000 =&gt; 0x0B</pre>