

1ump – Open Source

Sheet: /
File: twcl_test_circuit.sch

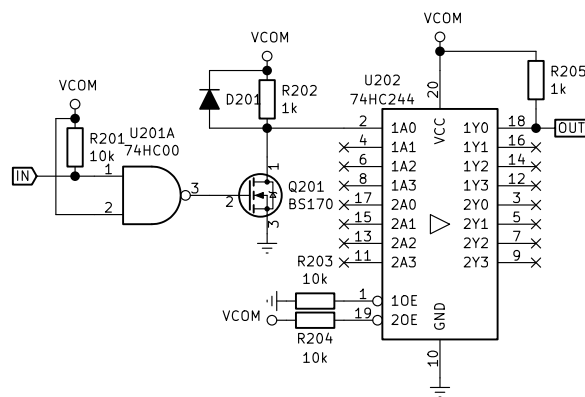
Title: TWCL network test circuits

Size: A4 Date: 2018-09-11

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Rev: 1.0

Id: 1/3



Tested with TH components on test board with 1kHz–500kHz square wave.
 ATmega2560 used as a square wave generator.
 UART packet transmistion success.

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Sheet: /Signal_test_circuit/
 File: Signal_test_circuit.sch

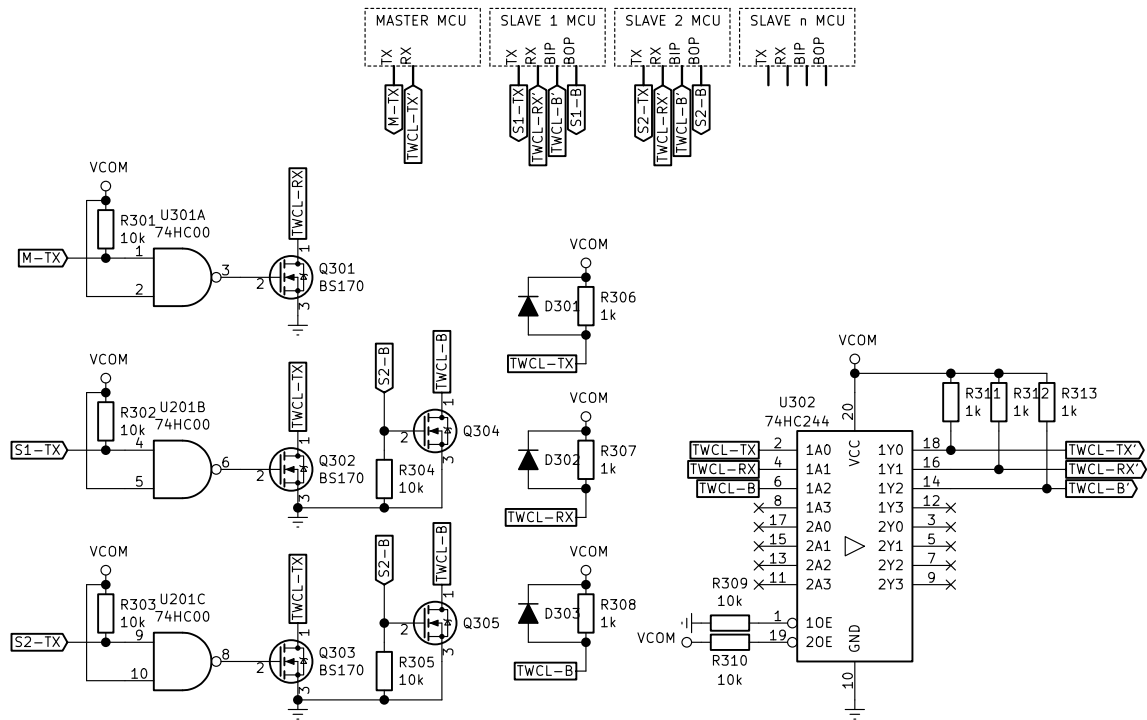
Title: TWCL interface signal propagation test circuit.

Size: A4 Date: 2018–09–11

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Rev: 1.0

Id: 2/3



Tested with TH components on test board and ATmega2560.
 1Mbps can be achieved with low noise on test boards.
 SLAVE n MCU can be any MCU with 1 or more hardware UARTs.
 MASTER MCU can be any MCU with 2 or more hardware UARTs.

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Sheet: /Basic_network_test_circuit/
 File: Basic_network_test_circuit.sch

Title: TWCL basic network test circuit

Size: A4 Date: 2018-09-11

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Rev: 1.0

Id: 3/3