

Microsemi VPROC API  
V1.0.0

Generated by Doxygen 1.8.6

Fri Jul 1 2016 13:58:34



# Contents

|          |                                      |          |
|----------|--------------------------------------|----------|
| <b>1</b> | <b>Main Page</b>                     | <b>1</b> |
| 1.1      | Introduction .....                   | 1        |
| <b>2</b> | <b>Module Index</b>                  | <b>3</b> |
| 2.1      | Modules .....                        | 3        |
| <b>3</b> | <b>Data Structure Index</b>          | <b>5</b> |
| 3.1      | Data Structures .....                | 5        |
| <b>4</b> | <b>File Index</b>                    | <b>7</b> |
| 4.1      | File List .....                      | 7        |
| <b>5</b> | <b>Module Documentation</b>          | <b>9</b> |
| 5.1      | Host Bus Interface .....             | 9        |
| 5.1.1    | Detailed Description .....           | 10       |
| 5.1.2    | Enumeration Type Documentation ..... | 10       |
| 5.1.2.1  | hbi_cmd_t .....                      | 10       |
| 5.1.2.2  | hbi_status_t .....                   | 11       |
| 5.1.2.3  | hbi_img_type_t .....                 | 11       |
| 5.1.2.4  | hbi_rst_mode_t .....                 | 11       |
| 5.1.3    | Function Documentation .....         | 11       |
| 5.1.3.1  | HBI_term .....                       | 11       |
| 5.1.3.2  | HBI_open .....                       | 11       |
| 5.1.3.3  | HBI_close .....                      | 12       |
| 5.1.3.4  | HBI_reset .....                      | 12       |
| 5.1.3.5  | HBI_read .....                       | 12       |
| 5.1.3.6  | HBI_write .....                      | 13       |
| 5.1.3.7  | HBI_set_command .....                | 13       |
| 5.1.3.8  | HBI_wake .....                       | 14       |
| 5.1.3.9  | HBI_sleep .....                      | 14       |
| 5.1.3.10 | HBI_get_header .....                 | 14       |
| 5.2      | System Service Layer .....           | 16       |
| 5.2.1    | Detailed Description .....           | 16       |

|          |   |           |
|----------|---|-----------|
| 5.2.2    | Enumeration Type Documentation.....       | 17        |
| 5.2.2.1  | ssl_status_t.....                         | 17        |
| 5.2.2.2  | ssl_wait_t.....                           | 17        |
| 5.2.2.3  | ssl_op_t.....                             | 17        |
| 5.2.3    | Function Documentation.....               | 17        |
| 5.2.3.1  | SSL_init.....                             | 17        |
| 5.2.3.2  | SSL_lock_create.....                      | 18        |
| 5.2.3.3  | SSL_lock.....                             | 18        |
| 5.2.3.4  | SSL_unlock.....                           | 18        |
| 5.2.3.5  | SSL_lock_delete.....                      | 18        |
| 5.2.3.6  | SSL_term.....                             | 18        |
| 5.2.3.7  | SSL_port_open.....                        | 18        |
| 5.2.3.8  | SSL_port_close.....                       | 19        |
| 5.2.3.9  | SSL_port_read.....                        | 19        |
| 5.2.3.10 | SSL_port_write.....                       | 19        |
| 5.2.3.11 | SSL_port_rw.....                          | 19        |
| 5.2.3.12 | SSL_memset.....                           | 19        |
| 5.2.3.13 | SSL_memcpy.....                           | 20        |
| 5.2.3.14 | SSL_delay.....                            | 20        |
| <b>6</b> | <b>Data Structure Documentation</b> ..... | <b>21</b> |
| 6.1      | hbi_data_t Struct Reference.....          | 21        |
| 6.1.1    | Detailed Description.....                 | 21        |
| 6.2      | hbi_dev_cfg_t Struct Reference.....       | 21        |
| 6.2.1    | Detailed Description.....                 | 22        |
| 6.2.2    | Field Documentation.....                  | 22        |
| 6.2.2.1  | dev_addr.....                             | 22        |
| 6.3      | hbi_img_hdr_t Struct Reference.....       | 22        |
| 6.3.1    | Detailed Description.....                 | 22        |
| 6.4      | hbi_init_cfg_t Struct Reference.....      | 22        |
| 6.4.1    | Detailed Description.....                 | 23        |
| 6.4.2    | Field Documentation.....                  | 23        |
| 6.4.2.1  | lock.....                                 | 23        |
| 6.5      | ssl_port_access_t Struct Reference.....   | 23        |
| 6.5.1    | Detailed Description.....                 | 23        |
| 6.5.2    | Field Documentation.....                  | 23        |
| 6.5.2.1  | pSrc.....                                 | 23        |
| 6.5.2.2  | nread.....                                | 24        |
| 6.5.2.3  | nwrite.....                               | 24        |
| <b>7</b> | <b>File Documentation</b> .....           | <b>25</b> |

|                    |  |           |
|--------------------|--|-----------|
| 7.1                | <a href="#">drivers/hbi/inc/hbi.h File Reference</a> ..... | 25        |
| 7.2                | <a href="#">include/ssl.h File Reference</a> .....         | 26        |
| 7.2.1              | <a href="#">Detailed Description</a> .....                 | 27        |
| <b>Index</b> ..... |  | <b>28</b> |

## Chapter 1

# Main Page

### 1.1 Introduction

This is an API specification document for Microsemi Voice Processor Class of Device SDK. This document covers platform-independent Host Bus Interface(HBI) And platform-dependent System Service Layer(SSL). HBI API are implemented over SSL. Below Diagram gives an overview of different components in system

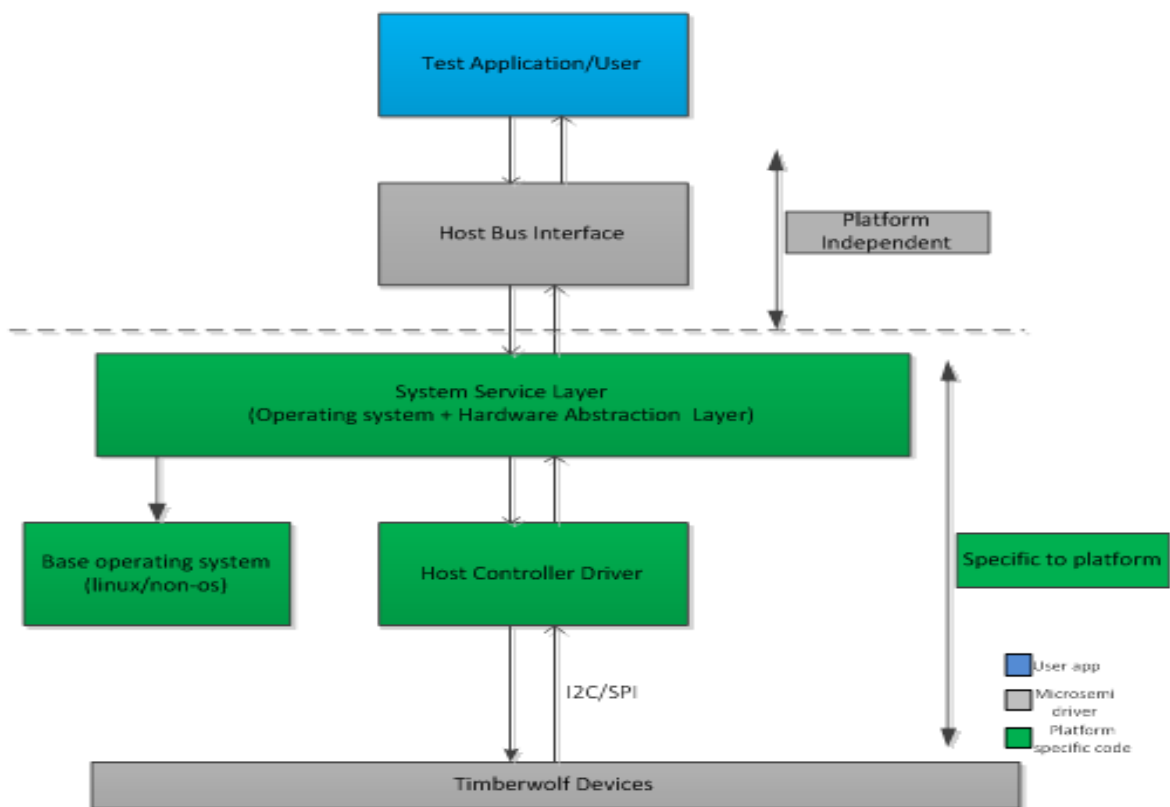


Figure 1.1: Microsemi VPROC SDK FLOW

Microsemi only define System Service Layer as an abstract API set . It should be ported by developer responsible for porting HBI Driver for a particular host platform. HBI Driver and Microsemi VPROC SDK is assisted by various

compile-time options which gives flexibility to host to configure Microsemi Voice Processor Device and software as per their system requirement ex. SPI/I2C, endianness, memory resources. Below tables summarizes both HBI and System-wide various compile time options

| Option               | Value                             | Description   |
|----------------------|-----------------------------------|---|
| BOOT_FROM_HOST       | yes<br>no                         | If set to yes or defined, then HBI driver will support booting voice processor device with specific firmware image from host                              |
| HBI_MAX_INST_PER_DEV | decimal value as per system       | This defines maximum number of simultaneous user on a single voice processor device   |
| FLASH_PRESENT        | yes<br>no                         | If defined, indicates the Microsemi Voice Processor device is connected to slave flash. In this case HBI driver will support all flash related operations |
| VPROC_DEV_ENDIAN     | big                               | This defines endianness of VPROC Device as big.   |
| HBI_MAX_INSTANCES    | Any decimal value starting from 1 | Maximum number of users of driver   |
| HBI_BUFFER_SIZE      | Any decimal value > 0             | Size of buffer used by HBI for any transactions   |
| HBI_ENABLE_PROCFS    | 0,1                               | Optional. Applicable for linux based system   |

Figure 1.2: Microsemi HBI Compile Time Options

| Option              | Value  | Description   |
|---------------------|--|---|
| VPROC_MAX_NUM_DEVS  | Any decimal value  | This defines maximum number of voice processor devices in a system  |
| TARGET              | TW   | This defines voice processor device i.e. value will "tw" for Timberwolf   |
| HBI                 | I2C,<br>SPI  | This defines physical bus in use for host control data communication to device. Value can be SPI or I2C   |
| BUILD_TYPE          | DEBUG  | Release or Debug. If release, all Print functions would be faked or none. If debug, then print functions would be enabled. User can further select a debug level. |
| HOST_ENDIAN         | big,<br>little   | Tell endianness of host platform  |
| VPROC_DEV_NAME_SIZE | Any decimal number   | Optional. Tells device name maximum size limit  |
| NUM_MAX_LOCKS       | Any decimal number   | Optional. Indicates maximum locks that can be used by driver.   |
| DEBUG_LEVEL         | Bitmask value of<br>VPROC_DBG_LVL<br>0x0 - None<br>0x1 – function entry/exit<br>0x2 - information<br>0x4 - warning<br>0x8 - Err<br>0xF - All |   |

Figure 1.3: Microsemi VPROC SDK Compile Time Options

## Chapter 2

# Module Index

### 2.1 Modules

Here is a list of all modules:

|                            |    |
|----------------------------|----|
| Host Bus Interface .....   | 9  |
| System Service Layer ..... | 16 |

## Chapter 3

# Data Structure Index

### 3.1 Data Structures

Here are the data structures with brief descriptions:

|                                   |  |    |
|-----------------------------------|--|----|
| <a href="#">hbi_data_t</a>        | Describes buffer format to be used by user to pass any data to HBI Driver.....   | 21 |
| <a href="#">hbi_dev_cfg_t</a>     | User passed in device configuration parameters at the time of open.....  | 21 |
| <a href="#">hbi_img_hdr_t</a>     | Provide header information of firmware image passed by user .....  | 22 |
| <a href="#">hbi_init_cfg_t</a>    | User-specific configuration passed at the time of driver initialization .....  | 22 |
| <a href="#">ssl_port_access_t</a> | User passed in structure during read/write done by making a call to <a href="#">SSL_port_read()</a> , <a href="#">SSL_port_write()</a> and <a href="#">SSL_port_rw()</a> ..... | 23 |



## Chapter 4

# File Index

### 4.1 File List

Here is a list of all documented files with brief descriptions:

|  |    |
|--|----|
| drivers/hbi/inc/ <a href="#">hbi.h</a> ..... | 25 |
| include/ <a href="#">ssl.h</a>               |    |
| System Service Layer .....                   | 26 |

## Chapter 5

# Module Documentation

### 5.1 Host Bus Interface

Host Bus Interface define control path communication to Microsemi VPROC Devices.

- enum `hbi_cmd_t` {  
`HBI_CMD_LOAD_FWR_FROM_HOST`, `HBI_CMD_LOAD_CFGREC_FROM_HOST`, `HBI_CMD_LOAD_FWR_COMPLETE`, `HBI_CMD_LOAD_FWRCFG_FROM_FLASH`,  
`HBI_CMD_SAVE_FWRCFG_TO_FLASH`, `HBI_CMD_ERASE_WHOLE_FLASH`, `HBI_CMD_ERASE_FWRCFG_FROM_FLASH`, `HBI_CMD_START_FWR`,  
`HBI_CMD_END` }  
*Enumerates various HOST commands that can be issued to device.*
- enum `hbi_status_t` {  
`HBI_STATUS_NOT_INIT`, `HBI_STATUS_INTERNAL_ERR`, `HBI_STATUS_RESOURCE_ERR`, `HBI_STATUS_INVALID_ARG`,  
`HBI_STATUS_BAD_HANDLE`, `HBI_STATUS_BAD_IMAGE`, `HBI_STATUS_FLASH_FULL`, `HBI_STATUS_NO_FLASH_PRESENT`,  
`HBI_STATUS_COMMAND_ERR`, `HBI_STATUS_INCOMPAT_APP`, `HBI_STATUS_INVALID_STATE`, `HBI_STATUS_OP_INCOMPLETE`,  
`HBI_STATUS_SUCCESS` }  
*enumerates various status codes of HBI Driver*
- enum `hbi_img_type_t` { `HBI_IMG_TYPE_FWR` =0, `HBI_IMG_TYPE_CR` =1, `HBI_IMG_TYPE_LAST` }  
*enumerates types of data as passed by application*
- enum `hbi_rst_mode_t` { `HBI_RST_POR` }  
*enumerates reset mode of device*
- typedef uint32\_t `hbi_handle_t`  
*HBI Handle updated by driver during HBI\_Open() call and used in further driver calls.*
- typedef unsigned char `user_buffer_t`  
*basic data type for buffer as should be used by user in call to `HBI_read()` ,`HBI_write()`*
- `hbi_status_t` `HBI_init` (`hbi_init_cfg_t` \*pCfg)
- `hbi_status_t` `HBI_term` (void)  
*Driver termination function.*
- `hbi_status_t` `HBI_open` (`hbi_handle_t` \*pHandle, `hbi_dev_cfg_t` \*pDevCfg)  
*function to open a device.*
- `hbi_status_t` `HBI_close` (`hbi_handle_t` handle)  
*function to close a device opened using `HBI_open()`.*
- `hbi_status_t` `HBI_reset` (`hbi_handle_t` handle, `hbi_rst_mode_t` mode)  
*function to reset a device.*
- `hbi_status_t` `HBI_read` (`hbi_handle_t` handle, `reg_addr_t` reg, `user_buffer_t` \*pData, `size_t` length)

*Reads the data from device memory starting from register address up to specified length.*

- **hbi\_status\_t HBI\_write** (**hbi\_handle\_t** handle, **reg\_addr\_t** reg, **user\_buffer\_t** \*pData, **size\_t** length)  
*Writes the data from user buffer to device memory starting from register address up to specified length.*
- **hbi\_status\_t HBI\_set\_command** (**hbi\_handle\_t** handle, **hbi\_cmd\_t** cmd, void \*pCmdArgs)  
*Set the Host Commands as listed in hbi\_cmd\_t enum.*
- **hbi\_status\_t HBI\_wake** (**hbi\_handle\_t** handle)  
*wakes up the device from sleep mode.*
- **hbi\_status\_t HBI\_sleep** (**hbi\_handle\_t** handle)  
*This function puts device to sleep mode.*
- **hbi\_status\_t HBI\_get\_header** (**hbi\_data\_t** \*plmg, **hbi\_img\_hdr\_t** \*pHeaderInfo)  
*This function parses header of the image passed by user.*

### 5.1.1 Detailed Description

Microsemi VPROC device control path communication is divided into transport and physical layer where Transport Layer sits on top of Physical Layer. Physical layer defines actual bus protocol to use at ground level. Current Microsemi VPROC Devices support SPI and I2C as physical interface. Transport layer defines a device-specific protocol and is termed as Host Bus Interface (HBI). Following section covers API of HBI Driver to configure VPROC Devices.

### 5.1.2 Enumeration Type Documentation

#### 5.1.2.1 enum hbi\_cmd\_t

HBI commands accept either of these types of arguments: mandatory: means this command needs an input to process optional: inputs are desirable but not mandatory. none: this command accepts no arguments. Details of argument w.r.t command is detailed as below

#### Enumerator

- HBI\_CMD\_LOAD\_FWR\_FROM\_HOST** Loads firmware image to device. [in] input args: pointer to hbi\_data\_buf\_t (populated with pointer to image buffer and length of buffer) [out] output args: none
- HBI\_CMD\_LOAD\_CFGREC\_FROM\_HOST** Loads configuration record from host to device. [in] input args: pointer to hbi\_data\_buf\_t (populated with pointer to config record buffer and length of buffer) [out] output args: none
- HBI\_CMD\_LOAD\_FWR\_COMPLETE** Signal device that host data transfer is complete. Should be called after HBI\_CMD\_LOAD\_FWR\_FROM\_HOST or HBI\_CMD\_LOAD\_CFGREC\_FROM\_HOST input args: none output args: none
- HBI\_CMD\_LOAD\_FWRCFG\_FROM\_FLASH** Loads requested firmware and associated configuration record from flash connected to voice processor device. Need image number from user. input args: an unsigned int image\_num output args: none Saves current firmware and configuration record in device memory to flash. connected to voice processor device. Should be called after HBI\_CMD\_LOAD\_FWR\_FROM\_HOST and HBI\_CMD\_HOST\_LOAD\_COMPLETE. input args: none output args: pointer to an unsigned int to pass back uploaded image number (optional)
- HBI\_CMD\_ERASE\_WHOLE\_FLASH** Erases whole flash connected to voice processor device input args: none output args: none.
- HBI\_CMD\_ERASE\_FWRCFG\_FROM\_FLASH** Erases specific firmware and associated configuration record from flash connected to voice processor device. Need an image number from user Input args: an unsigned int firmware image number output args: none
- HBI\_CMD\_START\_FWR** Starts executing current firmware in RAM after it is being loaded either from flash or host. Please note this should be called ONLY and IMMEDIATELY after HBI\_CMD\_LOAD\_FWR\_FROM\_HOST and HBI\_CMD\_HOST\_LOAD\_COMPLETE OR HBI\_CMD\_LOAD\_FWRCFG\_FROM\_FLASH. input args: none output args: none
- HBI\_CMD\_END** End marker for Host Command List.

Definition at line 74 of file hbi.h.

### 5.1.2.2 enum hbi\_status\_t

#### Enumerator

**HBI\_STATUS\_NOT\_INIT** driver not initialised  
**HBI\_STATUS\_INTERNAL\_ERR** platform specific layer reported an error  
**HBI\_STATUS\_RESOURCE\_ERR** request resource unavailable  
**HBI\_STATUS\_INVALID\_ARG** invalid argument passed to a function call  
**HBI\_STATUS\_BAD\_HANDLE** a bad reference handle passed  
**HBI\_STATUS\_BAD\_IMAGE** requested firmware image not present on flash  
**HBI\_STATUS\_FLASH\_FULL** no more space left on flash  
**HBI\_STATUS\_NO\_FLASH\_PRESENT** no flash connected to device  
**HBI\_STATUS\_COMMAND\_ERR** HBI Command failed.  
**HBI\_STATUS\_INCOMPAT\_APP** firmware image is incompatible  
**HBI\_STATUS\_INVALID\_STATE** driver is in invalid state for current action  
**HBI\_STATUS\_OP\_INCOMPLETE** operation incomplete  
**HBI\_STATUS\_SUCCESS** driver call successful

Definition at line 142 of file hbi.h.

### 5.1.2.3 enum hbi\_img\_type\_t

#### Enumerator

**HBI\_IMG\_TYPE\_FWR** firmware image to load on Device  
**HBI\_IMG\_TYPE\_CR** Configuration Record to load Device.  
**HBI\_IMG\_TYPE\_LAST** Limiter on input type.

Definition at line 162 of file hbi.h.

### 5.1.2.4 enum hbi\_rst\_mode\_t

#### Enumerator

**HBI\_RST\_POR** This simulates Power On Reset where it will clear everything including DSP memory and restart device with invocation of internal boot ROM code followed by loading of firmware from flash, if present. If flash not present in system then device will stop at Boot ROM prompt waiting on host

Definition at line 172 of file hbi.h.

## 5.1.3 Function Documentation

### 5.1.3.1 hbi\_status\_t HBI\_term ( void )

Deallocates and deinitializes all resources as acquired in HBI\_init() function. Make sure to close all of the devices and users before terminating the driver else it will return an error. function is not interrupt safe and should be called from process context.

### 5.1.3.2 hbi\_status\_t HBI\_open ( hbi\_handle\_t \* pHandle, hbi\_dev\_cfg\_t \* pDevCfg )

Inputs one-time device configuration as passed by user. User can call this function multiple time for different or same device. if called on already open device, driver will pass back already opened device reference with existing configuration and that user passed configuration will be ignored. This function is not interrupt safe.

**Parameters**

|     |                |  |
|-----|----------------|--|
| in  | <i>pHandle</i> | - Updated by driver after successful open call. Reference handle of device to be used by user in subsequent HBI driver device access call. |
| in  | <i>pDevCfg</i> | - Pointer to device configuration.   |
| out | <i>pHandle</i> | - Device handle  |

**5.1.3.3 hbi\_status\_t HBI\_close ( hbi\_handle\_t handle )**

Deallocate and free up resources acquired during open call. If there are multiple user on the device, device physically doesn't close until user count reaches to 0 otherwise it just free up user reference and access to device. This function is not interrupt safe.

**Parameters**

|    |               |   |
|----|---------------|---|
| in | <i>handle</i> | - Device handle as passed by <a href="#">HBI_open()</a> |
|----|---------------|---|

**Return values**

|                              |  |
|------------------------------|--|
| <i>HBI_STATUS_SUCCESS</i>    |  |
| <i>HBI_STATUS_BAD_HANDLE</i> |  |
| <i>HBI_STATUS_NOT_INIT</i>   |  |

**5.1.3.4 hbi\_status\_t HBI\_reset ( hbi\_handle\_t handle, hbi\_rst\_mode\_t mode )**

This function equivalent to Power On Reset. This function is not interrupt safe.

**Parameters**

|    |               |   |
|----|---------------|---|
| in | <i>handle</i> | - Device handle as returned by <a href="#">HBI_open()</a> |
| in | <i>mode</i>   | - Reset mode of device                                    |

**Return values**

|                                |  |
|--------------------------------|--|
| <i>HBI_STATUS_SUCCESS</i>      |  |
| <i>HBI_STATUS_BAD_HANDLE</i>   |  |
| <i>HBI_STATUS_NOT_INIT</i>     |  |
| <i>HBI_STATUS_INTERNAL_ERR</i> |  |

**5.1.3.5 hbi\_status\_t HBI\_read ( hbi\_handle\_t handle, reg\_addr\_t reg, user\_buffer\_t \* pData, size\_t length )**

Reads the data from device memory starting from register address up to specified length. It is caller responsibility to ensure that user buffer is sufficiently large enough to hold requested length of data else it may result in system crash. driver may expect length in multiple of 16-bit depending upon device family it is compiled for. This function is not interrupt safe.

**Parameters**

|    |               |   |
|----|---------------|---|
| in | <i>handle</i> | - Device handle as returned by <a href="#">HBI_open()</a> |
| in | <i>reg</i>    | - Device register address to read from                    |

## 5.1 Host Bus Interface

|     |               |   |
|-----|---------------|---|
| in  | <i>pData</i>  | - Pointer to user buffer to read data into  |
| in  | <i>length</i> | - length of the data to be read in bytes. should be passed based on number_of_elements_to_be_read*sizeof(user_buffer_t) |
| out | <i>pData</i>  | - updated by driver on success  |

### Return values

|                                |  |
|--------------------------------|--|
| <i>HBI_STATUS_SUCCESS</i>      |  |
| <i>HBI_STATUS_BAD_HANDLE</i>   |  |
| <i>HBI_STATUS_NOT_INIT</i>     |  |
| <i>HBI_STATUS_INTERNAL_ERR</i> |  |

#### 5.1.3.6 hbi\_status\_t HBI\_write ( hbi\_handle\_t handle, reg\_addr\_t reg, user\_buffer\_t \* pData, size\_t length )

Writes the data from user buffer to device memory starting from register address up to specified length. It is caller responsibility to ensure that user buffer is sufficiently allocated and filled to number of elements to be written. Else it may result in system crash. driver may expect length in multiple of 16-bit depending upon device family it is compiled for. This function is not interrupt safe.

### Parameters

|    |               |   |
|----|---------------|---|
| in | <i>handle</i> | - Device handle as returned by <a href="#">HBI_open()</a>   |
| in | <i>reg</i>    | - Device register address to write to   |
| in | <i>pData</i>  | - Pointer to user buffer to read data from  |
| in | <i>length</i> | - length of the data to be written in bytes. should be calculated based on sizeof(user_buffer_t)*number_of_elements_to_fill in buffer. lets say, user allocate a user_buffer_t reg[2] and what to read 2 entries of the buffer then length length = 2*sizeof(user_buffer_t) |

### Return values

|                                |  |
|--------------------------------|--|
| <i>HBI_STATUS_SUCCESS</i>      |  |
| <i>HBI_STATUS_BAD_HANDLE</i>   |  |
| <i>HBI_STATUS_NOT_INIT</i>     |  |
| <i>HBI_STATUS_INTERNAL_ERR</i> |  |

#### 5.1.3.7 hbi\_status\_t HBI\_set\_command ( hbi\_handle\_t handle, hbi\_cmd\_t cmd, void \* pCmdArgs )

Set the Host Commands as listed in hbi\_cmd\_t enum. This function is not interrupt safe.

### Parameters

|     |                 |   |
|-----|-----------------|---|
| in  | <i>handle</i>   | - Device handle as returned by <a href="#">HBI_open()</a> |
| in  | <i>cmd</i>      | - Host Command as listed in hbi_cmd_t                     |
| in  | <i>pCmdArgs</i> | - Pointer to respective command arguments                 |
| out | <i>pCmdArgs</i> | - May be updated by driver depending upon command issued. |

### Return values

|                           |  |
|---------------------------|--|
| <i>HBI_STATUS_SUCCESS</i> |  |
|---------------------------|--|

|                                |  |
|--------------------------------|--|
| <i>HBI_STATUS_BAD_HANDLE</i>   |  |
| <i>HBI_STATUS_NOT_INIT</i>     |  |
| <i>HBI_STATUS_INTERNAL_ERR</i> |  |
| <i>HBI_STATUS_INVALID_ARG</i>  |  |

#### 5.1.3.8 `hbi_status_t HBI_wake ( hbi_handle_t handle )`

Wakes up the device from sleep mode. This function is not interrupt safe.

##### Parameters

|    |               |   |
|----|---------------|---|
| in | <i>handle</i> | - Device handle as returned by <a href="#">HBI_open()</a> |
|----|---------------|---|

##### Return values

|                                |  |
|--------------------------------|--|
| <i>HBI_STATUS_SUCCESS</i>      |  |
| <i>HBI_STATUS_BAD_HANDLE</i>   |  |
| <i>HBI_STATUS_NOT_INIT</i>     |  |
| <i>HBI_STATUS_INTERNAL_ERR</i> |  |

#### 5.1.3.9 `hbi_status_t HBI_sleep ( hbi_handle_t handle )`

Puts device to sleep mode. This function is not interrupt safe.

##### Parameters

|    |               |   |
|----|---------------|---|
| in | <i>handle</i> | - Device handle as returned by <a href="#">HBI_open()</a> |
|----|---------------|---|

##### Return values

|                                |  |
|--------------------------------|--|
| <i>HBI_STATUS_SUCCESS</i>      |  |
| <i>HBI_STATUS_BAD_HANDLE</i>   |  |
| <i>HBI_STATUS_NOT_INIT</i>     |  |
| <i>HBI_STATUS_INTERNAL_ERR</i> |  |

#### 5.1.3.10 `hbi_status_t HBI_get_header ( hbi_data_t * plmg, hbi_img_hdr_t * pHeaderInfo )`

Parses Image Header. It is caller responsibility to ensure that user buffer is sufficiently allocated and filled to number of elements to be written. Else it may result in system crash. driver may expect length in multiple of 16-bit depending upon device family it is compiled for. This function is not interruptsafesafe.

##### Parameters

|     |                    |  |
|-----|--------------------|--|
| in  | <i>plmg</i>        | - pointer to a structure containing image buffer and its length      |
| out | <i>pHeaderInfo</i> | - pointer updated by function with information extracted from header |

## 5.1 Host Bus Interface

---

Return values

|                           |  |
|---------------------------|--|
| <i>HBI_STATUS_SUCCESS</i> |  |
|---------------------------|--|



## 5.2 System Service Layer

System Service Layer is Platform specific Abstraction Layer.

- enum `ssl_status_t` {  
`SSL_STATUS_NOT_INIT` = -1, `SSL_STATUS_INVALID_ARG` = -2, `SSL_STATUS_INTERNAL_ERR` = -3,  
`SSL_STATUS_BAD_HANDLE` = -4,  
`SSL_STATUS_RESOURCE_ERR` = -5, `SSL_STATUS_TIMEOUT` = -6, `SSL_STATUS_FAILED` = -7, `SSL_STATUS_OP_INCOMPLETE` = -8,  
`SSL_STATUS_OK` = 0 }  
*Enumerates Status Codes supported by SSL.*
- enum `ssl_wait_t` { `SSL_WAIT_NONE`, `SSL_WAIT_FOREVER` }  
*Enumerates wait type on lock.*
- enum `ssl_op_t` { `SSL_OP_PORT_RD` = 0x01, `SSL_OP_PORT_WR` = 0x02, `SSL_OP_PORT_RW` = (`SSL_OP_PORT_RD` | `SSL_OP_PORT_WR`) }  
*Enumerates port functions.*
- `ssl_status_t` `SSL_init` (`ssl_drv_cfg_t` \*pCfg)  
*Driver initialization function.*
- `ssl_status_t` `SSL_lock_create` (`ssl_lock_handle_t` \*pLock, const char \*pName, void \*pOption)  
*Lock Create Function.*
- `ssl_status_t` `SSL_lock` (`ssl_lock_handle_t` lock\_id, `ssl_wait_t` wait\_type)  
*Hold a lock.*
- `ssl_status_t` `SSL_unlock` (`ssl_lock_handle_t` lock\_id)  
*Release a lock.*
- `ssl_status_t` `SSL_lock_delete` (`ssl_lock_handle_t` lock\_id)  
*Deletes lock.*
- `ssl_status_t` `SSL_term` (void)  
*Terminates SSL Driver.*
- `ssl_status_t` `SSL_port_open` (`ssl_port_handle_t` \*pHandle, `ssl_dev_cfg_t` \*pDevCfg)  
*Opens an port to device.*
- `ssl_status_t` `SSL_port_close` (`ssl_port_handle_t` handle)  
*Close the port opened to device.*
- `ssl_status_t` `SSL_port_read` (`ssl_port_handle_t` handle, void \*pDst, size\_t \*pNread)  
*Reads the data from port into user buffer pointed by pDst.*
- `ssl_status_t` `SSL_port_write` (`ssl_port_handle_t` handle, void \*pSrc, size\_t \*pNwrite)  
*Write the data from user buffer to device.*
- `ssl_status_t` `SSL_port_rw` (`ssl_port_handle_t` handle, `ssl_port_access_t` \*pPort)  
*Read and Writes the data from and to device.*
- `ssl_status_t` `SSL_memset` (void \*pDst, int32\_t val, size\_t size)  
*Sets the content of memory to specified value.*
- `ssl_status_t` `SSL_memcpy` (void \*pDst, const void \*pSrc, size\_t size)  
*Copy the content of memory from source to destination pointer.*
- `ssl_status_t` `SSL_delay` (uint32\_t msec)  
*Implements delay in millisecond.*

### 5.2.1 Detailed Description

This layer prototypes API and data structures. It should be implemented by developer responsible for porting HBI driver over host platform.

## 5.2 System Service Layer

### 5.2.2 Enumeration Type Documentation

#### 5.2.2.1 enum ssl\_status\_t

##### Enumerator

- SSL\_STATUS\_NOT\_INIT** SSL Driver not initialised. Should be returned by any other call to SSL API if called before [SSL\\_init\(\)](#)
- SSL\_STATUS\_INVALID\_ARG** Invalid argument passed to SSL API. Should be returned by every AAL API, if given argument doesn't fall in supported range
- SSL\_STATUS\_INTERNAL\_ERR** Indicates to caller that an error is reported from platform specific layer.
- SSL\_STATUS\_BAD\_HANDLE** Invalid port handle passed. Should be returned by an API if called before successful [SSL\\_port\\_open\(\)](#)
- SSL\_STATUS\_RESOURCE\_ERR** Requested resource unavailable. Should be returned by SSL API if requested resource not available. Example memory, bus, device
- SSL\_STATUS\_TIMEOUT** Return if API times out waiting on a certain operation.
- SSL\_STATUS\_FAILED** SSL API failed. A more generic message to indicate a call failure for any reason than listed above
- SSL\_STATUS\_OP\_INCOMPLETE** port read/write incomplete. May be returned when number of bytes read/written is less than actual requested. This is optional and depends upon developer choice of [SSL\\_read\(\)](#) and [SSL\\_write\(\)](#) implementation
- SSL\_STATUS\_OK** SSL API call successful.

Definition at line 41 of file ssl.h.

#### 5.2.2.2 enum ssl\_wait\_t

Depends on System Service Layer implementation and support of locking mechanism.

if Port layer does not support locking, these can be don't care or stub.

##### Enumerator

- SSL\_WAIT\_NONE** Return immediately, if failed to get lock.
- SSL\_WAIT\_FOREVER** Wait until lock is attained. Please note this may block the call.

Definition at line 89 of file ssl.h.

#### 5.2.2.3 enum ssl\_op\_t

Can be 'Read Only', 'Write Only' or 'Read/Write' both

##### Enumerator

- SSL\_OP\_PORT\_RD** Read only operation on Port.
- SSL\_OP\_PORT\_WR** Write only operation on Port.

Definition at line 102 of file ssl.h.

### 5.2.3 Function Documentation

#### 5.2.3.1 ssl\_status\_t SSL\_init ( ssl\_drv\_cfg\_t \* pCfg )

Developer should Allocate and initialize resources as required by driver in this function call. pCfg is optional argument and host system specific. if required, developer may add initialization configuration parameter. Definition of data type `ssl_drv_cfg_t` is developer-specific and should be defined in `typedefs.h` in `sdk`. if used, it should be one-time configuration parameter and should not be modified.

**Parameters**

|    |             |   |
|----|-------------|---|
| in | <i>pCfg</i> | Pointer to driver init configuration. This is optional parameter. |
|----|-------------|---|

**5.2.3.2 ssl\_status\_t SSL\_lock\_create ( ssl\_lock\_handle\_t \* pLock, const char \* pName, void \* pOption )**

This function implements Locking and Unlocking mechanism. pName and pOption are optional parameter and are implementation specific. ssl\_lock\_handle\_t is host specific data type and should be declared by developer in typedefs.h

**Parameters**

|     |                |  |
|-----|----------------|--|
| in  | <i>pLock</i>   | Pointer to Lock structure. Should not be NULL.                                 |
| in  | <i>pName</i>   | Optional Null terminated string to identify lock                               |
| in  | <i>pOption</i> | Optional pointer to options structure as declared by host system in typedef.h. |
| out | <i>pLock</i>   | Updated by call on successful completion                                       |

**5.2.3.3 ssl\_status\_t SSL\_lock ( ssl\_lock\_handle\_t lock\_id, ssl\_wait\_t wait\_type )**

This function is called to hold a Locking. This can act as stub function if implementation does not support a locking mechanism.

**Parameters**

|    |                  |   |
|----|------------------|---|
| in | <i>lock_id</i>   | handle as returned by call to <a href="#">SSL_lock_create()</a> |
| in | <i>wait_type</i> | enum indicating WAIT_FOREVER or WAIT_NONE                       |

**5.2.3.4 ssl\_status\_t SSL\_unlock ( ssl\_lock\_handle\_t lock\_id )**

This function is called to release a Lock. This can act as stub function if implementation doesnt support a locking mechanism.

**Parameters**

|    |                |   |
|----|----------------|---|
| in | <i>lock_id</i> | handle as returned by call to <a href="#">SSL_lock_create()</a> |
|----|----------------|---|

**5.2.3.5 ssl\_status\_t SSL\_lock\_delete ( ssl\_lock\_handle\_t lock\_id )**

This function is called to delete a Lock created by call to SSL\_lock\_create.

**Parameters**

|    |                |   |
|----|----------------|---|
| in | <i>lock_id</i> | handle as returned by call to <a href="#">SSL_lock_create()</a> |
|----|----------------|---|

**5.2.3.6 ssl\_status\_t SSL\_term ( void )**

This function should deallocate and free all resources as acquired during [SSL\\_init\(\)](#) call.

**5.2.3.7 ssl\_status\_t SSL\_port\_open ( ssl\_port\_handle\_t \* pHandle, ssl\_dev\_cfg\_t \* pDevCfg )**

This function should initialise, allocate and setup all necessary infrastructure required to transmit data to and from device. Initialisation of SPI / I2C interface happen here. User should be able to do read/write to device after successful execution of this API

## 5.2 System Service Layer

### Parameters

|     |                 |   |
|-----|-----------------|---|
| in  | <i>*pHandle</i> | Pointer to port handle. <code>ssl_port_handle_t</code> data type should be declared by developer in <code>typedefs.h</code>   |
| in  | <i>*pDevCfg</i> | Pointer to device configuration. User passed in bus number, chip address or id. Example for SPI, bus number can be spi bus number and device address can be chip select value. For i2c, device address can be allowable range of i2c addresses. |
| out | <i>pHandle</i>  | Should be updated by driver on successful execution of call   |

#### 5.2.3.8 `ssl_status_t SSL_port_close ( ssl_port_handle_t handle )`

This function should deallocate and free all resources as acquired during `SSL_port_open()` call. After successful execution of this function, user should not be able to send/receive data to device.

### Parameters

|               |   |
|---------------|---|
| <i>handle</i> | Port Handle as returned by <code>SSL_port_open()</code> |
|---------------|---|

#### 5.2.3.9 `ssl_status_t SSL_port_read ( ssl_port_handle_t handle, void * pDst, size_t * pNread )`

This function read data from device into user buffer pointed by `pDst`

### Parameters

|     |               |   |
|-----|---------------|---|
| in  | <i>handle</i> | Port Handle as returned by <code>SSL_port_open()</code>                   |
| in  | <i>pDst</i>   | Pointer to user passed in destination buffer to read data in to           |
| in  | <i>pNread</i> | Pointer to variable containing size of data to read                       |
| out | <i>pNread</i> | Optional. May be updated by driver to indicate size of data actually read |

#### 5.2.3.10 `ssl_status_t SSL_port_write ( ssl_port_handle_t handle, void * pSrc, size_t * pNwrite )`

This function writes user passed data in `pSrc` buffer to device.

### Parameters

|     |                |  |
|-----|----------------|--|
| in  | <i>handle</i>  | Port Handle as returned by <code>SSL_port_open()</code>                      |
| in  | <i>pSrc</i>    | Pointer to source buffer containing data to be sent to device                |
| in  | <i>pNwrite</i> | Pointer to buffer containing size of data to be written                      |
| out | <i>pNwrite</i> | Optional. May be updated by driver to indicate size of data actually written |

#### 5.2.3.11 `ssl_status_t SSL_port_rw ( ssl_port_handle_t handle, ssl_port_access_t * pPort )`

This function performs both read/write transaction to device in a single call. May be supported by driver for combined transactions.

### Parameters

|     |               |   |
|-----|---------------|---|
| in  | <i>handle</i> | Port Handle as returned by <code>SSL_port_open()</code>                         |
| in  | <i>pPort</i>  | Pointer to structure populated with valid arguments for read/write transactions |
| out | <i>pPort</i>  | May update size of data actually read/written in <code>pPort</code> structure   |

#### 5.2.3.12 `ssl_status_t SSL_memset ( void * pDst, int32_t val, size_t size )`

Sets the content of memory to specified value. May be ported over system specific `memset` call.

## Parameters

|    |             |  |
|----|-------------|--|
| in | <i>pDst</i> | Pointer to memory location to be updated |
| in | <i>val</i>  | Value to be written to                   |
| in | <i>size</i> | size of the memory to be updated         |

5.2.3.13 **ssl\_status\_t** SSL\_memcpy ( void \* *pDst*, const void \* *pSrc*, size\_t *size* )

Copies the content of source buffer to destination buffer. May be ported over system specific memcpy call.

## Parameters

|    |             |  |
|----|-------------|--|
| in | <i>pDst</i> | Pointer to memory location to be copied to |
| in | <i>pSrc</i> | Pointer to memory location to copy from    |
| in | <i>size</i> | size of the data to be copied              |

5.2.3.14 **ssl\_status\_t** SSL\_delay ( uint32\_t *tmsec* )

Implements delay in milliseconds . May be ported over system specific delay/sleep call.

## Parameters

|    |              |                        |
|----|--------------|------------------------|
| in | <i>tmsec</i> | - time in milliseconds |
|----|--------------|------------------------|

## Chapter 6

# Data Structure Documentation

### 6.1 hbi\_data\_t Struct Reference

describes buffer format to be used by user to pass any data to HBI Driver.

```
#include <hbi.h>
```

#### Data Fields

- unsigned char \*[pData](#)  
*pointer to user data buffer*
- size\_t [size](#)  
*length of the buffer in bytes*

#### 6.1.1 Detailed Description

Definition at line 198 of file hbi.h.

The documentation for this struct was generated from the following file:

- drivers/hbi/inc/[hbi.h](#)

### 6.2 hbi\_dev\_cfg\_t Struct Reference

user passed in device configuration parameters at the time of open.

```
#include <hbi.h>
```

#### Data Fields

- uint8\_t [dev\\_addr](#)  
*device address.*
- uint8\_t \*[pDevName](#)  
*an optional pointer to device name*
- uint8\_t [bus\\_num](#)  
*bus number device physically present on*
- ssl\_lock\_handle\_t [dev\\_lock](#)  
*lock to serialise device access*

### 6.2.1 Detailed Description

Definition at line 186 of file hbi.h.

### 6.2.2 Field Documentation

#### 6.2.2.1 uint8\_t dev\_addr

can be i2c complaint or spi chip select id

Definition at line 188 of file hbi.h.

The documentation for this struct was generated from the following file:

- drivers/hbi/inc/[hbi.h](#)

## 6.3 hbi\_img\_hdr\_t Struct Reference

provide header information of firmware image passed by user

```
#include <hbi.h>
```

### Data Fields

- int [major\\_ver](#)  
*header version major num*
- int [minor\\_ver](#)  
*header version minor num*
- [hbi\\_img\\_type\\_t](#) [image\\_type](#)  
*firmware or configuration record*
- int [endianness](#)  
*endianness of the image excluding header*
- int [fwr\\_code](#)  
*if image\_type == firmware, tells the firmware opn code*
- size\_t [block\\_size](#)  
*block length in words image is divided into*
- size\_t [img\\_len](#)  
*total length of the image excluding header*
- int [hdr\\_len](#)  
*length of header*

### 6.3.1 Detailed Description

Definition at line 218 of file hbi.h.

The documentation for this struct was generated from the following file:

- drivers/hbi/inc/[hbi.h](#)

## 6.4 hbi\_init\_cfg\_t Struct Reference

user-specific configuration passed at the time of driver initialization

```
#include <hbi.h>
```

## 6.5 ssl\_port\_access\_t Struct Reference

---

### Data Fields

- [ssl\\_lock\\_handle\\_t lock](#)  
*Driver serialising lock.*

### 6.4.1 Detailed Description

Definition at line 207 of file hbi.h.

### 6.4.2 Field Documentation

#### 6.4.2.1 ssl\_lock\_handle\_t lock

supposed to be created and passed by user. if passed, then driver will serialize all calls using this lock.

Definition at line 209 of file hbi.h.

The documentation for this struct was generated from the following file:

- drivers/hbi/inc/[hbi.h](#)

## 6.5 ssl\_port\_access\_t Struct Reference

user passed in structure during read/write done by making a call to [SSL\\_port\\_read\(\)](#), [SSL\\_port\\_write\(\)](#) and [SSL\\_port\\_rw\(\)](#)

```
#include <ssl.h>
```

### Data Fields

- void \*[pSrc](#)  
*Pointer to caller source buffer.*
- void \*[pDst](#)  
*Pointer to caller destination buffer, Must be set to a valid value for PORT READ Operation.*
- size\_t [nread](#)  
*Number of bytes to read.*
- size\_t [nwrite](#)  
*Number of bytes to write.*
- [ssl\\_op\\_t op\\_type](#)  
*Enum indicating port operation: 'read', 'write' or 'read/write'.*

### 6.5.1 Detailed Description

Definition at line 115 of file ssl.h.

### 6.5.2 Field Documentation

#### 6.5.2.1 void\* pSrc

Must be set to a valid value for PORT WRITE Operation

Definition at line 117 of file ssl.h.



#### 6.5.2.2 size\_t nread

Ignore in case of PORT Write operation

Definition at line 123 of file ssl.h.

#### 6.5.2.3 size\_t nwrite

Ignore in case of PORT Write operation

Definition at line 126 of file ssl.h.

The documentation for this struct was generated from the following file:

- include/[ssl.h](#)

## Chapter 7

# File Documentation

### 7.1 drivers/hbi/inc/hbi.h File Reference

#### Data Structures

- struct [hbi\\_dev\\_cfg\\_t](#)  
*user passed in device configuration parameters at the time of open.*
- struct [hbi\\_data\\_t](#)  
*describes buffer format to be used by user to pass any data to HBI Driver.*
- struct [hbi\\_init\\_cfg\\_t](#)  
*user-specific configuration passed at the time of driver initialization*
- struct [hbi\\_img\\_hdr\\_t](#)  
*provide header information of firmware image passed by user*
- enum [hbi\\_cmd\\_t](#) {  
[HBI\\_CMD\\_LOAD\\_FWR\\_FROM\\_HOST](#), [HBI\\_CMD\\_LOAD\\_CFGREC\\_FROM\\_HOST](#), [HBI\\_CMD\\_LOAD\\_FWR\\_COMPLETE](#), [HBI\\_CMD\\_LOAD\\_FWRCFG\\_FROM\\_FLASH](#),  
[HBI\\_CMD\\_SAVE\\_FWRCFG\\_TO\\_FLASH](#), [HBI\\_CMD\\_ERASE\\_WHOLE\\_FLASH](#), [HBI\\_CMD\\_ERASE\\_FWR\\_CFG\\_FROM\\_FLASH](#), [HBI\\_CMD\\_START\\_FWR](#),  
[HBI\\_CMD\\_END](#) }  
*Enumerates various HOST commands that can be issued to device.*
- enum [hbi\\_status\\_t](#) {  
[HBI\\_STATUS\\_NOT\\_INIT](#), [HBI\\_STATUS\\_INTERNAL\\_ERR](#), [HBI\\_STATUS\\_RESOURCE\\_ERR](#), [HBI\\_STATUS\\_INVALID\\_ARG](#),  
[HBI\\_STATUS\\_BAD\\_HANDLE](#), [HBI\\_STATUS\\_BAD\\_IMAGE](#), [HBI\\_STATUS\\_FLASH\\_FULL](#), [HBI\\_STATUS\\_NO\\_FLASH\\_PRESENT](#),  
[HBI\\_STATUS\\_COMMAND\\_ERR](#), [HBI\\_STATUS\\_INCOMPAT\\_APP](#), [HBI\\_STATUS\\_INVALID\\_STATE](#), [HBI\\_STATUS\\_OP\\_INCOMPLETE](#),  
[HBI\\_STATUS\\_SUCCESS](#) }  
*enumerates various status codes of HBI Driver*
- enum [hbi\\_img\\_type\\_t](#) { [HBI\\_IMG\\_TYPE\\_FWR](#) =0, [HBI\\_IMG\\_TYPE\\_CR](#) =1, [HBI\\_IMG\\_TYPE\\_LAST](#) }  
*enumerates types of data as passed by application*
- enum [hbi\\_rst\\_mode\\_t](#) { [HBI\\_RST\\_POR](#) }  
*enumerates reset mode of device*
- typedef uint32\_t [hbi\\_handle\\_t](#)  
*HBI Handle updated by driver during HBI\_Open() call and used in further driver calls.*
- typedef unsigned char [user\\_buffer\\_t](#)  
*basic data type for buffer as should be used by user in call to [HBI\\_read\(\)](#) ,[HBI\\_write\(\)](#)*
- [hbi\\_status\\_t](#) [HBI\\_init](#) ([hbi\\_init\\_cfg\\_t](#) \*pCfg)

- [hbi\\_status\\_t HBI\\_term](#) (void)  
*Driver termination function.*
- [hbi\\_status\\_t HBI\\_open](#) ([hbi\\_handle\\_t](#) \*pHandle, [hbi\\_dev\\_cfg\\_t](#) \*pDevCfg)  
*function to open a device.*
- [hbi\\_status\\_t HBI\\_close](#) ([hbi\\_handle\\_t](#) handle)  
*function to close a device opened using [HBI\\_open\(\)](#).*
- [hbi\\_status\\_t HBI\\_reset](#) ([hbi\\_handle\\_t](#) handle, [hbi\\_rst\\_mode\\_t](#) mode)  
*function to reset a device.*
- [hbi\\_status\\_t HBI\\_read](#) ([hbi\\_handle\\_t](#) handle, [reg\\_addr\\_t](#) reg, [user\\_buffer\\_t](#) \*pData, [size\\_t](#) length)  
*Reads the data from device memory starting from register address up to specified length.*
- [hbi\\_status\\_t HBI\\_write](#) ([hbi\\_handle\\_t](#) handle, [reg\\_addr\\_t](#) reg, [user\\_buffer\\_t](#) \*pData, [size\\_t](#) length)  
*Writes the data from user buffer to device memory starting from register address up to specified length.*
- [hbi\\_status\\_t HBI\\_set\\_command](#) ([hbi\\_handle\\_t](#) handle, [hbi\\_cmd\\_t](#) cmd, void \*pCmdArgs)  
*Set the Host Commands as listed in [hbi\\_cmd\\_t](#) enum.*
- [hbi\\_status\\_t HBI\\_wake](#) ([hbi\\_handle\\_t](#) handle)  
*wakes up the device from sleep mode.*
- [hbi\\_status\\_t HBI\\_sleep](#) ([hbi\\_handle\\_t](#) handle)  
*This function puts device to sleep mode.*
- [hbi\\_status\\_t HBI\\_get\\_header](#) ([hbi\\_data\\_t](#) \*pImg, [hbi\\_img\\_hdr\\_t](#) \*pHeaderInfo)  
*This function parses header of the image passed by user.*

## 7.2 include/ssl.h File Reference

System Service Layer.

### Data Structures

- struct [ssl\\_port\\_access\\_t](#)  
*user passed in structure during read/write done by making a call to [SSL\\_port\\_read\(\)](#), [SSL\\_port\\_write\(\)](#) and [SSL\\_port\\_rw\(\)](#)*
- enum [ssl\\_status\\_t](#) {  
[SSL\\_STATUS\\_NOT\\_INIT](#) = -1, [SSL\\_STATUS\\_INVALID\\_ARG](#) = -2, [SSL\\_STATUS\\_INTERNAL\\_ERR](#) = -3,  
[SSL\\_STATUS\\_BAD\\_HANDLE](#) = -4,  
[SSL\\_STATUS\\_RESOURCE\\_ERR](#) = -5, [SSL\\_STATUS\\_TIMEOUT](#) = -6, [SSL\\_STATUS\\_FAILED](#) = -7, [SSL\\_STATUS\\_OP\\_INCOMPLETE](#) = -8,  
[SSL\\_STATUS\\_OK](#) = 0 }  
*Enumerates Status Codes supported by SSL.*
- enum [ssl\\_wait\\_t](#) { [SSL\\_WAIT\\_NONE](#), [SSL\\_WAIT\\_FOREVER](#) }  
*Enumerates wait type on lock.*
- enum [ssl\\_op\\_t](#) { [SSL\\_OP\\_PORT\\_RD](#) = 0x01, [SSL\\_OP\\_PORT\\_WR](#) = 0x02, [SSL\\_OP\\_PORT\\_RW](#) = ([SSL\\_OP\\_PORT\\_RD](#) | [SSL\\_OP\\_PORT\\_WR](#)) }  
*Enumerates port functions.*
- [ssl\\_status\\_t SSL\\_init](#) ([ssl\\_drv\\_cfg\\_t](#) \*pCfg)  
*Driver initialization function.*
- [ssl\\_status\\_t SSL\\_lock\\_create](#) ([ssl\\_lock\\_handle\\_t](#) \*pLock, const char \*pName, void \*pOption)  
*Lock Create Function.*
- [ssl\\_status\\_t SSL\\_lock](#) ([ssl\\_lock\\_handle\\_t](#) lock\_id, [ssl\\_wait\\_t](#) wait\_type)  
*Hold a lock.*
- [ssl\\_status\\_t SSL\\_unlock](#) ([ssl\\_lock\\_handle\\_t](#) lock\_id)

## 7.2 include/ssl.h File Reference

- Release a lock.*
  - [ssl\\_status\\_t SSL\\_lock\\_delete](#) (ssl\_lock\_handle\_t lock\_id)  
*Deletes lock.*
- [ssl\\_status\\_t SSL\\_term](#) (void)  
*Terminates SSL Driver.*
- [ssl\\_status\\_t SSL\\_port\\_open](#) (ssl\_port\_handle\_t \*pHandle, ssl\_dev\_cfg\_t \*pDevCfg)  
*Opens an port to device.*
- [ssl\\_status\\_t SSL\\_port\\_close](#) (ssl\_port\_handle\_t handle)  
*Close the port opened to device.*
- [ssl\\_status\\_t SSL\\_port\\_read](#) (ssl\_port\_handle\_t handle, void \*pDst, size\_t \*pNread)  
*Reads the data from port into user buffer pointed by pDst.*
- [ssl\\_status\\_t SSL\\_port\\_write](#) (ssl\_port\_handle\_t handle, void \*pSrc, size\_t \*pNwrite)  
*Write the data from user buffer to device.*
- [ssl\\_status\\_t SSL\\_port\\_rw](#) (ssl\_port\_handle\_t handle, [ssl\\_port\\_access\\_t](#) \*pPort)  
*Read and Writes the data from and to device.*
- [ssl\\_status\\_t SSL\\_memset](#) (void \*pDst, int32\_t val, size\_t size)  
*Sets the content of memory to specified value.*
- [ssl\\_status\\_t SSL\\_memcpy](#) (void \*pDst, const void \*pSrc, size\_t size)  
*Copy the content of memory from source to destination pointer.*
- [ssl\\_status\\_t SSL\\_delay](#) (uint32\_t tmsec)  
*Implements delay in millisecond.*

### 7.2.1 Detailed Description

System Service Layer is combination of 3 header files :

[ssl.h](#) - Master file that prototypes functions and data structures to be ported

[typedefs.h](#) - Base file defining data types as used by layers above

[vproc\\_dbg.h](#) - Debug header file ported for debug messages. If no debugging supported, implementation to leave function/macro as stub but still should define

This is Platform-Specific layer and must be ported to host platform-specific function for correct functioning of HBI Driver over host platform

Expected and Intended use of data structures and functions are explained here. In case of confusion, developer may reach at <>

Definition in file [ssl.h](#).

# Index

- dev\_addr
  - hbi\_dev\_cfg\_t, [22](#)
- drivers/hbi/inc/hbi.h, [25](#)
- HBI\_CMD\_END
    - Host Bus Interface, [10](#)
  - HBI\_CMD\_ERASE\_FWRCFG\_FROM\_FLASH
    - Host Bus Interface, [10](#)
  - HBI\_CMD\_ERASE\_WHOLE\_FLASH
    - Host Bus Interface, [10](#)
  - HBI\_CMD\_LOAD\_CFGREC\_FROM\_HOST
    - Host Bus Interface, [10](#)
  - HBI\_CMD\_LOAD\_FWR\_COMPLETE
    - Host Bus Interface, [10](#)
  - HBI\_CMD\_LOAD\_FWR\_FROM\_HOST
    - Host Bus Interface, [10](#)
  - HBI\_CMD\_LOAD\_FWRCFG\_FROM\_FLASH
    - Host Bus Interface, [10](#)
  - HBI\_CMD\_START\_FWR
    - Host Bus Interface, [10](#)
  - HBI\_IMG\_TYPE\_CR
    - Host Bus Interface, [11](#)
  - HBI\_IMG\_TYPE\_FWR
    - Host Bus Interface, [11](#)
  - HBI\_IMG\_TYPE\_LAST
    - Host Bus Interface, [11](#)
  - HBI\_RST\_POR
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_BAD\_HANDLE
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_BAD\_IMAGE
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_COMMAND\_ERR
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_FLASH\_FULL
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_INCOMPAT\_APP
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_INTERNAL\_ERR
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_INVALID\_ARG
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_INVALID\_STATE
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_NO\_FLASH\_PRESENT
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_NOT\_INIT
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_OP\_INCOMPLETE
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_RESOURCE\_ERR
    - Host Bus Interface, [11](#)
  - HBI\_STATUS\_SUCCESS
    - Host Bus Interface, [11](#)
  - HBI\_close
    - Host Bus Interface, [12](#)
  - HBI\_get\_header
    - Host Bus Interface, [14](#)
  - HBI\_open
    - Host Bus Interface, [11](#)
  - HBI\_read
    - Host Bus Interface, [12](#)
  - HBI\_reset
    - Host Bus Interface, [12](#)
  - HBI\_set\_command
    - Host Bus Interface, [13](#)
  - HBI\_sleep
    - Host Bus Interface, [14](#)
  - HBI\_term
    - Host Bus Interface, [11](#)
  - HBI\_wake
    - Host Bus Interface, [14](#)
  - HBI\_write
    - Host Bus Interface, [13](#)
  - hbi\_cmd\_t
    - Host Bus Interface, [10](#)
  - hbi\_data\_t, [21](#)
  - hbi\_dev\_cfg\_t, [21](#)
    - dev\_addr, [22](#)
  - hbi\_img\_hdr\_t, [22](#)
  - hbi\_img\_type\_t
    - Host Bus Interface, [11](#)
  - hbi\_init\_cfg\_t, [22](#)
    - lock, [23](#)
  - hbi\_rst\_mode\_t
    - Host Bus Interface, [11](#)
  - hbi\_status\_t
    - Host Bus Interface, [10](#)
  - Host Bus Interface, [9](#)
    - HBI\_CMD\_END, [10](#)
    - HBI\_CMD\_ERASE\_FWRCFG\_FROM\_FLASH, [10](#)
    - HBI\_CMD\_ERASE\_WHOLE\_FLASH, [10](#)
    - HBI\_CMD\_LOAD\_CFGREC\_FROM\_HOST, [10](#)
    - HBI\_CMD\_LOAD\_FWR\_COMPLETE, [10](#)
    - HBI\_CMD\_LOAD\_FWR\_FROM\_HOST, [10](#)
    - HBI\_CMD\_LOAD\_FWRCFG\_FROM\_FLASH, [10](#)
    - HBI\_CMD\_START\_FWR, [10](#)
    - HBI\_IMG\_TYPE\_CR, [11](#)
    - HBI\_IMG\_TYPE\_FWR, [11](#)

- HBI\_IMG\_TYPE\_LAST, [11](#)
- HBI\_RST\_POR, [11](#)
- HBI\_STATUS\_BAD\_HANDLE, [11](#)
- HBI\_STATUS\_BAD\_IMAGE, [11](#)
- HBI\_STATUS\_COMMAND\_ERR, [11](#)
- HBI\_STATUS\_FLASH\_FULL, [11](#)
- HBI\_STATUS\_INCOMPAT\_APP, [11](#)
- HBI\_STATUS\_INTERNAL\_ERR, [11](#)
- HBI\_STATUS\_INVALID\_ARG, [11](#)
- HBI\_STATUS\_INVALID\_STATE, [11](#)
- HBI\_STATUS\_NO\_FLASH\_PRESENT, [11](#)
- HBI\_STATUS\_NOT\_INIT, [11](#)
- HBI\_STATUS\_OP\_INCOMPLETE, [11](#)
- HBI\_STATUS\_RESOURCE\_ERR, [11](#)
- HBI\_STATUS\_SUCCESS, [11](#)
- HBI\_close, [12](#)
- HBI\_get\_header, [14](#)
- HBI\_open, [11](#)
- HBI\_read, [12](#)
- HBI\_reset, [12](#)
- HBI\_set\_command, [13](#)
- HBI\_sleep, [14](#)
- HBI\_term, [11](#)
- HBI\_wake, [14](#)
- HBI\_write, [13](#)
- hbi\_cmd\_t, [10](#)
- hbi\_img\_type\_t, [11](#)
- hbi\_rst\_mode\_t, [11](#)
- hbi\_status\_t, [10](#)
- include/ssl.h, [26](#)
- lock
  - hbi\_init\_cfg\_t, [23](#)
- nread
  - ssl\_port\_access\_t, [23](#)
- nwrite
  - ssl\_port\_access\_t, [24](#)
- pSrc
  - ssl\_port\_access\_t, [23](#)
- SSL\_OP\_PORT\_RD
  - System Service Layer, [17](#)
- SSL\_OP\_PORT\_WR
  - System Service Layer, [17](#)
- SSL\_STATUS\_BAD\_HANDLE
  - System Service Layer, [17](#)
- SSL\_STATUS\_FAILED
  - System Service Layer, [17](#)
- SSL\_STATUS\_INTERNAL\_ERR
  - System Service Layer, [17](#)
- SSL\_STATUS\_INVALID\_ARG
  - System Service Layer, [17](#)
- SSL\_STATUS\_NOT\_INIT
  - System Service Layer, [17](#)
- SSL\_STATUS\_OK
  - System Service Layer, [17](#)
- SSL\_STATUS\_OP\_INCOMPLETE
  - System Service Layer, [17](#)
- SSL\_STATUS\_RESOURCE\_ERR
  - System Service Layer, [17](#)
- SSL\_STATUS\_TIMEOUT
  - System Service Layer, [17](#)
- SSL\_WAIT\_FOREVER
  - System Service Layer, [17](#)
- SSL\_WAIT\_NONE
  - System Service Layer, [17](#)
- SSL\_delay
  - System Service Layer, [20](#)
- SSL\_init
  - System Service Layer, [17](#)
- SSL\_lock
  - System Service Layer, [18](#)
- SSL\_lock\_create
  - System Service Layer, [18](#)
- SSL\_lock\_delete
  - System Service Layer, [18](#)
- SSL\_memcpy
  - System Service Layer, [20](#)
- SSL\_memset
  - System Service Layer, [19](#)
- SSL\_port\_close
  - System Service Layer, [19](#)
- SSL\_port\_open
  - System Service Layer, [18](#)
- SSL\_port\_read
  - System Service Layer, [19](#)
- SSL\_port\_rw
  - System Service Layer, [19](#)
- SSL\_port\_write
  - System Service Layer, [19](#)
- SSL\_term
  - System Service Layer, [18](#)
- SSL\_unlock
  - System Service Layer, [18](#)
- ssl\_op\_t
  - System Service Layer, [17](#)
- ssl\_port\_access\_t, [23](#)
  - nread, [23](#)
  - nwrite, [24](#)
  - pSrc, [23](#)
- ssl\_status\_t
  - System Service Layer, [17](#)
- ssl\_wait\_t
  - System Service Layer, [17](#)
- System Service Layer, [16](#)
  - SSL\_OP\_PORT\_RD, [17](#)
  - SSL\_OP\_PORT\_WR, [17](#)
  - SSL\_STATUS\_BAD\_HANDLE, [17](#)
  - SSL\_STATUS\_FAILED, [17](#)
  - SSL\_STATUS\_INTERNAL\_ERR, [17](#)
  - SSL\_STATUS\_INVALID\_ARG, [17](#)
  - SSL\_STATUS\_NOT\_INIT, [17](#)
  - SSL\_STATUS\_OK, [17](#)
  - SSL\_STATUS\_OP\_INCOMPLETE, [17](#)

SSL\_STATUS\_RESOURCE\_ERR, [17](#)  
SSL\_STATUS\_TIMEOUT, [17](#)  
SSL\_WAIT\_FOREVER, [17](#)  
SSL\_WAIT\_NONE, [17](#)  
SSL\_delay, [20](#)  
SSL\_init, [17](#)  
SSL\_lock, [18](#)  
SSL\_lock\_create, [18](#)  
SSL\_lock\_delete, [18](#)  
SSL\_memcpy, [20](#)  
SSL\_memset, [19](#)  
SSL\_port\_close, [19](#)  
SSL\_port\_open, [18](#)  
SSL\_port\_read, [19](#)  
SSL\_port\_rw, [19](#)  
SSL\_port\_write, [19](#)  
SSL\_term, [18](#)  
SSL\_unlock, [18](#)  
ssl\_op\_t, [17](#)  
ssl\_status\_t, [17](#)  
ssl\_wait\_t, [17](#)