

Microsemi VPROC Apps  
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# Chapter 1

## Main Page

### 1.1 Introduction

This is an document summarizes test suite for verifying Microsemi Voice Device SDK.

### 1.2 Build Instructions

Sample Makefile available with apps that build sample applicatons for linux platform as userspace apps. User can modify Makefile as per their platform. In exisiting Makefile,all sample applications are built as an independent execuatable binaries.hbi\_test sample app is built by default. To enable compilation of other sample, app following TEST Macros are defined

HBI\_LOAD\_GRAMMAR - Builds grammar loading

HBI\_LOAD\_FIRMWARE - Build firmware and configuration record loading app

Example Make Command:

make apps HBI\_TEST=1- This build only hbi\_test sample app

make apps HBI\_LOAD\_GRAMMAR=1 - Builds hbi\_load\_grammar

make apps TEST\_LDFWRCFG=1 - Builds hbi\_load\_firmware

## Chapter 2

# Module Index

### 2.1 Modules

Here is a list of all modules:

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## Chapter 3

# Data Structure Index

### 3.1 Data Structures

An initialized instance of that structure below must be defined within the example apps.

**.bus\_num:** specifies the SPI or I2C bus number of the host controller to which the ZL380xx device is interfaced to

**.dev\_addr:** specifies the SPI chip select or I2C address of the ZL380xx device

**.pFirmware:** must point to the firmware data array if the firmware \*.h is to be compiled with the app statically, or NULL

**.pConfig:** must point to the config record data array if the config \*.h is to be compiled with the app statically, or NULL

Example definition for two ZL380xx devices interfaced to the host via SPI bus 0 and chip selects 0 and 1.

The device Id for these definition pertains to the index of the array. Example definition below allows two device Ids 0 and 1

```
static hbi_dev_info_t hbi_devices_info[] = {  
  
    {  
        .bus_num = 0, /*SPI or I2C bus number*/  
        .dev_addr = 0, /*SPI chip select or I2C address*/  
        .pFirmware = NULL, /*a pointer to the firmware data array from *.h if in c code format*/  
        .pConfig = NULL, /*a pointer to the config data array from *.h if in c code format*/  
    },  
    {  
        .bus_num = 0,  
        .dev_addr = 1,  
        .pFirmware = NULL,  
        .pConfig = NULL,  
    }  
};
```

## Chapter 4

# File Index

### 4.1 File List

Here is a list of all documented files with brief descriptions:

apps/app_util.c . . . . .	??
apps/app_util.h . . . . .	??
apps/hbi_test.c.....	<a href="#">15</a>
apps/hbi_load_grammar . . . . .	??
apps/hbi_load_firmware.c . . . . .	??
	??
	??

## Chapter 5

# Module Documentation

### 5.1 hbi\_test

hbi\_test is a simple application to test HBI driver functions and demonstrate its usage.

#### 5.1.1 Detailed Description

Test is based on various TEST macros and command line options User can enable/disable them at compile-time to validate specific set of functions.

Test Macros to test various hbi driver feature

TEST\_RST - Resets the device. A successful reset verify HBI\_read and HBI\_write function calls

TEST\_LOAD\_FWRCFG\_FROM\_FLASH - Test firmware loading from flash. Load image number 1 from flash.

TEST\_ERASE\_IMAGE - Test Erasing a particular image from flash. Erase image number 1 from flash

TEST\_ERASE\_FLASH - Test Erasing whole flash

To Execute test case, simply run:

```
. hbi_test
```

To read specific register

```
. hbi_test -d <device Id> -r <reg address="" in="" hex>=""> <number of="" bytes="" to="" read="" in="" decimal>="">, example To read 10 bytes from address 0x200
```

```
. hbi_test -d 0 -r 0x200 10
```

To write single word at specific register

```
hbi_test -d <device Id> -w <reg address="" in="" hex>=""> <16-bit length word>
```

Example, to write 0xDEAD at register 0x200 to device Id 0

```
hbi_test -d 0 -w 0x200 0xDEAD
```

To write multiple words starting register 0x200

```
hbi_test -d 0 -w 0x200 0xDEAD 0xBEEF
```

To Display help menu

```
. hbi_test -h
```

## 5.2 hbi\_load\_grammar

hbi\_load\_grammar is a sample application to load an ASR grammar into the ZL380xx device and optionally save it to flash.

### 5.2.1 Detailed Description

The application allows to load a \*.bin grammar file into the device at runtime. The binary (\*.bin) grammar image must be generated using the Python tool /apps/Python/tw\_grammar\_converter.py

The generated \*.bin grammar file is a combination of both an ASR command file and an ASR trigger file

Press 'ctrl-c' to exit test.

For more description, please run 'hbi\_load\_grammar -h'.

Test support two input options at run time: -h = help display

Usage: ./hbi\_load\_grammar -[d deviceId] [-q] [-l binPath] [-f] [-s flashSlot] [-h]

- "-q":  
Query the number of grammars stored in flash
- "-l binPath":  
Load a grammar from a bin file to RAM  
Note: generate the bin file using "tw\_grammar\_converter.py"
- "-f":  
Save the grammar from RAM to flash
- "-s flashSlot":  
Swap grammars from flash
- "-h":  
Prints that help

Ex: hbi\_load\_grammar -d 0 -l my\_grammar.bin



## 5.3 hbi\_load\_firmware

### 5.3 hbi\_load\_firmware

This sample app demonstrates how to use the SDK functions to load a compatible ZL380xx firmware and configuration record into a ZL380xx Microsemi Vproc device.

It supports both Dynamic and Static image loading.

Firmware and Configuration Record Image can be compiled as static C-based (\*.h) array along with the app or can be loaded dynamically at run time.

For static compilation, user needs to un-comment or define the following two macros within the /apps/C/makefile at compile time.

LOAD\_FWR\_STATIC - Defined at Makefile. if defined, expects user to provide a C header file containing firmware boot image for static compilation

FWR\_C\_FILE - C header file containing firmware boot image for static compilation. if not provided, then hbi\_test would throw compile-time error

LOAD\_CFGREC\_STATIC - Defined at Makefile. if defined, expects user to provide C files containing configuration record

CFGREC\_C\_FILE C files to be included during compilation

. hbi\_load\_firmware

For dynamic bootimage loading to device (undef LOAD\_FWR\_STATIC), run

. hbi\_load\_firmware -d <device Id> -i <firmware filename>=""> -c <cfgrec filename>="">

where firmware filename = binary firmware image with .bin extension.

configuration record filename = configuration record file with .cr2 extension

For loading and saving to flash, use -s option

. hbi\_load\_firmware -d <device Id> -i <filename> -c <cfgrec filename>=""> -s , or

.

. For static loading use the command below

. hbi\_load\_firmware -d <device Id>

. or, if the firmware must be save to flash

. hbi\_load\_firmware -d <device Id> -s

**NOTE** - firmware binary image or c-output is generated by running Microsemi Provided Image convertor tool.

## Chapter 6

# Data Structure Documentation

### 6.1 cfgrec\_t Struct Reference

#### Data Fields

- void \***pImage**
- unsigned short **ImageLen**
- unsigned short **configBlockSize**

#### 6.1.1 Detailed Description

Definition at line 4 of file app\_util.h.

The documentation for this struct was generated from the following file:

- apps/app\_util.h

## Chapter 7

# File Documentation

### 7.1 apps/hbi\_test.c File Reference

```
#include <unistd.h>
#include <stdint.h>
#include <fcntl.h>
#include <stdio.h>
#include <stdlib.h>
#include <errno.h>
#include <string.h>
#include "typedefs.h"
#include "chip.h"
#include "hbi.h"
```

- **#define MAX\_RW\_SIZE** 64
- **char \* strtou** (char \*str, int len, unsigned char \*val)
- **int hbi\_test** (int argc, char \*\*argv)
- **void main** (int argc, char \*\*argv)

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