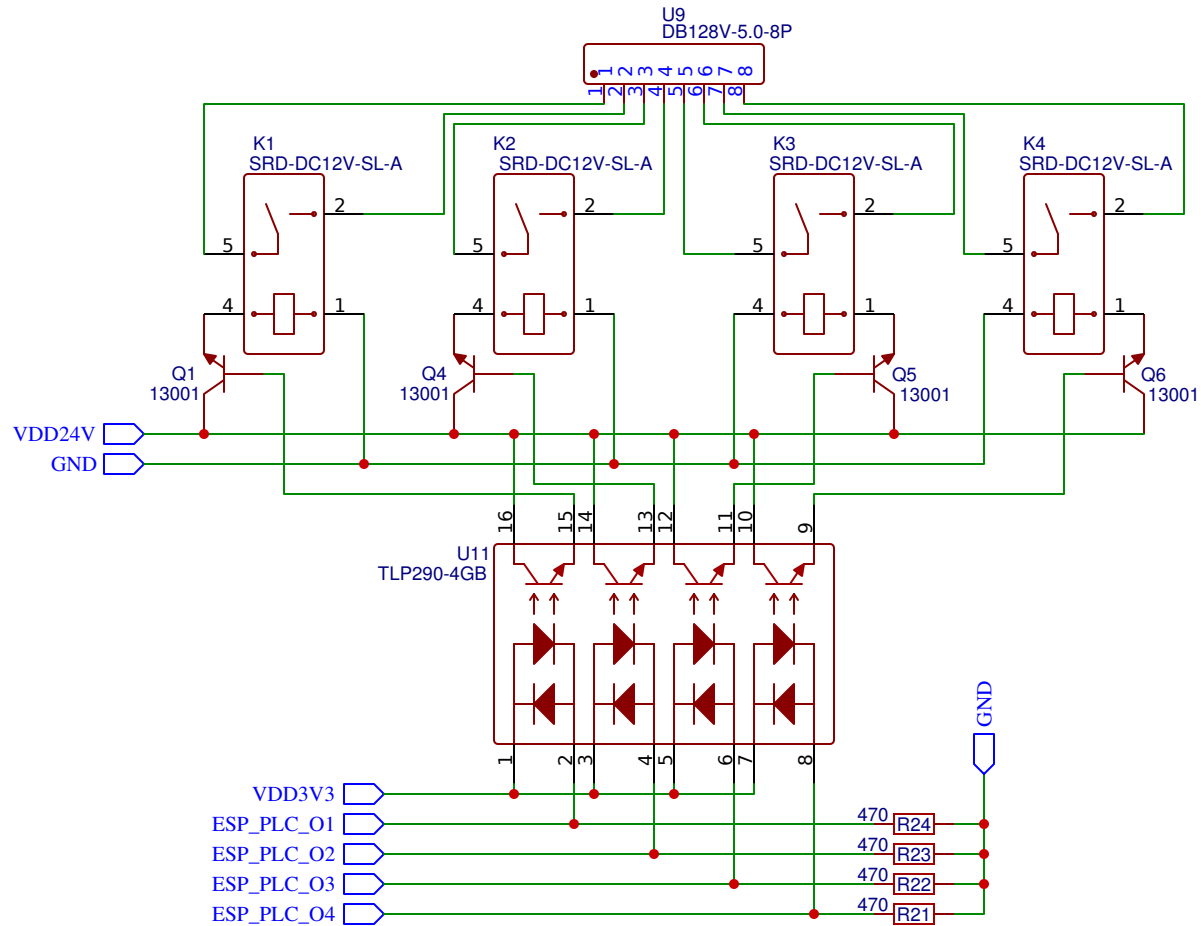
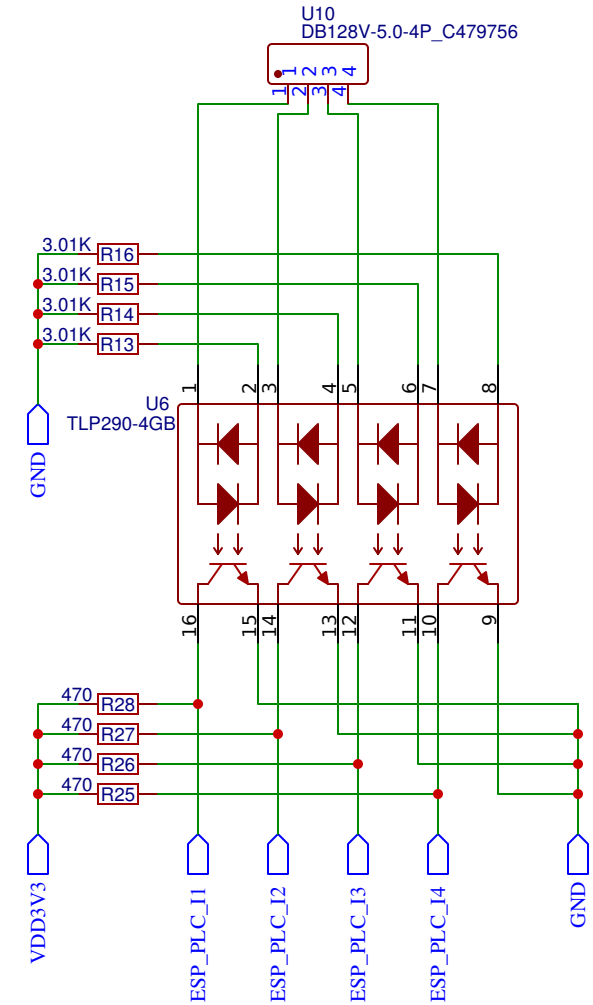


TITLE:		CV MODULE - LOGIC DIAGRAM	REV: 1.0
Company:		Universidad Fermín Toro	Sheet: 1/1
Date:		2020-07-17	Drawn By: Enmanuel D Becerra C

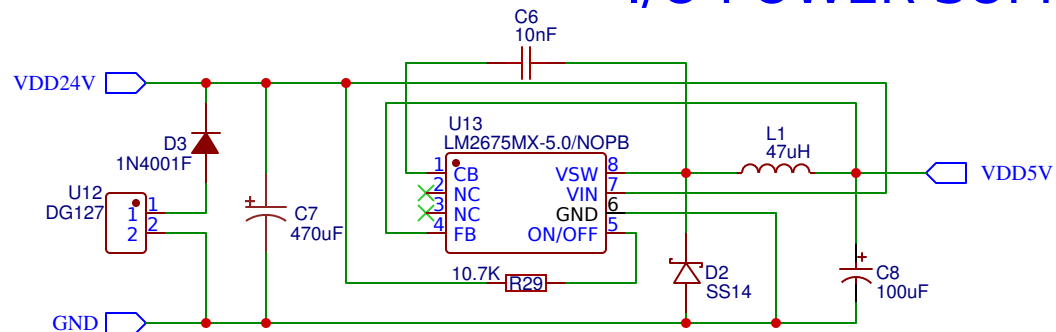
DIGITAL OUTPUT



DIGITAL INPUT



I/O POWER SUPPLY



TITLE: CV MODULE - I/O DIAGRAM		REV: 1.0
Company: Universidad Fermín Toro		Sheet: 1/1
Date: 2020-07-19	Drawn By: Enmanuel D Becerra C	