Team 11 “Squarinator” Mini-Project
Customer Sign-off Sheet

Specifications	Validations	Date of Validation
With an input voltage between 8.7V and 9.4V, the device shall meet all performance specifications and draw no more than 100mA.	Using a multimeter we will measure the 9V bus and verify the input voltage and current at both 10Hz and 1kHz.	Date: _____
The signal generator should be able to output a square wave signal with ranging frequencies between 10Hz-1kHz ±1% set by an adjustable potentiometer.	By connecting an oscilloscope at test-point “J11x2” at the output of the 555 timer, we will validate the required output as specified.	Date: _____
Inverting bias voltages shall be within working ranges of 4.8 - 5v , 3.1 - 3.3 v, and 1.6 - 1.8 v.	Using a digital multimeter at test points “J9”, “J7” and “J5”, we will measure and verify the inverting bias voltages working within their specified range.	Date: _____
Circuit shall generate three separate digital logic voltage levels at 1.8V, 3.3V and 5V ±2% at a desired frequency with 9V input.	Using an oscilloscope at test points “J1x1”, “J1x2” and “J1x3”, we will measure and verify the frequency and amplitude of each generated waveform at the output.	Date: _____

Table 3: Summary of Specifications and Validations along with Date of Validation

This project has successfully demonstrated and met all requirements for all specifications:

Signature: _____ Team: _____ Date: _____